North-Holland
Microprocessing and Microprogramming 25 (1989) 399–454



# AUTHOR INDEX TO VOLUMES 1–25 [1]

Abdelhay, S.A., M. Azharulhaque, DC-motor control using a minimum-variance self-tuner 19 (1987) 227–232
Abellard, P., B. Barbagelata, A data flow numerical processing operator 25 (1989) 133–138
Adamov, R., A naive approach to software structure validation 25 (1989) 361–365
Adamson, K., G. Donnan, N.D. Black, Simple transformation rules in the application of transputer to the physiological processing of speech 24 (1988) 397–402
Adler, H.D., G. Schülke, R. Schumacher, A microcomputer system for emission monitoring in nuclear power plants 6 (1980) 308–310
Adreoni, G., *see* Saletti, V. 9 (1982) 263–266
Agrawal, D.P., J. Mauney, L. Taylor Simpson, Structure of a parallelizing compiler for the B-HIVE multicomputer 24 (1988) 79–84
Agusa, K., *see* Gal, T. 8 (1981) 103–110
Ahlbom, G., A. Johansson, A microprogrammable system applied to multidimensional analysis of EEG in real time 9 (1982) 267–270
Ahmad, M., Implementable decimal arithmetic algorithms for micro/minicomputers 19 (1987) 119–128
Äijänen, T., Distributed interconnection of a reconfigurable multicomputer system 23 (1988) 243–246
Aillaud, J.P., *see* Etiemble, D. 6 (1980) 249–255
Ajmone Marsan, M., G. Carra, Bus and memory interference in double bus multiprocessor systems 13 (1984) 73–96
Ajmone Marsan, M., P. Camarda, F. Neri, Multichannel protocols for real-time microcomputer networks 18 (1986) 469–477
Al-Mouhamed, M., H.A. Almohammad, A multi-microprocessor system for the control of robot welders 23 (1988) 123–128
Al-Rabeh, A.H., M. Masud, Trends in the development of microcomputer hardware and software 17 (1986) 79–88
Alabau, A., *see* Figueras, J. 3(2) (1977) 34–38
Albert, B., A. Bode, W. Händler, A case study in vertical migration: The implementation of a dedicated associative instruction set 8 (1981) 257–262
Albert, M., *see* Mahler, H. 20 (1987) 179–182
Alderighi, M., L. Mantoani, G.R. Sechi, A DAISY architecture for abstract data type implementation in image processing 23 (1988) 95–102
Alderighi, M., A. Conni, L. Mantoani, G.R. Sechi, An automatic software generator in the design of reliable procedural programs 25 (1989) 367–373
Aleksić, T., *see* Jovanov, E. 23 (1988) 273–278
Aléonard, C., *see* Robach, C. 5 (1979) 31–37
Alexandridis, N., *see* Dimitriadis, B. 11 (1983) 227–232
Alexandridis, N., *see* Dimitriadis, B. 13 (1984) 267–274
Allerton, D.J., *see* Anido, M.L. 24 (1988) 581–588
Allison, A., Standard specification for proposed microcomputer system backplanes P896/D2 6 (1980) 363–368
Alló, G., G. Staszny, Two algorithms for image segmentation 19 (1987) 19–26
Alló, G., G. Staszny, L. Feró, Colour picture converter for image processing 19 (1987) 27–36
Alma, J.G.H., *see* Stevens, M.P.J. 1(5) (1975) 30–39

[1] Where applicable, the volume number is followed by the issue number in parentheses.

Almohammad, H.A., *see* Al-Mouhamed, M. 23 (1988) 123–128
Aloisio, G., V. Di Lecce, A VLSI 'Rest' processing element with improved bus connections (An evaluation) 22 (1988) 315–324
Ambrozy, G., J. Miskolczi, Development of microprogrammable microprocessor system by means of a PDP-8E minicomputer 4 (1978) 92–102
Anceau, F., Microprocesseurs et microprogrammations en France 1(2) (1975) 9
Anceau, F., Activities in microprocessors field in LSI manufacturers and universities in France 3(4) (1977) 10–18
Ancona, M., A. Clematis, G. Dodero, E.B. Fernandez, V. Gianuzzi, Using different language levels for implementing fault-tolerant programs 20 (1987) 33–38
Ancona, M., A. Clematis, L. De Floriani, E. Puppo, A hardware description language based on a hierarchical graph model 20 (1987) 183–188
Ancona, M., A. Clematis, G. Dodero, V. Filippone, V. Gianuzzi, Structuring a distributed program: The XMDS approach 21 (1987) 287–294
Ancona, M., A. Clematis, V. Gianuzzi, Two language levels for system programming 24 (1988) 57–62
Ancona, M., T. Bottino, A. Clematis, G. Dodero, V. Gianuzzi, L. Pareto, M. Pronzato, A. Repetti, LINDA: An allocator for embedded multiprocessor systems 24 (1988) 85–92
Andima, H.S., S. Arungu-Olende, The use of microcomputers for financial modelling of a public utility system in a developing country 19 (1987) 101–118
Andresen, H., R.T. Kölsch, Dynamic microprogramming on the Upper Siemens 7.000 Series 4 (1978) 322–325
Andriessen, J.H.M., Supermatrix, an interconnection network 16 (1985) 133–138
Andriessen, J.H.M., Polynomials on the Delft parallel processor 20 (1987) 107–112
Andriessen, J.H.M., Task scheduling programming system for the Delft parallel processor 23 (1988) 283–288
Anger, H., *see* Hahn, W. 24 (1988) 291–298
Anido, M.L., D.J. Allerton, E.J. Zaluska, The architecture of RIG: A RISC for image generation in a multimicroprocessor environment 24 (1988) 581–588
Anlauff, H., *see* Schecher, H. 9 (1982) 155–160
Annaratone, M., M. Borghesi, High speed, multi-word data processing by means of microprocessor based architecture 8 (1981) 15–22
Annaratone, M., A. Cagnola, M. Spalla, A proposal for a self-testing interface in multi-processor structures 9 (1982) 315–318
Annaratone, M., B. Bartolommei, M.G. Sami, A protocol for diagnostic communications in a fault tolerant network 11 (1983) 195–202
Annunziata, M., G. Cima, P. Mantica, G.R. Sechi, A daisy architecture for the multiprocessor real time data acquisition system of the Thor Tokamak experiment 17 (1986) 285–296
Annunziata, M., P. Guazzoni, L. Lisca, G.R. Sechi, L. Zetta, AMDAS: An advanced microprogrammed data acquisition system: A first evaluation prototype 18 (1986) 515–524
Ansorge, M., C. Piguet, E. Dijkstra, Design methodology for low-power full custom RISC microprocessors 18 (1986) 427–434
Antipa, V., *see* Krolak, S.T. 24 (1988) 241–248
Antola, A., I. Erenyi, N. Scarabottolo, Transient fault management in systems based on the AMD 2900 microprocessors 17 (1986) 205–218
Antola, A., R. Negrini, N. Scarabottolo, An approach to fault-tolerance in architectures for discrete fourier transforms 18 (1986) 275–288
Antola, A., L. Breveglieri, N. Scarabottolo, Designing and testing of a microprogrammed fault-tolerant CPU 20 (1987) 3–9
Antola, A., Evaluation of complexity for different layouts of butterfly networks 24 (1988) 353–360
Antonino, M., *see* Giulio, I. 23 (1988) 317–324

Antonov, A., *see* Fey, P. 15 (1985) 191–194
Antoy, S., F. Corano, F. Serio, T. Vernazza, GPCA: A general purpose cross-assembler 4 (1978) 222–226
Aramberri, J., *see* Ramos, I. 2(4) (1976) 65–71
Aranci, G.L., C.A. Binda, U. Clerici, E. Comignaghi, A fault tolerant oriented CMOS systolic implementation of an FIR filter 25 (1989) 97–105
Arato, P., J. Grantner, B. Lantis, An on-line computer-aided design method and developing system for microprogrammed systems specified by flow chart 3(1) (1977) 34–48
Arghiracopulos, S., A. Serra, A microcomputer-based automatic machine for reflector and headlight testing 6 (1980) 336–339
Arora, R.K., N.K. Sharma, On the design of a distributed operating system using a high level distributed programming language 10 (1982) 247–254
Arora, R.K., M.N. Gupta, S.P. Rana, Ring based termination detection algorithm for distributed computations 19 (1987) 219–226
Arora, R.K., M.N. Gupta, An algorithm for solving distributed termination problem 22 (1988) 263–272
Arungu-Olende, S., *see* Andima, H.S. 19 (1987) 101–118
Arvillias, A.C., D.G. Maritsas, C. Halatsis, Microcomputer implemented random and key stream generating mechanisms 6 (1980) 161–167
Aspinall, D., M.H. Barton, E.L. Dagless, Comparison of microprocessors: A graphical approach 3(2) (1977) 84–91
Assmuth, R., F. Irro, L. Reichl, H. Schaal, Input/output control of IBM System/370 Model 125 through dedicated input/output processors 2(3) (1976) 24–40
Aston, M.H., Videotex and its educational potential 6 (1980) 202–204
Astor, E., Component control from a high-level language 9 (1982) 85–92
Atkins, P., B.V. Smith, The optimal extraction of velocity information from a sonar correlation log 21 (1987) 161–170
Attisani, A., P.G. Barattieri, A real time processor for radar data handling 4 (1978) 148–154
Auer, A., P. Kemppainen, A. Okkonen, V. Seppänen, Automated code generation of embedded real-time systems 24 (1988) 51–56
Auguin, M., F. Boeri, J.P. Dalban, A. Vincent-Carrefour, Experience using a SIMD/SPMD multiprocessor architecture 21 (1987) 171–178
Aylward, A.J., *see* Cottis, R.A. 6 (1980) 92–95
Azaria, H., D. Tabak, Bit-sliced realization of a CMOVE architecture microcomputer 6 (1980) 373–380
Azaria, H., D. Tabak, Design considerations of a single instruction microcomputer: A case study 11 (1983) 187–194
Azaria, H., D. Tabak, The MODHEL microcomputer for RISCS study 12 (1983) 199–206
Azaria, H., Preliminary analysis of RISC architecture performance 14 (1984) 133–138
Azaria, H., *see* Shallom, I. 16 (1985) 113–120
Azaria, H., Y. Veler, Functional language directed data driven machine 16 (1985) 127–132
Azaria, H., Y. Veler, Machine instructions of a data-driven architecture: Design considerations 20 (1987) 99–105
Azharulhaque, M., *see* Abdelhay, S.A. 19 (1987) 227–232

Bacci, A., *see* Scarabottolo, N. 9 (1982) 309–314
Bae, P., *see* Bina, M. 19 (1987) 327–334
Bagchi, K.K., *see* Das, P.K. 12 (1983) 237–244
Bagchi, K.K., P.K. Das, B.B. Bhaumik, A hierarchical design methodology for multiple microprocessor systems 16 (1985) 277–286
Baitinger, U.G., *see* Mahler, H. 20 (1987) 179–182
Baitinger, U.G., *see* Mahler, H. 23 (1988) 33–36
Bakowski, P., A. Pawlak, LIDO: A silicon compiler preprocessor 20 (1987) 167–171

Balakrishnan, M., B.B. Madan, P.C.P. Bhatt, A survey of microprogramming languages 17 (1986) 19–28
Balakrishnan, M., P.C.P. Bhatt, B.B. Madan, An efficient retargetable microprogram generating system 19 (1987) 305–318
Balakrishnan, M., A.K. Majumdar, D.K. Banerji, J.G. Linders, Synthesis of decentralised controllers from high level description 22 (1988) 217–230
Baleanu, C., *see* Tomescu, D. 7 (1981) 344–350
Baleanu, C., *see* Tomescu, D. 10 (1982) 333–340
Balfour, J., *see* Hutchison, D. 13 (1984) 97–104
Ballieu, G., J. Lewi, Y.D. Willems, A microprogramming language at register transfer level 8 (1981) 179–188
Banaszek, D., *see* Sapiecha, K. 16 (1985) 217–220
Banerji, D.K., J. Raymond, A cross-assembler and simulator for the 'skinny' microcomputer 1(5) (1975) 25–29
Banerji, D.K., *see* Balakrishnan, M. 22 (1988) 217–230
Barattieri, P.G., *see* Attisani, A. 4 (1978) 148–154
Barbagelata, B., *see* Abellard, P. 25 (1989) 133–138
Barbosa, V.C., F.M.G. França, Specification of a communication virtual processor for parallel processing systems 24 (1988) 511–518
Barbosa, V.C., *see* Fernandes, E.S.T. 25 (1989) 229–232
Barker, P.G., Computer control of a random access slide projector 10 (1982) 261–272
Barker, P.G., Programming a video disc 15 (1985) 263–276
Bartkowski, T., *see* Kozlowski, W.E. 21 (1987) 497–504
Bartolommei, B., A. Bovo, M. Springolo, Microprogramming techniques in front-end-processors of the PROTEO digital switching system 10 (1982) 311–324
Bartolommei, B., *see* Annaratone, M. 11 (1983) 195–202
Barton, M.H., *see* Aspinall, D. 3(2) (1977) 84–91
Barzilai, Z., Z. Segall, E. Strasbourger, Improvements for dedicated microcomputer based systems 6 (1980) 149–157
Baud, M., A program loader using a cassette magnetic tape 5 (1979) 280–284
Bautista, A., J.F. Tirado, J. Ruz, R. Hermida, An architectural design for simultaneous microdiagnostic 9 (1982) 27–38
Bautista, A., *see* Hermida, R. 12 (1983) 99–110
Beaufils, R., J.P. Cabanel, J.P. Lagasse, Ph. Puech, Structure of a packet-switching computer 1(4) (1975) 19–25
Beaufils, R., J.P. Sansonnet, An efficient hardware tool for structured information management 4 (1978) 275–282
Becker, B., H. Soukup, CMOS stuck-open self-test for an optimal-time VLSI-multiplier 20 (1987) 153–157
Bedi, J., *see* Chaudhry, G.M. 18 (1986) 205–212
Bedina, M., *see* Scarabottolo, N. 9 (1982) 309–314
Beer, M.D., *see* Tsai, S.R. 19 (1987) 375–384
Beer, M.D., J.D.T. Martin, The provision of flexibility in hierarchical control systems 21 (1987) 295–300
Be'ery, Y., *see* Pardo, O. 25 (1989) 79–83
Behbahani, A.M., F.J. Hill, M.R. Patel, Intelligence driven test sequence generator for VLSI design 18 (1986) 355–362
Behr, P., W.K. Giloi, R. Gueth, Education in firmware engineering and microprogramming 8 (1981) 153–166
Beister, J., *see* Ditzinger, A. 9 (1982) 161–174
Belcher, I.T., *see* Winsborrow, R.P.J. 3(3) (1977) 3–8
Belomorski, P., Pseudorandom self-testing of microprocessors 19 (1987) 37–48
Bemmerl, T., Realtime high level debugging in host/target environments 18 (1986) 387–400
Bemmerl, T., G. Schröder, A portable realtime multitasking kernel for embedded microcomputer systems 21 (1987) 181–188

Bemmerl, T., N. Erl, O. Hansen, Menu and graphic driven human interfaces for high level debuggers 24 (1988) 153–160
Bemmerl, T., T. Bonk, Optimizing cache parameters using CAE workstations 24 (1988) 541–546
Ben-Yaakov, S., H. Guterman, On line optimization of an arbitrary process by a personal microcomputer system: Application in algae ponds 16 (1985) 319–324
Bender, H., P. Hartlmueller, H. Rzehak, A local area network with dynamic switch-over between a CSMA/CD and a token-passing access protocol 18 (1986) 153–159
Benes, E., *see* Harms, K. 1(5) (1975) 40–45
Berbers, Y., P. Verbaeten, Design of the HERMIX distributed operating system: Structural aspects 24 (1988) 187–194
Berce, J., Event-driven real time operating system in intelligent network communicator 16 (1985) 249–254
Berce, J., Graphics fundamentals for a PCB-CAD PC system 23 (1988) 359–364
Berg, H.K., A model of timing characteristics in computer control 5 (1979) 206–218
Bermbach, R., H. Geisse, Ultrasonics make microrobot see 14 (1984) 177–180
Bernárdez, J., *see* Mandado, E. 14 (1984) 211–214
Bernárdez, J., *see* Mandado, E. 20 (1987) 203–208
Berndt, H., Higher-level microprogramming based on a formal description of system behavior 1(3) (1975) 17–21
Berndt, H., Computer architecture symposium '76: A conference report 2(2) (1976) 68–69
Berndt, H., Input/output microprogramming: An overview 2(3) (1976) 3–8
Berndt, H., A. Hassitt, H.W. Lawson Jr, G. Musstopf, Special applications firmware: A panel discussion (part I) 4 (1978) 293–296
Berndt, H., A. Hassitt, H.W. Lawson Jr, G. Musstopf, Special applications firmware: A panel discussion (part II) 4 (1978) 338–339
Berndt, H., Software support in hardware 13 (1984) 1–10
Berndt, H., Microprocessors in switching system control 15 (1985) 113–118
Berry, R., *see* Purvis, A. 25 (1989) 271–276
Bertora, F., C. De Cena, M. Di Manzo, C. Micheli, Multiprocessor architectures for control applications 7 (1981) 169–176
Berzieri, M., *see* Piuri, V. 20 (1987) 15–23
Beunder, M., *see* Spaanenburg, L. 16 (1985) 83–88
Beune, F.A., *see* Spaanenburg, L. 16 (1985) 83–88
Bhatt, P.C.P., *see* Balakrishnan, M. 17 (1986) 19–28
Bhatt, P.C.P., *see* Balakrishnan, M. 19 (1987) 305–318
Bhaumik, B.B., *see* Das, P.K. 12 (1983) 237–244
Bhaumik, B.B., *see* Bagchi, K.K. 16 (1985) 277–286
Bhavsar, V.C., *see* Kamath, C. 10 (1982) 25–32
Biehl, G., A. Ditzinger, Computer-aided design of microprocessor-based digital controllers 7 (1981) 326–333
Billingsley, J., A.A. Collie, T.C. Dadd, Cooker programmers with embedded microcomputers 21 (1987) 413–416
Billingsley, J., *see* Naghdy, F. 21 (1987) 603–612
Billingsley, J., *see* Luk, B.L. 24 (1988) 419–424
Bilton, J.M., A survey of self-test and BITE program generation 6 (1980) 168–174
Bina, M., P. Bae, P. Gaillard, Pattern recognition machine: Its conception and creation 19 (1987) 327–334
Binaghi, E., L. Lisca, A. Proserpio, G.R. Sechi, An abstract description generator for the reliability analysis in the design of real time systems 23 (1988) 157–166
Binaghi, E., G.R. Sechi, R. Vaccaro, L. Verdoscia, HCRC-Parallel Computer: A massively parallel combined architecture supercomputer 25 (1989) 287–292
Binda, C.A., *see* Aranci, G.L. 25 (1989) 97–105

Bisaschi, A., *see* Piuri, V. 20 (1987) 15–23
Bishop, P.G., The design of software for distributed control systems 5 (1979) 358–362
Bisiani, R., L. Mezzalira, Some considerations on the standard bus for microcomputer proposed by J.D. Nicoud 2(2) (1976) 11–13
Bisio, G.M., *see* Curatelli, F. 24 (1988) 213–218
Bisio, G.M., *see* Di Zitti, E. 25 (1989) 259–263
Bissett, A., J. Forrest, An external data structure tool for Pascal 25 (1989) 387–390
Black, N.D., *see* Adamson, K. 24 (1988) 397–402
Blattner, J., Emulation by microprogramming of the PDP 11 instruction set on Varian V73. Part 1: A modular conception 3(3) (1977) 37–41
Blattner, J., Emulation by microprogramming of the PDP 11 instruction set on Varian V73. Part 2. Partial implementation of the modular concept 3(3) (1977) 42–47
Blauensteiner, A., METASM: A general assembler 14 (1984) 201–204
Blažek, Z., A microcomputer controlled corn silo 9 (1982) 271–276
Blažek, Z., V. Jánes, A microcomputer controlled diesel engine 14 (1984) 187–190
Blažek, Z., V. Jánes, Microcomputer hardware education at a Czechoslovakian technical university 17 (1986) 77–78
Blažek, Z., P. Kroha, Design of a reconfigurable parallel RISC machine 21 (1987) 39–46
Blödel, J., R.W. Hartenstein, W. Nebel, M. Ryba, A technology description method for generalized layout/circuit relations 23 (1988) 15–20
Bode, A., *see* Albert, B. 8 (1981) 257–262
Boeri, F., *see* Auguin, M. 21 (1987) 171–178
Bohr, H., Supercomputer accelerator card for PC 25 (1989) 51–54
Bojun, V.P., *see* Malinovsky, B.N. 3(1) (1977) 67–72
Bolch, G., F. Hofmann, B. Hoppe, H.J. Kolb, C.U. Linster, R. Polzer, W. Schüssler, G. Wackersreuther, F.X. Wurm, A multiprocessor system for simulating data transmission systems (MUPS) 12 (1983) 267–278
Bondarenko, V., *see* Koushtuev, A. 22 (1988) 151–156
Bonev, S., *see* Yankov, B. 16 (1985) 221–226
Bonk, T., *see* Bemmerl, T. 24 (1988) 541–546
Bonomo, A., G. Bussolino, G. Girardi, M. Italiano, From structural RT description to floor plan 21 (1987) 569–576
Bonzio, F.C., M.G. Sami, G. Storti Gajani, Fault-tolerant solutions for complex-numbers multipliers 21 (1987) 91–98
Borghero, G., *see* Curatelli, F. 24 (1988) 213–218
Borghesi, M., *see* Annaratone, M. 8 (1981) 15–22
Borsotti, A., A. Fumagalli, M. Ciccotti, M. Papa, MINDS: A microprocessor integrated development system for applications in SPC exchanges 9 (1982) 77–84
Botella, D., *see* Sansonnet, J.P. 9 (1982) 143–154
Botta, A., *see* Tomescu, D. 7 (1981) 344–350
Bottino, T., *see* Ancona, M. 24 (1988) 85–92
Boudillet, O., S.C. Winter, D.R. Wilson, The implementation of a functional machine on a transputer network 24 (1988) 389–396
Bourbakis, N.G., *see* Dimitriadis, B. 11 (1983) 227–232
Bourbakis, N.G., *see* Dimitriadis, B. 13 (1984) 267–274
Bourbakis, N.G., *see* Panogiotopoulos, D. 14 (1984) 125–132
Bourbakis, N.G., A quadtree multimicroprocessor architecture for robot vision systems 16 (1985) 267–272
Bourbakis, N.G., A real time hierarchical multimicroprocessor image reduction system 16 (1985) 287–294
Bourbakis, N.G., ANAGNOSTIS: An automatic text reading system 23 (1988) 103–114
Bourbakis, N.G., Won Jang, An efficient, parallel-symmetric thinning algorithm and its hardware implementation 23 (1988) 115–122

Bourbakis, N.G., I.N. Savvides, Geometric transformations for optimum VLSI layout placement 25 (1989) 163–170
Bourbakis, N.G., M.P. Papazoglou, S.H. Nguyen, Design and simulation using Petri nets of a parallel WHT multiprocessor system 25 (1989) 341–346
Bousse, L., *see* Nyssen, M. 5 (1979) 243–248
Boute, R.T., The binary decision machine as a programmable controller 2(1) (1976) 16–22
Boute, R.T., Microcomputer education in an industrial environment 3(2) (1977) 12–15
Bovo, A., *see* Bartolommei, B. 10 (1982) 311–324
Bowen, J.P., Formal specification and documentation of microprocessor instruction sets 21 (1987) 223–230
Bramley, R.G., D.J. Creasey, A real-time image compressor using a modular signal-processing system employing Occam and the transputer 21 (1987) 49–56
Brandolini, A., E. Carminati, A DC energy meter based on a microprocessor: Design and testing 5 (1979) 239–242
Brandolini, A., E. Carminati, A new family of electrical measuring instruments based on microprocessors 5 (1979) 302–308
Brandolini, A., E. Carminati, A thermal energy meter using a microcomputer 6 (1980) 340–342
Brause, R., *see* Dal Cin, M. 20 (1987) 301–308
Breimer, A.J., *see* Houtekamer, G.E. 18 (1986) 491–497
Breveglieri, L., *see* Antola, A. 20 (1987) 3–9
Breveglieri, L., Design and implementation of a VLSI serial multiplier for fixed point numbers with self-checking capability 24 (1988) 637–646
Brezány, P., Parallel parsing in a multiprocessor environment 24 (1988) 819–826
Brie, C., *see* Dewitte, J. 5 (1979) 93–99
Brioschi, A., P. Scaini, The microprograms correctness 2(2) (1976) 21–37
Broekaert, A., *see* Nyssen, M. 5 (1979) 243–248
Broekema, A., *see* Spaanenburg, L. 18 (1986) 341–346
Brofferio, S., G. Scirè, Proposal for a normalized microcomputer system 1(4) (1975) 13–18
Brofferio, S., G. Scirè, Electronic distributed switching systems (EDDS): A distributed structure for the coordinating processor 6 (1980) 115–119
Brofferio, S., Two case studies used in teaching controller design in electronic systems 7 (1981) 160–168
Brofferio, S., G. Scirè, Physical communication protocols (PCP) for digital systems 8 (1981) 93–102
Brofferio, S., A modular architecture for adaptive predictive coders of digital television signals 15 (1985) 141–152
Brofferio, S., M. Piacentini, S. Tubaro, An audio computer interface: A case study of structured electronic equipment design 17 (1986) 139–152
Brown, B.M., *see* Clarke, A.T. 24 (1988) 549–554
Bruck, K., A.S. Kirilov, D. Krushinsky, V.I. Prikhodko, K. Voigt, A personal computer based graphic workstation 23 (1988) 357–358
Bruck, K., M. Griese, Ch. Jatho, A.S. Kirilov, V.I. Prikhodko, Some computer graphics algorithms and their hardware realization 25 (1989) 61–64
Bruggeman, F., *see* Frietman, E.E.E. 14 (1984) 173–176
Bruggeman, F., *see* Frietman, E.E.E. 21 (1987) 313–318
Bruinshorst, B.T.J., A.H.J. Schatorjé, Modest: A novel development system for an industrial microcomputer system 6 (1980) 41–43
Brusamolino, M., L. Reina, M. Spalla, An example of microprocessor's application in minicomputer systems: A copy volume design and implementation 13 (1984) 331–340
Bruschi, O., R. Negrini, S. Ravaglia, Systolic arrays for serial signal processing 20 (1987) 133–140

Brzeziński, J., W. Cellary, J. Kreglewski, A family of compatible single- and multi-microprocessor systems with 8-bit and 16-bit microprocessors 14 (1984) 195–200
Buck-Emden, R., *see* Langendoerfer, H. 18 (1986) 505–512
Buehrer, R.E., Emulation of a parallel codeblock dataflow processor 21 (1987) 319–324
Bulckaert, P., *see* Vermeesch, J. 21 (1987) 129–136
Burd, N.C., The development and performance of a CSMA/CD network for low level distributed control 20 (1987) 69–72
Burkhardt, K.J., *see* Hess, W.E. 7 (1981) 97–103
Bürki, M., *see* Hersch, R.D. 24 (1988) 503–510
Burns, A., *see* Morrison, I.W. 18 (1986) 233–240
Buschke, R., *see* Grass, W. 20 (1987) 161–165
Buschke, R., K. Lagemann, Computer aided evaluation of simulation results, the SIMUEVA program package 21 (1987) 471–478
Buschke, R., K. Lagemann, Error retracing and fault localization techniques within SIMUEVA, a system for evaluation of simulation results 24 (1988) 461–468
Bushell, R.G., Higher level language for microprogramming 4 (1978) 67–75
Bussolino, G., *see* Bonomo, A. 21 (1987) 569–576

Cabanel, J.P., *see* Beaufils, R. 1(4) (1975) 19–25
Cabodi, G., P. Camurati, P. Prinetto, M. Sonza Reorda, C_TPDL*: Adapting TPDL* to concurrent simulation evironments 18 (1986) 39–45
Cabodi, G., P. Camurati, P. Prinetto, S. Reorda, An extension to base Conlan in the temporal domain 18 (1986) 559–566
Cagnola, A., *see* Annaratone, M. 9 (1982) 315–318
Cagnola, A., M. Corti, G. Vignati, Laplace: Another second generation PLA design tool 16 (1985) 61–66
Cajani, V., Architectural issues in designing a UNIX multiprocessor system 20 (1987) 79–84
Čajavec, R., *see* Marić, S. 23 (1988) 203–206
Camarda, P., *see* Ajmone Marsan, M. 18 (1986) 469–477
Camurati, P., *see* Cabodi, G. 18 (1986) 39–45
Camurati, P., *see* Cabodi, G. 18 (1986) 559–566
Cantone, G., A. Cimitile, P. Maresca, A new methodological proposal for program maintenance 18 (1986) 319–331
Cantone, G., A. Cimitile, U. De Carlini, Testability and path testing strategies 21 (1987) 371–382
Cantone, G., B. Ciciani, Inserting state restoration requests in systems of distributed processes 21 (1987) 481–488
Capocaccia, F., Automation and microcomputers 5 (1979) 341–343
Carballo, P.P., *see* Nuñez, A. 25 (1989) 127–132
Carlo, S., *see* Giulio, I. 23 (1988) 317–324
Carlstedt, G., H.W. Lawson Jr, L.E. Thorelli, Some microprogramming and microprocessor activities in Sweden 4 (1978) 239–242
Carlstedt, G., *see* Suk, D. 13 (1984) 153–170
Carlstedt, G., A language for behavior, structure and geometry 18 (1986) 567–580
Carminati, E., *see* Brandolini, A. 5 (1979) 239–242
Carminati, E., *see* Brandolini, A. 5 (1979) 302–308
Carminati, E., *see* Brandolini, A. 6 (1980) 340–342
Carminati, E., A. Gandelli, A parallel processor for the dynamic characterization of A/D converters 25 (1989) 335–339
Carpenter, G.F., D.J. Holding, A.M. Tyrrell, The design and simulation of software fault tolerant mechanisms for application in distributed processing systems 22 (1988) 175–186

Carpenter, G.F., State space modelling in the design of robust software for distributed systms: A case study 24 (1988) 793–800
Carpenter, G.F., *see* Holding, D.J. 24 (1988) 801–806
Carra, G., *see* Ajmone Marsan, M. 13 (1984) 73–96
Carracedo, J., M.A. Nuñez, E. Pastor, Microsimplex: A didactic microcomputer specially designed for computer education 12 (1983) 217–222
Casaglia, G.F., Distributed computing systems: A biased review 2(4) (1976) 5–18
Casero, E., D. Paleari, R. Polillo, G.P. Rossi, Interfacing a Honeywell level 62 host computer to an E.I.N. communications subnetwork with a microcomputer system 4 (1978) 283–292
Castellani, V., D. del Corso, M. Giordana, Communication interface for high-speed synchronous telephone line connection of a master computer with several interactive video terminals 2(1) (1976) 23–29
Castellari, G., *see* Daprà, A. 6 (1980) 391–394
Castillo, A., M.A. Díaz, P.L. Chas, F. Mínguez, Microprocessor controlled data acquisition system for radio propagation measurements 12 (1983) 207–210
Cattaneo, G., V. Piuri, N. Scarabottolo, F. Urero, Chill concurrency on Intel iAPX 432 architecture 15 (1985) 233–252
Cavalcoli, A., L.Mezzalira, Personal computers in Italy: An overview 6 (1980) 87–89
Cea, J., *see* León, G. 21 (1987) 541–548
Cegrell, T., *see* Sandberg, U. 24 (1988) 829–834
Cellary, W., *see* Brzeziński, J. 14 (1984) 195–200
Cerrada, J.A., M. Collado, Implementation of a CSP-based extension of Pascal 22 (1988) 233–242
Cesarini, F., G. Soda, G. Zappa, A multiprocessor architecture and its application to adaptive control 22 (1988) 301–310
Chalmers, A., OCCAM: The language for educating future parallel programmes? 24 (1988) 757–760
Chang, Chorng Hwa, *see* Das Gupta, S. 18 (1986) 417–424
Chao, Yen, Multiple-microprocessor/microcomputer performance: What to acquire and how to evaluate: A status report 17 (1986) 267–276
Chao, Yen, Xu Jia Dong, Gong Wei Ting, The TP microcomputers in China: How little workhorses from a small laboratory become widely used in a huge country 20 (1987) 227–232
Charlton, C.C., D. Elliott, P.H. Leng, An interactive software system for microcode development 13 (1984) 105–114
Charlton, C.C., P.H. Leng, D.M. Wilkinson, A microprogram Meta-disassembler 22 (1988) 255–262
Chas, P.L., *see* Castillo, A. 12 (1983) 207–210
Chaudhry, G.M., J. Bedi, Cache interleaving in multiprocessor systems 18 (1986) 205–212
Chaudouard, C., J.P. Elloy, A real-time monitor and its representation by Petri nets 7 (1981) 241–248
Cheminaud, M., *see* Girault, C. 2(4) (1976) 19–28
Chen, C.N., *see* Tsai, S.R. 22 (1988) 81–96
Chernykh, E.V., L.G. Efimov, I.F. Kolpakov, V.A. Smirnov, VMEbus in relativistic nuclei research and control tasks 20 (1987) 65–67
Cheung, Wing-Hong, *see* Moon, Yiu-Sang 15 (1985) 85–94
Chientaroli, M., G. Gatti, A system for real-time analysis and compact viewing of low-frequency quasi-periodic signals 16 (1985) 305–312
Chlamtac, I., A concurrent network simulator for automated protocol development and performance evaluation 18 (1986) 145–152
Chotukov, V., Some practical aspects in intelligent sensors' design 14 (1984) 191–194
Chowdhury, A., *see* Oberreiter, W. 5 (1979) 295–301
Christensen, K.S., A Pascal-like portable, interactive development system for small microcontrollers 18 (1986) 55–58

Christodoulakis, D.N., B. Soupos, C. Zaroliagis, The implementation of a software engineering database using desk-size computing resources 21 (1987) 383–390
Christodoulakis, D.N., Petri net semantics of Smalltalk-80 24 (1988) 267–272
Chroust, G., Microprogramming and microcontrol: A hardware definition 1(1) (1974) 21–27
Chroust, G., Microprogramming in Austria 1(2) (1975) 5
Chroust, G., *see* Schweinzer, H. 2(2) (1976) 66–67
Chroust, G., Microprogramming: An interface property 2(4) (1976) 48–53
Chroust, G., *see* Schauer, H. 3(4) (1977) 5–9
Chroust, G., J. Mühlbacher, Rivalling multiprocessor organization: A novel approach to performance increases 5 (1979) 65–72
Chroust, G., *see* Schauer, H. 5 (1979) 113–117
Chroust, G., Orthogonal extensions in microprogrammed multiprocessor systems: A chance for increased firmware usage 6 (1980) 104–110
Chroust, G., A. Kreuzer, K. Stadler, A microprogrammed page fault monitor 8 (1981) 247–256
Chu, Y., Report on the Dise workshop on microprocessor and education 3(2) (1977) 8–11
Chung, D.H., The secret of American services in EDP 6 (1980) 9–10
Churin, I.N., A. Georgiev, A CAMAC crate controller for the IBM PC/XT family computers with built-in selftest features 23 (1988) 153–154
Ciborra, C., G. Gasbarri, P. Maggiolini, Microinformatics and work organization: A case study 4 (1978) 232–238
Ciccarella, G., M. Faccio, P. Marietti, C. Padovani, A microcomputer LAN for process control and its VLSI access unit 22 (1988) 353–366
Ciccotti, M., *see* Borsotti, A. 9 (1982) 77–84
Ciciani, B., *see* Velardi, P. 11 (1983) 287–294
Ciciani, B., G. Di Vito, M. Rosati, An efficient strategy for the detection of faults and a domino-free recovery in distributed systems 14 (1984) 29–34
Ciciani, B., *see* Cantone, G. 21 (1987) 481–488
Cima, G., *see* Annunziata, M. 17 (1986) 285–296
Ciminiera, L., *see* Demartini, C. 16 (1985) 231–238
Ciminiera, L., C. Demartini, E. Nicola, R. Samarotto, A. Valenzano, Design and implementation of an ISO/OSI session layer for a microprocessor-based LAN 20 (1987) 59–64
Ciminiera, L., *see* Valenzano, A. 25 (1989) 265–270
Cimitile, A., *see* Cantone, G. 18 (1986) 319–331
Cimitile, A., *see* Cantone, G. 21 (1987) 371–382
Cioffi, G., P. Velardi, A fully distributed arbiter for multiprocessor systems 11 (1983) 15–22
Cioffi, G., P. Libri, A. Micarelli, A gate level simulator for MS-DOS personal computers 18 (1986) 47–51
Civera, P.L., *see* Del Corso, D. 19 (1987) 153–166
Civera, P.L., G.L. Piccinini, M. Zamboni, Design considerations on a VLSI Prolog interpreter 21 (1987) 267–274
Clarke, A.T., B.M. Brown, M.P. Thorne, Using a micro to automate data acquisition in music publishing 24 (1988) 549–554
Clarke, R.T., *see* Nichols, S.J. 25 (1989) 327–331
Clausen, K., IMP 2000, an industrial microprocessor 6 (1980) 24–27
Clematis, A., *see* Ancona, M. 20 (1987) 33–38
Clematis, A., *see* Ancona, M. 20 (1987) 183–188
Clematis, A., *see* Ancona, M. 21 (1987) 287–294
Clematis, A., *see* Ancona, M. 24 (1988) 57–62
Clematis, A., *see* Ancona, M. 24 (1988) 85–92

Clemen, R., E. Eisenbraun, W. Fischer, W. Hang, K. Helwig, W. Loehlein, H. Lindner, M. Tong, Fast and dense embedded arrays for microprocessors in CMOS-technology 18 (1986) 441–444
Clerici, U., *see* Aranci, G.L. 25 (1989) 97–105
Cochet-Muchy, A., J.P. Thomesse, Compiling distributed applications 7 (1981) 13–19
Coey, W.A., D.Q.M. Fay, Character generation experiment for a computer logic design course 9 (1982) 93–98
Cohen, E.J., R.A. Marko, J. Levy, Integrating an on-chip MMU into a highly pipelined architecture 24 (1988) 709–714
Colard, D., Development aid programs for bit-slice based systems 7 (1981) 58–65
Coleman, M.G., Microcomputer systems: Revolution or revolt? 6 (1980) 96–97
Coleman, M.G., Educational software and the primary school teacher 12 (1983) 223–226
Coleman, M.G., S. Pratt, Dimensional design: A software engineering tool for the design of concurrent systems 23 (1988) 305–310
Collado, M., *see* Cerrada, J.A. 22 (1988) 233–242
Collie, A.A., *see* Billingsley, J. 21 (1987) 413–416
Collier, M., A software training environment for electronic engineers using UNIX, Modula-2 and a local area network 24 (1988) 761–766
Collis, G.V., M.D. Edwards, Automatic hardware synthesis from a behavioural description language: OCCAM 18 (1986) 243–250
Comerford, P.J., B.M. Eaglestone, Bradford musical instrument simulator and workstation 24 (1988) 555–562
Comfort, J.C., *see* Tal, D. 25 (1989) 91–95
Comignaghi, E., *see* Aranci, G.L. 25 (1989) 97–105
Comte, J.P., *see* Parchoux, R. 3(2) (1977) 31–33
Conni, A., *see* Alderighi, M. 25 (1989) 367–373
Constantinescu, C., Designing multibus priority resolver by means of a field programmable logic sequencer 13 (1984) 325–330
Conte, G., D. del Corso, M. Giordana, A proposal for $\mu$P prototyping systems 2(2) (1976) 7–10
Conte, G., D. Del Corso, M. Giordana, Microprocessors in a university electronic laboratory 3(2) (1977) 63–65
Conway, L., The MPC adventures: Experiences with the generation of VLSI design and implementation methodologies 10 (1982) 209–228
Cook, J., J. Gilbert, Parallel neural network simulation using sparse matrix techniques 24 (1988) 621–626
Cooper, R.E.M., Emulation on the DG Eclipse S/130 6 (1980) 179–183
Cooper, R.E.M., On using micro-coda to enhance compiler portability 13 (1984) 249–254
Corano, F., *see* Antoy, S. 4 (1978) 222–226
Corbett, P.F., *see* Dasiewicz, P. 18 (1986) 591–595
Corbett, W.A., *see* Taylor, M.J. 16 (1985) 313–318
Corcoran, P., The Suma microprogramming system 7 (1981) 37–45
Cordes, R., *see* Langendoerfer, H. 18 (1986) 505–512
Cornelis, J., *see* Taeymans, J. 3(3) (1977) 14–21
Cornett, D., *see* Papachristou, C.A. 16 (1985) 73–82
Cornford, T., B. Hayes, Designing a computer workstation for researchers in the quantitative social sciences 17 (1986) 195–204
Corradi, A., A. Natali, Using the iAPX-432 system as a support for Chill parallel constructs 12 (1983) 159–166
Cortadella, J., *see* González, A. 24 (1988) 565–572
Cortadella, J., T. Jové, Designing a branch target buffer for executing branches with zero time cost in a RISC processor 24 (1988) 573–580
Corti, M., *see* Cagnola, A. 16 (1985) 61–66
Cottis, R.A., A.J. Aylward, P. Crowe, The E78 bus 6 (1980) 92–95
Coughlan, J.R., *see* Taylor, M.J. 16 (1985) 313–318

Coulouris, G., *see* Macres, P. 1(1) (1974) 45–52
Couprie, M., *see* Gonzalez-Rubio, R. 24 (1988) 599–606
Courtois, B., M. Marinescu, J.F. Pons, Skalp: Skeleton architecture for fault-tolerant distributed processing 7 (1981) 312–325
Cox, G.C., *see* Winsborrow, R.P.J. 3(3) (1977) 3–8
Creasey, D.J., *see* Bramley, R.G. 21 (1987) 49–56
Cremonini, R., E. Lamma, P. Mello, Optimization techniques in building expert systems 21 (1987) 531–538
Crespi Reghizzi, S., *see* Daprà, A. 6 (1980) 391–394
Crespi Reghizzi, S., *see* Ferrari, R. 20 (1987) 91–97
Crookes, D., P.J. Morrow, P. Milligan, N.S. Scott, P.L. Kilpatrick, Notes on implementing a language for transputer networks 21 (1987) 559–566
Crookes, D., *see* Milligan, P. 23 (1988) 253–258
Crookes, D., *see* Scott, N.S. 24 (1988) 535–540
Crookes, D., *see* Scott, N.S. 25 (1989) 247–251
Crowe, P., *see* Cottis, R.A. 6 (1980) 92–95
Csopaki, G., Hardware description language for specification of digital systems 25 (1989) 151–155
Curatelli, F., G.M. Bisio, G.Borghero, E. Di Zitti, A behavioural simulator and its use for the validation of hypercube architectures 24 (1988) 213–218
Curatelli, F., *see* Di Zitti, E. 25 (1989) 259–263
Curti, R., Tightly coupled multiprocessor systems with high availability exploiting fault-tolerance features 20 (1987) 11–13
Cuypers, L., Computer music programming 25 (1989) 65–69
Czichon, T., *see* Sapiecha, K. 18 (1986) 371–379

Dack, D., *see* Loeb, J.Z. 3(2) (1977) 51–56
Dadd, T.C., *see* Billingsley, J. 21 (1987) 413–416
Dadda, L., On the simulation of Petri nets as a control tool 2(1) (1976) 38–45
Dadda, L., Petri nets for controlling and synchronizing complex asynchronous operations 3(1) (1977) 7–26
Dagless, E.L., J.S. Mason, Teaching microprocessors at university 3(2) (1977) 46–50
Dagless, E.L., *see* Aspinall, D. 3(2) (1977) 84–91
Dagless, E.L., B.G. Davies, M.D. Edwards, A keyboard orientated microprogram emulator and tester 4 (1978) 76–81
Dal Cin, M., E. Dilger, On the diagnosability of self-testing multi-microprocessor systems 7 (1981) 177–184
Dal Cin, M., Distributed diagnosis for computing networks 14 (1984) 139–144
Dal Cin, M., R. Brause, J. Lutz, E. Dilger, Th. Risse, Attempto: An experimental fault-tolerant multiprocessor system 20 (1987) 301–308
Dalban, J.P., *see* Auguin, M. 21 (1987) 171–178
Damnjanović, L.B., The software implementation of signature analyzers 16 (1985) 203–206
Damnjanović, Z., Microprocessor controlled programmable telegraph channel provides slow frequency hopping 16 (1985) 139–142
Dang, M., M. Diaz-Nava, G. Michel, Design of a VLSI communicating circuit for an industrial local network in control process and automated production 18 (1986) 173–181
Dang, M., C. Diot, I. Sabouni, L. Sponga, Specific data structure intended for the implementation of high level ISO standards: Associated algorithms and dedicated hardware 24 (1988) 103–110
Dang, Ng.X., M. Diaz-Nava, N. Husovic, Small local area network with a collision-free technique for connecting heterogeneous microcomputer systems 12 (1983) 167–174
Danthine, A.A.S., Microprocessing and microprogramming in Belgium 1(2) (1975) 6–8

Daprà, A., G. Castellari, S. Crespi Reghizzi, BTL: A language for testing electrical equipment 6 (1980) 391–394
Daprà, A., *see* Maderna, A. 6 (1980) 399–402
Das, P.K., B.B. Bhaumik, K.K. Bagchi, Dorms: A design tool for multiple microprocessor systems 12 (1983) 237–244
Das, P.K., *see* Bagchi, K.K. 16 (1985) 277–286
Das, P.K., *see* Fay, D.Q.M. 21 (1987) 623–628
Das, P.K., D.Q.M. Fay, Dynamically reconfigurable multi-transputer systems 23 (1988) 247–252
Das, P.K., D.Q.M. Fay, Performance studies of multi-transputer architectures with static and dynamic links 24 (1988) 281–290
Das Gupta, S., Chorng Hwa Chang, A pre-design tool for a class of parallel processing systems 18 (1986) 417–424
Dasiewicz, P., *see* Seviora, R.E. 18 (1986) 89–95
Dasiewicz, P., P.F. Corbett, R.E. Seviora, A VLSI architecture for the central processor of a digital switch 18 (1986) 591–595
Dasiewicz, P., A.O. Oshisanwo, MAPPS: A VLSI multiprocessor for the execution of production system programs 21 (1987) 251–258
Dassen, J.M.H., *see* Veldstra, B. 2(4) (1976) 54–64
David, D.J., A European user's view on the PET 6 (1980) 83–86
David, D.J., Vectoring and prioritizing interrupts by means of a PIA 6 (1980) 175–178
Davies, B.G., *see* Dagless, E.L. 4 (1978) 76–81
Davies, O.J., *see* Osmany, J. 20 (1987) 287–299
Davila, J.M., A.J. Phillipps, D. Tabak, Floating point arithmetic on a RISC 23 (1988) 179–184
De Amorim, C.L., *see* Fernandes, E.S.T. 25 (1989) 229–232
De Blasi, M., N. Fanelli, G. Gianelli, G. Degli Antoni, Profile finder, a firmware instrument for program measurements 3(1) (1977) 27–33
De Blasi, M., A. Gentile, E. Luque, J. Sorribes, A development system for self-tuning machines 14 (1984) 145–148
De Blasi, M., A. Gentile, E. Luque, A. Ripoll, Self-tuning machines 15 (1985) 195–202
De Blasi, M., A. Gentile, A co-processor for unification in Prolog: The microprogramming level 23 (1988) 143–148
De Blasi, M., A. Gentile, G. Lopez, A. Franco, Parallel backtracking Prolog engine 24 (1988) 607–612
De Bruyne, P.F., *see* Taeymans, J. 9 (1982) 231–236
De Carlini, U., *see* Cantone, G. 21 (1987) 371–382
De Cena, C., *see* Bertora, F. 7 (1981) 169–176
De Decker, D., P. Verbaeten, Modeling distributed systems: Communication issues in HERMIX 25 (1989) 239–243
De Floriani, L., *see* Ancona, M. 20 (1987) 183–188
De la Fuente, A., *see* León, G. 21 (1987) 541–548
De Luigi, B., *see* Männer, R. 6 (1980) 388–390
De Man, J.A., Designing digital systems with a function language 24 (1988) 227–232
De Moncuit, C., Interprocessor communication with decentralized bus 3(3) (1977) 9–13
De Monte, U., P. Fiorini, P. Rotoloni, G. Scirè, A software structure for appliance control 6 (1980) 17–19
De Sá, L., J. Dias, V. Silva, Image processing system with multiple DSPs 25 (1989) 41–46
De Souza, A.F., *see* Fernandes, E.S.T. 25 (1989) 229–232
Dearnley, P.A., The development of user-level microprograms for the Data General Eclipse computer 4 (1978) 82–86
Debaere, E.H., J.M. van Campenhout, A shared-memory MODULA-2 Multiprocessor for real-time control applications 18 (1986) 213–220
Debaere, E.H., A language coprocessor for the interpretation of threaded code 21 (1987) 593–602

Debaere, E.H., A language coprocessor as an HLL directed architecture 24 (1988) 701–708
Decuypere, H.J.M., Object oriented system supporting dynamic configuration 18 (1986) 455–462
Decuypere, H.J.M., *see* Hellemans, P.M.J. 24 (1988) 111–120
Defrise, M., *see* Vermeesch, J. 21 (1987) 129–136
Degli Antoni, G., *see* De Blasi, M. 3(1) (1977) 27–33
Del Corso, D., *see* Castellani, V. 2(1) (1976) 23–29
Del Corso, D., *see* Conte, G. 2(2) (1976) 7–10
Del Corso, D., A note on TM5501 usage 3(1) (1977) 54–58
Del Corso, D., *see* Conte, G. 3(2) (1977) 63–65
Del Corso, D., An experimental multimicroprocessor system with improved internal communication facilities 4 (1978) 326–332
Del Corso, D., L. Verrua, Contention delay in distributed priority networks 13 (1984) 21–30
Del Corso, D., P.L. Civera, L. Reyneri, C. Sansoè, An integrated controller for modified inter-integrated circuit protocol 19 (1987) 153–166
Delbar, P., A parallel approach to rule based systems 21 (1987) 507–514
Delgado, J.C., Designing computer architectures with SADAN 22 (1988) 205–216
Della Rosa, F., *see* Spalla, M. 23 (1988) 379–384
Demarteau, J., Indirection feature in control address at microlevel: A facility for macro interpreting 1(1) (1974) 29–34
Demartini, C., L. Ciminiera, A. Valenzano, Monosk: A network operating system for real time applications 16 (1985) 231–238
Demartini, C., *see* Ciminiera, L. 20 (1987) 59–64
Demeczky, J., T.Ujhelyi, An arithmetic oriented multiprocessor system for industrial measurements 6 (1980) 330–335
Demuth, Ch., *see* Glück, R. 21 (1987) 549–558
Depledge, P.G., *see* Rial, A. 11 (1983) 31–40
Desgoutte, P., *see* Parchoux, R. 3(2) (1977) 31–33
Devesa, N., G. Goncalves, M.-P. Lecouffe, B. Toursel, A controlled reduction model of functional programs on a distributed associative network 24 (1988) 613–620
Dewitte, J., J. Pellegrin, C. Brie, Design of a small development system to teach microprocessors 5 (1979) 93–99
D'Hautcourt-Carette, F., A microprogrammed virtual memory for Eclipse 4 (1978) 141–147
D'Hondt, O., *see* Taeymans, J. 3(3) (1977) 14–21
Di Lecce, V., *see* Aloisio, G. 22 (1988) 315–324
Di Manzo, M., *see* Bertora, F. 7 (1981) 169–176
Di Manzo, M., A. Frisiani, T. Vernazza, A monitoring distributed system 10 (1982) 19–24
Di Vito, G., *see* Ciciani, B. 14 (1984) 29–34
Di Zitti, E., *see* Curatelli, F. 24 (1988) 213–218
Di Zitti, E., G.M. Bisio, F. Curatelli, G. Parodi, High efficiency solution of triangular system equations on a 2-D array of transputers 25 (1989) 259–263
Dias, J., *see* De Sá, L. 25 (1989) 41–46
Díaz, M.A., *see* Castillo, A. 12 (1983) 207–210
Diaz-Nava, M., *see* Dang, Ng.X. 12 (1983) 167–174
Diaz-Nava, M., *see* Dang, M. 18 (1986) 173–181
Dickie, A.A., *see* Papazoglou, M.P. 15 (1985) 179–190
Diel, H., *see* Kreyßig, J. 25 (1989) 55–60
Dierickx, W., B. Philips, Rewriting programs to have a base for new software 16 (1985) 211–214
Dijkstra, E., *see* Ansorge, M. 18 (1986) 427–434
Dijkstra, E., *see* Piguet, C. 21 (1987) 463–470
Dilger, E., *see* Dal Cin, M. 7 (1981) 177–184

Dilger, E., *see* Dal Cin, M. 20 (1987) 301–308
Dimitriadis, B., N. Alexandridis, N.G. Bourbakis, A C-MOVE architecture based multiprocessor system for encoding Walsh-Hadamard transformed images 11 (1983) 227–232
Dimitriadis, B., N.G. Bourbakis, N. Alexandridis, On a multimicroprocessor pipelined structure for the real time encoding and transmission of regularly decomposed images, using a C-Move architecture 13 (1984) 267–274
Diot, C., *see* Dang, M. 24 (1988) 103–110
Dirkx, E., CRISC: Configurable reduced instruction set computer or complex reconfigurable instruction set computer 23 (1988) 239–242
Dirkx, E., J. Tiberghien, An animated simulation environment for microprocessors 24 (1988) 149–152
Dirkx, E., *see* Van der Jeugt, G. 24 (1988) 205–208
Distante, F., *see* Scarabottolo, N. 9 (1982) 309–314
Distante, F., L. Galvani, A. Maderna, M. Minotti, A software tool for microprocessor functional test 16 (1985) 107–112
Distante, F., L. Galvani, A. Maderna, M. Minotti, V. Piuri, BAT: Optimization algorithms and overall design of a behavioral automatic tester 18 (1986) 363–370
Distante, F., D. Sciuto, Behavioral testing of multilevel system software 24 (1988) 63–70
Ditzinger, A., *see* Biehl, G. 7 (1981) 326–333
Ditzinger, A., J. Beister, Word reduction in microprogrammed controllers by state-dependent preprocessing of the inputs 9 (1982) 161–174
Djurić, M., A.Jocić, A method for data acquisition, storage and sending applied to measuring instruments 16 (1985) 245–248
Doallo, R., *see* Zapata, E.L. 24 (1988) 647–654
Docchio, F., A. Longoni, G. Fusi, Microprocessor-based systems for optical spectroscopy 6 (1980) 316–320
Dodero, G., *see* Ancona, M. 20 (1987) 33–38
Dodero, G., *see* Ancona, M. 21 (1987) 287–294
Dodero, G., *see* Ancona, M. 24 (1988) 85–92
Domán, A., Paradocs: A highly parallel dataflow computer and its dataflow language 7 (1981) 20–31
Dong, Xu Jia, *see* Chao, Yen 20 (1987) 227–232
Doniants, V.N., S. Lori, M. Pellegrino, E.I. Pi'il, R. Stefanelli, Fault-tolerant reconfigurable processing arrays using bi-directional switches 14 (1984) 109–116
Doniants, V.N., V.G. Lazarev, M.G. Sami, R. Stefanelli, Reconfiguration of VLSI arrays: A technique for increased flexibility and reliability 16 (1985) 101–106
Doniants, V.N., V.G. Lazarev, R. Stefanelli, Fault-tolerance of cellular processing arrays: Algorithmic methods for yield enhancement and reliability 25 (1989) 113–118
Donnan, G., *see* Adamson, K. 24 (1988) 397–402
Donnelly, T., D.J. Mapps, R. Wilson, An intelligent microprocessor interface for a low-cost digital magnetic tape recorder 23 (1988) 333–338
Donnelly, T., *see* Jackson, T.J. 25 (1989) 281–285
Doroudi, K., *see* Navabi, Z. 18 (1986) 123–129
Doussy, J., Presentation of a skeletal microprogram assembler 1(3) (1975) 35–43
Drescher, N., A new microarchitecture based on a RISC like structure but with a CISC like instruction set 25 (1989) 121–125
Drinić, P., *see* Marić, S. 23 (1988) 203–206
Dubus, J.P., F. Wattrelot, A study of the visualization of alphanumerical texts on a tv screen for the use of partially sighted persons 9 (1982) 133–142
Duchene, J., J. Pro, Microprocessor based prototype development problems and solutions: A low-cost development system 4 (1978) 333–337
Duchene, J., *see* Lerallut, J.F. 5 (1979) 375–379

Duncan, F.G., J.R. Mühlbacher, Storage structure for rivalling multiprocessor organization 6 (1980) 44–50
Duncan, F.G., J.R. Mühlbacher, Personal computing in computer science education: How to solve the educational paradox 6 (1980) 78–80
Dunchev, L.S., *see* Koinakov, G.P. 14 (1984) 79–84
Duponcheel, L., J. Heyman, W. Van Puymbroeck, J. Lewi, I. Van Horebeek,
The algebraic data type specification language 21 (1987) 231–240
Dupuy, M., The 'communicateur': A multimicroprocessors simulation tool 1(5) (1975) 21–24
Duran, J., T. Mangir, Application of signature analysis to the concurrent test of microprogrammed control units 20 (1987) 309–322
Dvorak, V., A microprogrammed controller based on the logic processing unit 16 (1985) 341–344
Dvorak, V., A table-driven system controller with multi-way branching 23 (1988) 9–14
Dvorak, V., Synthesis of minimal multivalued decision diagrams 23 (1988) 207–212
Dye, C., The Sequel software language 4 (1978) 30–38

Eaglestone, B.M., *see* Comerford, P.J. 24 (1988) 555–562
Eberbach, E., J.R. Just, On fault-tolerance mechanisms in distribute computer systems 16 (1985) 239–244
Eberbach, E., *see* Just, J.R. 20 (1987) 25–32
Eberle, H., Analysis of processor-memory communication by the NS32000 processor family 22 (1988) 367–378
Edel, W., The 8002 microprocessing development aid system 5 (1979) 25–30
Edwards, M.D., *see* Dagless, E.L. 4 (1978) 76–81
Edwards, M.D., *see* Collis, G.V. 18 (1986) 243–250
Efimov, L.G., *see* Chernykh, E.V. 20 (1987) 65–67
Egan, K., C.P. Richardson, Object recognition using a data-flow computing system 7 (1981) 119–123
Eghtesadi, S., *see* Sandler, M.B. 24 (1988) 403–408
Eichenseher, I., T. Ungerer, E. Zehendner, CADL: A formal description language for parallel computer architectures 24 (1988) 363–370
Eicher, W., *see* Von Puttkamer, E. 18 (1986) 81–88
Eicher, W., E. Von Puttkamer, A. Scheel, H. Streit, A new strategy for interpreting LISP applied to FranzLISP 22 (1988) 379–384
Eisenbraun, E., *see* Clemen, R. 18 (1986) 441–444
El-Dhaher, A.H.G., T.S. Hassan, Y.A. El-Imam, An interactive microprocessor-based system for measurements and on-line analysis of stopped-flow data 11 (1983) 301–310
El Hamalaway, M.A., M.C. van Wormhoudt, A standard microcomputer system with diagnostic facilities 3(1) (1977) 59–66
El-Imam, Y.A., *see* El-Dhaher, A.H.G. 11 (1983) 301–310
El-Shirbeeny, E.-H.T., E.E.-D. Zakzouk, Microprocessor control of reactive power compensator 5 (1979) 384–386
Eliassen, F., K. Ellingsen, K.A. Olsen, A multilayered operating system for microcomputers 14 (1984) 45–54
Ellingsen, K., *see* Eliassen, F. 14 (1984) 45–54
Elliott, D., *see* Charlton, C.C. 13 (1984) 105–114
Elloy, J.P., *see* Chaudouard, C. 7 (1981) 241–248
Elyousfi, M.N., *see* Rateb, M.M. 6 (1980) 34–36
Elyousfi, M.N., *see* Rateb, M.M. 6 (1980) 37–40
Endriss, H., G. Klein-Hessling, G. Lawitzky, B. Schallenberger, Fast evaluation of design alternatives: A new approach for exploratory chip design 23 (1988) 75–80

Ercoli, P., Quantative evaluation and selection criteria for implementations of decision tables 18 (1986) 617–621
Erenyi, I., *see* Antola, A. 17 (1986) 205–218
Erenyi, I., *see* Tal, D. 25 (1989) 91–95
Ergang, K., *see* Téllez-Girón, R. 20 (1987) 147–152
Ergincan, F., A. Saatci, A stand-alone in-circuit emulator 17 (1986) 159–168
Erl, N., *see* Bemmerl, T. 24 (1988) 153–160
Erlandsson, G., A Pet operating system for GPIB-computer communications 9 (1982) 215–220
Ershov, A.P., Programming, the second literacy 8 (1981) 1–10
Eschenröder, G., *see* Winkelhage, F. 15 (1985) 1–10
Escher, W., *see* Nilsson, S.A. 2(3) (1976) 54–61
Etiemble, D., J.P. Aillaud, Multiple valued logic for microprocessors 6 (1980) 249–255
Etkin, J., D. Tabak, Method for implementing distributed control of a communication system 14 (1984) 181–186
Etkin, J., The transition from network operating systems to distributed global operating systems: Do we stretch too much the real-time requirements? 25 (1989) 295–300
Etkin, J., J.A. Zinky, Distributed debugging: Network analysis tools 25 (1989) 307–312
Eveking, H., SMAX: A Conlan member language for verifiable hardware descriptions 18 (1986) 549–558
Eveking, H., Formal reasoning about switch-level MOS descriptions 21 (1987) 453–462

Fabi, A., *see* Piuri, V. 20 (1987) 15–23
Faccio, M., *see* Ciccarella, G. 22 (1988) 353–366
Fadon, E.L., L.M. Ruiz, J.F. Tirado Fernandez, A general purpose computer emalator 4 (1978) 133–140
Faggin, F., Trends in microcomputers 5 (1979) 344–349
Faggin, F., Microelectronics in the eigthies 9 (1982) 1–6
Faldella, E., G. Neri, T. Salomon, Cost-effective station implementations in mininet 5 (1979) 285–294
Faldella, E., A microprocessor controlled packet transmission system 6 (1980) 417–427
Fan, K.H., *see* Londeix, B. 21 (1987) 241–248
Fanelli, N., *see* De Blasi, M. 3(1) (1977) 27–33
Faquet, G., D. Petitpierre, N. Magnenat-Thalmann, D. Thalmann, A portable relational data base management system for microcomputer 9 (1982) 17–26
Färber, G., A decentralized fair bus-arbiter 7 (1981) 32–36
Färber, G., Future architecture of high performance workstations 21 (1987) 7–14
Farina, J., *see* Mandado, E. 20 (1987) 203–208
Fay, D.Q.M., *see* Coey, W.A. 9 (1982) 93–98
Fay, D.Q.M., Implementation of the bounded buffer algorithm on 8-bit microcomputers 15 (1985) 21–34
Fay, D.Q.M., P.K. Das, Hardware reconfiguration of transputer networks for distributed object-oriented programming 21 (1987) 623–628
Fay, D.Q.M., *see* Das, P.K. 23 (1988) 247–252
Fay, D.Q.M., *see* Das, P.K. 24 (1988) 281–290
Fazio, G., A. Salsano, A fault-tolerant microcomputer with fail-safe outputs 12 (1983) 279–284
Fekete-Szuecs, L., Micro aided information system design 16 (1985) 215–216
Felix, R., *see* Temme, K.-H. 23 (1988) 47–50
Fernandes, E.S.T., Parallelism description on target architectures: An approach 16 (1985) 121–126
Fernandes, E.S.T., *see* Vasconcelos, N.Q. 20 (1987) 209–216
Fernandes, E.S.T., C.L. de Amorim, V.C. Barbosa, F.M.G. França, A.F. de Souza, MPH: A hybrid parallel machine 25 (1989) 229–232

Fernandes, E.S.T., *see* Vasconcelos, N.Q. 25 (1989) 381–386
Fernandes, P., *see* Navaux, P. 24 (1988) 197–204
Fernández, C., *see* Vaquero, A. 11 (1983) 127–132
Fernández, C., J.A. Martínez, Concurrent Pascal as a simulation language 12 (1983) 245–248
Fernandez, E.B., *see* Ancona, M. 20 (1987) 33–38
Fernandez del Campo, A., *see* Vinyes Sanz, J. 12 (1983) 111–114
Fernin, P., S.C. Winter, D.R. Wilson, H. Reignier, Implementation of an intelligent SAR image registration system 24 (1988) 315–324
Feró, L., *see* Alló, G. 19 (1987) 27–36
Ferrari, R., M. Tagliabue, L. Zoccolante, S. Crespi Reghizzi, Implementing the kernel for the concurrent distributed language MML on different microprocessors 20 (1987) 91–97
Fey, P., A. Antonov, Real-time extension of a higher level programming language for microcomputers 15 (1985) 191–194
Figueras, J., A. Alabau, An experience on microcomputer education 3(2) (1977) 34–38
Filippone, V., *see* Ancona, M. 21 (1987) 287–294
Finnila, C.A., *see* Love, H.H. 6 (1980) 381–387
Fiorini, P., *see* De Monte, U. 6 (1980) 17–19
Fischer, W., *see* Clemen, R. 18 (1986) 441–444
Fisher, J.A., Microprogramming, microprocessing and supercomputing 24 (1988) 17–20
Flego, D., H. Schweinzer, VAMPIX, a multimicroprocessor control system adaptable to special complex machines 21 (1987) 325–330
Flinn, E.A., Development of microprocessor in-service training courses 9 (1982) 289–294
Florentin, J.S., An alternative approach to multiprocessor design 13 (1984) 11–20
Fontenier, G., *see* Lerallut, J.F. 5 (1979) 375–379
Fontenier, G., *see* Hou, K.M. 19 (1987) 319–326
Fontenier, G., *see* Hou, K.M. 19 (1987) 401–406
Fontenier, G., *see* Lebee, P. 22 (1988) 187–204
Forcina, A., *see* Velardi, P. 17 (1986) 255–266
Fornasiero, A., *see* Spalla, M. 23 (1988) 379–384
Forrest, J., *see* Bissett, A. 25 (1989) 387–390
Fox, A., C.T. Spracklen, C.P. Jolly, Logic synthesis with constraints 24 (1988) 339–346
Fox, A., *see* Loewenstein, P. 24 (1988) 767–772
Fox, E.R., K.J. Kiefer, R.F. Vangen, S.P. Whalen, RISC, a microprocessor architecture for GaAs technology 22 (1988) 385–392
França, F.M.G., *see* Vasconcelos, N.Q. 20 (1987) 209–216
França, F.M.G., *see* Barbosa, V.C. 24 (1988) 511–518
França, F.M.G., *see* Fernandes, E.S.T. 25 (1989) 229–232
Francis, N.D., Generation of random numbers on micros: A simulation study 15 (1985) 17–20
Franco, A., *see* De Blasi, M. 24 (1988) 607–612
Frietman, E.E.E., F. Bruggeman, C.J. van Spronsen, Eyeball-position controlled communication system EPCOS 14 (1984) 173–176
Frietman, E.E.E., F. Bruggeman, The inner product processor 21 (1987) 313–318
Frisiani, A., *see* Di Manzo, M. 10 (1982) 19–24
Frison, P., E. Gautrin, The Madmacs layout system 18 (1986) 445–452
Fujita, S., K. Yoshida, A general purpose cross-assembler for microprocessors 3(4) (1977) 88–89
Fukami, S., Performance evaluation method for interprocessor interrupts by message passing 23 (1988) 185–190
Fukumoto, I., *see* Kato, S. 3(4) (1977) 86–87
Fumagalli, A., *see* Borsotti, A. 9 (1982) 77–84
Funakubo, H., T. Yamaguchi, Y. Saito, Cooperative movement of electrical total arm and hand prosthesis controlled by microcomputer 3(4) (1977) 65–70

Furht, B., The application of the RMX/80 in the multimicroprocessor system for digital signal processing 9 (1982) 7–16
Furht, B., A contribution to classification and evaluation of structures for parallel computers 25 (1989) 203–208
Furnadziev, V.I., *see* Koinakov, G.P. 14 (1984) 79–84
Fusi, G., *see* Docchio, F. 6 (1980) 316–320

Gabrys, B., *see* Gorawski, M. 15 (1985) 75–84
Gai, S., *see* Somenzi, F. 15 (1985) 119–128
Gai, S., F. Somenzi, M. Spalla, Zero delay elements in logic simulation 16 (1985) 335–340
Gai, S., A. Lioy, F. Neri, VLSI implementation of linear feedback shift registers for microprocessor applications 18 (1986) 435–440
Gai, S., A. Lioy, A multilevel hardware description language 25 (1989) 183–187
Gaillard, P., *see* Bina, M. 19 (1987) 327–334
Gaines, B.R., Hardware engineering and software engineering 3(2) (1977) 16–21
Gal, T., K. Agusa, Y. Ohno, SAFE: A stand alone fast editor 8 (1981) 103–110
Gallacher, J., Microcomputer control of a batch chemical process plant 3(3) (1977) 60–67
Galvani, L., *see* Distante, F. 16 (1985) 107–112
Galvani, L., *see* Distante, F. 18 (1986) 363–370
Gandelli, A., *see* Carminati, E. 25 (1989) 335–339
Garcao, A.S., Status report Portugal 3(4) (1977) 33
Garetti, P., P. Laface, S. Rivoira, MODOSK: A modular distributed operating system kernel for real-time process control 9 (1982) 201–214
Garitagoitia, J.R., I. Ramos, Relocating direct linking loader for MCS-80 microcomputer series 3(3) (1977) 22–28
Gasbarri, G., *see* Ciborra, C. 4 (1978) 232–238
Gatti, G., *see* Chientaroli, M. 16 (1985) 305–312
Gautrin, E., *see* Frison, P. 18 (1986) 445–452
Geideman, W.A., *see* Lane, J.H. 21 (1987) 31–38
Geisse, H., *see* Bermbach, R. 14 (1984) 177–180
Gelles, A., Whither the home robot? A personal opinion 9 (1982) 285–288
Gelles, A., Information and the household: A second opinion 10 (1982) 299–310
Gelles, A., Robotics and artificial intelligence as educational tools for developing self-sufficiency. Part I: Descriptive overview 13 (1984) 221–226
Gelles, A., Robotics and artificial intelligence as educational tools for developing self-sufficiency. Part II: Curriculum sampler 13 (1984) 227–248
Gentile, A., *see* De Blasi, M. 14 (1984) 145–148
Gentile, A., *see* De Blasi, M. 15 (1985) 195–202
Gentile, A., *see* De Blasi, M. 23 (1988) 143–148
Gentile, A., *see* De Blasi, M. 24 (1988) 607–612
Gentile, G., M.G. Sami, M. Terzoli, Design of switches for self-reconfiguring VLSI array structures 14 (1984) 99–108
Georgiadis, P.I., *see* Papazoglou, M.P. 15 (1985) 179–190
Georgiev, A., *see* Churin, I.N. 23 (1988) 153–154
Gerez, S.H., *see* Spaanenburg, L. 16 (1985) 83–88
Gerst, H., Verification of the VLSI-/370 microprocessor 24 (1988) 137–146
Ghosal, D., L.M. Patnaik, SHAMP: An experimental shared memory multimicroprocessor system for performance evaluation of parallel algorithms 19 (1987) 179–192
Gianelli, G., *see* De Blasi, M. 3(1) (1977) 27–33
Gianuzzi, V., *see* Ancona, M. 20 (1987) 33–38
Gianuzzi, V., *see* Ancona, M. 21 (1987) 287–294
Gianuzzi, V., *see* Ancona, M. 24 (1988) 57–62
Gianuzzi, V., *see* Ancona, M. 24 (1988) 85–92
Gilbert, J., *see* Cook, J. 24 (1988) 621–626

Gilbert, L.A., New technologies and alternative forms of education 6 (1980) 221–225
Giloi, W.K., *see* Behr, P. 8 (1981) 153–166
Giordana, M., *see* Castellani, V. 2(1) (1976) 23–29
Giordana, M., *see* Conte, G. 2(2) (1976) 7–10
Giordana, M., *see* Conte, G. 3(2) (1977) 63–65
Giordano, A., F. Giunchiglia, M. Maresca, T. Vernazza, Evaluation of a multiprocessor structure for image processing 19 (1987) 5–18
Girardi, G., *see* Bonomo, A. 21 (1987) 569–576
Girault, C., M. Cheminaud, M.F. Le Roch, Le projet P.A.R.I.S. 2(4) (1976) 19–28
Giulio, I., M. Antonino, S. Carlo, Support environment for the development of problem oriented concurrent systems 23 (1988) 317–324
Giunchiglia, F., *see* Giordano, A. 19 (1987) 5–18
Glowacz, C., *see* Matthäus, C. 24 (1988) 233–238
Glück, R., Ch. Demuth, OC-FP, an applicative language combination with Occam and the algebra of processes 21 (1987) 549–558
Gobbi, G., G. Martella, MIRS: A machine information retrieval system 7 (1981) 104–109
Goczyla, K., Disk space allocation schemes for real-time data gathering applications 18 (1986) 499–504
Goczyla, K., Reliability of disk file organisation for microcomputers 23 (1988) 191–196
Goczyla, K., Microcomputer database design 25 (1989) 3–7
Goeldel, C., *see* Husson, R. 3(2) (1977) 39–45
Golze, U., *see* Sedlak, H. 18 (1986) 583–590
Goncalves, G., M.-P. Lecouffe, B. Toursel, S. Niar, A network of transputers to emulate a parallel symbolic processor 23 (1988) 149–152
Goncalves, G., *see* Devesa, N. 24 (1988) 613–620
González, A., J.M. Llabería, J. Cortadella, A Mechanism for reducing the cost of branches in RISC architectures 24 (1988) 565–572
Gonzalez-Rubio, R., M. Couprie, DDC: Delta Driven Computer, and uSyC: Microprogrammable Symbolic Coprocessor 24 (1988) 599–606
Gorawski, M., B. Gabrys, R. Winiarczyk, On the industrial phase of the development of a process control microcomputer system 15 (1985) 75–84
Gorski, J., H.J. Napiatek, Formal specification of basic mechanisms of a message passing kernel 21 (1987) 213–222
Goser, K.F., *see* Marks, K.M. 21 (1987) 523–530
Gossens, M., *see* Rijks, E. 21 (1987) 639–646
Grabowiecki, T., A. Pawlak, W. Sakowski, A university framework for correct by construction IC design 23 (1988) 37–44
Grabowiecki, T., A. Pawlak, PROLOG as a formalism for VLSI design specification 25 (1989) 157–162
Grantner, J., *see* Arato, P. 3(1) (1977) 34–48
Grass, W., A synthesis system for PLA-based programmable hardware 12 (1983) 15–32
Grass, W., R. Buschke, R. Rauscher, BOSS: A functional block oriented intermediate language for RT-level hardware descriptions 20 (1987) 161–165
Grätsch, W., H. Kästner, Firmware monitoring: History and perspective 8 (1981) 237–246
Gregoretti, F., *see* Marsan, M.A. 7 (1981) 124–133
Gregoretti, F., F. Maddaleno, M. Zamboni, Monitoring tools for multiprocessors 18 (1986) 409–416
Gregoretti, F., Z. Segall, Analysis and evaluation of parallel rectangle intersection for VLSI design rule checking 19 (1987) 85–100
Griese, M., *see* Bruck, K. 25 (1989) 61–64
Grimaldi, P., A. Marcelli, Toward a structured approach to expert system development 25 (1989) 27–32
Gritzali, F., *see* Papakonstantinou, G. 6 (1980) 226–231
Grossmann, B., E. Kwee, A. Lehmann, Practical experiences with vertical migration 12 (1983) 185–192

Gruhn, V., M. Hallmann, A Petri net based compiler for the prototyping language RELOS 24 (1988) 471–482
Grünwald, S., The selection of expert system development environments 25 (1989) 21–26
Gruszecki, M., Throughput evaluation in digital switching systems with distributed microprocessor control 11 (1983) 207–216
Gruszecki, M., P. van Esbroeck, Capacity and performance prediction in large distributed microprocessor systems under increasing processing speed 18 (1986) 667–674
Guazzoni, P., *see* Annunziata, M. 18 (1986) 515–524
Guesalaga, A.R., A digital method of demodulation and filtering for angular errors in conical scan tracking radars 23 (1988) 327–332
Gueth, R., *see* Behr, P. 8 (1981) 153–166
Guggenbühl, W., Status report of microprocessing and microprogramming in Switzerland 5 (1979) 143–144
Guiheux, H., C. Mardapittas, D.R. Wilson, S.C. Winter, A model based recognition system for tactile data 24 (1988) 425–432
Guillemont, M., *see* Lebee, P. 22 (1988) 187–204
Gunhildsbu, O.M., *see* Knoppers, P. 24 (1988) 275–280
Gupta, M.N., *see* Arora, R.K. 19 (1987) 219–226
Gupta, M.N., *see* Arora, R.K. 22 (1988) 263–272
Guterman, H., *see* Ben-Yaakov, S. 16 (1985) 319–324
Guyot, A., *see* Mermet, J. 1(1) (1974) 35–43
Gyergyek, L., *see* Trobec, R. 21 (1987) 75–82
Gyergyek, L., *see* Novak, F. 21 (1987) 489–496
Gyergyek, L., *see* Novak, F. 22 (1988) 273–276
Gyergyek, L., *see* Trobec, R. 25 (1989) 353–358
Györkös, J., *see* Welzer, T. 25 (1989) 375–380

Hagerer, A., *see* Hahn, W. 24 (1988) 291–298
Hahn, W., *see* Mündemann, F. 16 (1985) 67–72
Hahn, W., Event-flow computation as key to fast digital design simulation 18 (1986) 27–38
Hahn, W., H. Anger, A. Hagerer, B. Schuster, A multi-transputer-net as a hardware simulation environment 24 (1988) 291–298
Hakimi, S.L., *see* Kreutzer, S.E. 20 (1987) 323–330
Hakozaki, K., *see* Yamamoto, M. 3(4) (1977) 58–64
Halang, W.A., On real-time features available in high-level languages and yet to be implemented 12 (1983) 79–88
Halang, W.A., An independent test plan processor aiding the approval of software written for embedded systems 16 (1985) 207–210
Halang, W.A., On methods for direct memory access without cycle stealing 17 (1986) 277–284
Halang, W.A., A precisely timed fully parallel process input/output facility for control applications 18 (1986) 657–664
Halang, W.A., A distributed logic program instruction prefetching scheme 19 (1987) 407–416
Halang, W.A., A PC-based evaluation and control system for gas-liquid chromatography 21 (1987) 153–160
Halang, W.A., S.-K. Jung, An independently working computer peripheral generating robot arm trajectories 23 (1988) 129–134
Halang, W.A., Parallel administration of events in real-time systems 24 (1988) 687–692
Halang, W.A., Education of real-time systems engineers 25 (1989) 71–76
Halatsis, C., *see* Arvillias, A.C. 6 (1980) 161–167
Halatsis, C., *see* Pombortsis, A. 18 (1986) 463–467
Halatsis, C., *see* Paschalis, A.M. 20 (1987) 271–281

Halatsis, C., *see* Vlahavas, J. 21 (1987) 259–266
Halatsis, C., *see* Stamatis, D. 21 (1987) 515–522
Hallmann, M., *see* Gruhn, V. 24 (1988) 471–482
Halsall, F., P.F. Lister, Microprocessor applications training 6 (1980) 359–362
Hammer, D., *see* Harms, K. 1(5) (1975) 40–45
Händler, W., *see* Albert, B. 8 (1981) 257–262
Hang, W., *see* Clemen, R. 18 (1986) 441–444
Hanna, F.K., Microprocessors in the United Kingdom: The state of the Art-1978 5 (1979) 145–149
Hanna, W.A., Assembly language for a three dimensional computer 2(2) (1976) 38–47
Hanna, W.A., Parallel algorithms for solving matrix-oriented problems on the three-dimensional computer 4 (1978) 227–231
Hannemyr, G., High quality document production on a personal computer 11 (1983) 233–242
Hansen, O., *see* Bemmerl, T. 24 (1988) 153–160
Harmat, L., A static board tester system for development purposes 5 (1979) 315–320
Harms, K., D. Hammer, E. Benes, A program development system for the Intel sim 8-01 microcomputer set 1(5) (1975) 40–45
Harrison, P.G., The technology race in microprocessor application 4 (1978) 18–26
Hartenstein, R.W., On the interpretive mechanisms of microprogrammed systems 1(1) (1974) 15–20
Hartenstein, R.W., Research and development in microprogramming, microprocessors and related fields in the F.R. Germany 5 (1979) 121–124
Hartenstein, R.W., K. Lemmert, A CAD tool box for VLSI around a hardware description language 18 (1986) 99–105
Hartenstein, R.W., A. Hirschbiel, M. Weber, A flexible architecture for image processing 21 (1987) 65–72
Hartenstein, R.W., U. Welters, MLED: A multiple abstraction level graphical editor 21 (1987) 585–590
Hartenstein, R.W., *see* Blödel, J. 23 (1988) 15–20
Hartlmueller, P., *see* Bender, H. 18 (1986) 153–159
Hassan, T.S., *see* El-Dhaher, A.H.G. 11 (1983) 301–310
Hassitt, A., *see* Berndt, H. 4 (1978) 293–296
Hassitt, A., *see* Berndt, H. 4 (1978) 338–339
Haus, G., Music processing at L.I.M. 24 (1988) 435–442
Hausen, H.-L., An effectively instrumentable life cycle model 21 (1987) 361–370
Hayes, B., *see* Cornford, T. 17 (1986) 195–204
Healy, J., A new microprocessor testing method 1(5) (1975) 15–20
Hebenstreit, J., The French national experiment in computer based education 7 (1981) 82–91
Heering, J., The Intel 8086, the Zilog Z8000, and the Motorola MC68000 microprocessors 6 (1980) 135–143
Heinkele, S., Timing verification for the VLSI-/370 microprocessor 24 (1988) 125–130
Hellemans, P.M.J., H.J.M. Decuypere, Automatic protocol generation based on X.409 specifications 24 (1988) 111–120
Helwig, K., *see* Clemen, R. 18 (1986) 441–444
Hemphill, J.M., The microcomputer and computer science education 3(2) (1977) 26–27
Hermida, R., *see* Bautista, A. 9 (1982) 27–38
Hermida, R., J. Ruz, J. Tirado, A. Bautista, RDBAS: A relational database system for non-experienced users 12 (1983) 99–110
Herrmann, J., *see* Temme, K.-H. 23 (1988) 47–50
Herrmann, J., LIMES: LearnIng Module EStimations 23 (1988) 81–86
Hersch, R.D., F. Maddaleno, C. Nicks, M. Bürki, The video-ram multiprocessor architecture 24 (1988) 503–510

Hess, W.E., K.J. Burkhardt, Design of a binary coded decimal integer arithmetic unit 7 (1981) 97–103
Heusala, H.H., *see* Kivelae, J.E. 18 (1986) 631–635
Heyman, J., *see* Duponcheel, L. 21 (1987) 231–240
Hildering, W.C., *see* Reijns, G.L. 21 (1987) 405–412
Hill, F.J., *see* Behbahani, A.M. 18 (1986) 355–362
Hill, M.R., *see* Holding, D.J. 24 (1988) 801–806
Hinkel, R., Low-level-vision in an autonomous mobile robot 23 (1988) 135–140
Hinkel, R., T. Knieriemen, E. von Puttkamer, A rotating laser range finder and attached data interpretation for use in an autonomous mobile robot 24 (1988) 411–418
Hirakawa, Y., N. Kawashima, M. Imase, Extensible packaged architecture 22 (1988) 65–74
Hirschbiel, A., *see* Hartenstein, R.W. 21 (1987) 65–72
Hoffman, H., J. Nemec, A fast microprocessor for control applications 3(3) (1977) 53–59
Hoffmann, K., Status and trends of dynamic and static RAMs 10 (1982) 119–128
Hoffmann, R., A classification of interpreter systems 12 (1983) 2–8
Hofmann, F., *see* Bolch, G. 12 (1983) 267–278
Hofstede, J., *see* Scholten, J. 21 (1987) 613–620
Hoirup, T., Teaching of children with general learning difficulties 6 (1980) 215–220
Hojka, N., K.P. Judmann, On the design of a micro-processor education-system 3(2) (1977) 66–68
Hojka, N., Increasing performance by microcoding 8 (1981) 229–236
Holding, D.J., *see* Carpenter, G.F. 22 (1988) 175–186
Holding, D.J., M.R. Hill, G.F. Carpenter, The design of distributed, software fault tolerant, real-time systems incorporating decision mechanisms 24 (1988) 801–806
Holstein, B., *see* Spaanenburg, L. 16 (1985) 83–88
Holtkamp, B., UNIX requirements for architectural support 15 (1985) 129–140
Hoppe, B., *see* Bolch, G. 12 (1983) 267–278
Hoppe, B., Circuit optimization: Gate level modeling and multiobjective programming 25 (1989) 171–176
Hoshiko, Y., *see* Ohba, N. 14 (1984) 17–28
Hou, K.M., G. Fontenier, U.T.C. multiprocessor hardware structure and software approach to design its monitor 19 (1987) 319–326
Hou, K.M., G. Fontenier, M. Senadji, A simple bus analyzer: Parallel debugging tool and real time performance measure instrument and its application 19 (1987) 401–406
Hou, K.M., *see* Lebee, P. 22 (1988) 187–204
Houghton, A.D., N.L. Seed, A computer architecture for real time image processing using VLSI 24 (1988) 309–314
Houtekamer, G.E., A.A. Vlasblom, A.J. Breimer, A model of the I/O subsystem of the Philips P4500 minicomputer 18 (1986) 491–497
Houtekamer, G.E., *see* Kroon, J.G.M. 21 (1987) 347–356
Hsu, J.P., *see* Kuo, Y.H. 21 (1987) 647–654
Huang, K., *see* Ni, P.N. 20 (1987) 283–286
Huang, K., Diagnosability of system faults with propagation under asymmetric invalidation 24 (1988) 723–730
Huang, W.-K., F. Lombardi, A low complexity approach for fault detection in C-testable orthogonal VLSI arrays 22 (1988) 277–300
Hübner, U., *see* Matthäus, C. 24 (1988) 233–238
Hughes, P., Microcomputers: The European challenge to the United States 6 (1980) 4–8
Huijsman, R.D., J. Van Katwijk, W.J. Toetenel, Performance aspects of ADA tasking in embedded systems 21 (1987) 301–310

Hulin, M., An expert system for mapping computer architectures into semicustom integrated circuits 18 (1986) 107–122
Hunger, A., *see* Téllez-Girón, R. 20 (1987) 147–152
Hurson, A.R., L.L. Miller, Indexed files in the parallel file environment 22 (1988) 243–255
Huschke, H., Communications strategies in international banking 24 (1988) 11–14
Husovic, N., *see* Dang, Ng.X. 12 (1983) 167–174
Husson, R., *see* Lagier, M.L. 2(4) (1976) 34–39
Husson, R., C. Goeldel, J.P. Thomesse, P. Nonn, Information processing, automation and microprocessor education 3(2) (1977) 39–45
Hutchinson, N., Bandwidth reduction for speech transmission: A microprocessor based simulator 12 (1983) 231–236
Hutchison, D., J. Balfour, J. Mariani, A dual interface to Cambridge ring and Ethernet-type local networks 13 (1984) 97–104
Huys, C., *see* Spaanenburg, L. 18 (1986) 341–346
Hwang, Being-Yuh, *see* Kuo, Yau-Hwang 23 (1988) 21–26

Ifeachor, E.C., *see* Jackson, T.J. 25 (1989) 281–285
Iglesias, M., *see* Vaquero, A. 11 (1983) 127–132
Ikeda, K., *see* Kato, S. 3(4) (1977) 86–87
Imam, M.H., *see* Mir, M. 25 (1989) 177–182
Imase, M., *see* Hirakawa, Y. 22 (1988) 65–74
Inose, H., Information and the household 7 (1981) 211–219
Irro, F., *see* Assmuth, R. 2(3) (1976) 24–40
Isaac, J.R., *see* Rao, S.S.S.P. 16 (1985) 17–30
Ishikawa, C., *see* Maekawa, M. 8 (1981) 189–202
Ismail, M.A., *see* Zapata, E.L. 24 (1988) 647–654
Isoda, S., Verification of microprogram transformation commands for a firmware editor (revised) 17 (1986) 61–76
Isoda, S., Machine-independent microprogram address allocation through hierarchical structuring 19 (1987) 291–304
Isomura, M., *see* Okamoto, K. 6 (1980) 288–295
Isurugi, Y., *see* Koyama, S. 3(4) (1977) 75–78
Italiano, M., *see* Bonomo, A. 21 (1987) 569–576
Itoh, K., *see* Kato, S. 3(4) (1977) 86–87
Ivanov, G.I., The universal programs method and its application for implementing boolean functions using microcomputers 4 (1978) 87–91
Iványi, G., Synchronization means in an M 051 duplex microprocessor system 2(2) (1976) 48–52

Jackson, T.J., D.J. Mapps, E.C. Ifeachor, T. Donnelly, A real-time transputer-based system for a digital recording data channel 25 (1989) 281–285
Jäger, U., *see* Schindel, U. 10 (1982) 139–152
James, P., Operation of the Signetics 2652 multi-protocol communication controller 5 (1979) 16–24
Janecek, J., V. Jánes, Register array processor 21 (1987) 23–30
Jánes, V., *see* Blažek, Z. 14 (1984) 187–190
Jánes, V., *see* Blažek, Z. 17 (1986) 77–78
Jánes, V., *see* Janecek, J. 21 (1987) 23–30
Jang, Won, *see* Bourbakis, N.G. 23 (1988) 115–122
Janković, S.M.Matić.D.J.I., An error-controlling device for HF channels 6 (1980) 395–398
Jansen, P.G., *see* Smit, G.J.M. 24 (1988) 519–524
Janssens, D., Message passing and graph transformations: A model of actor computation 18 (1986) 307–318
Jarocki, R., *see* Sapiecha, K. 16 (1985) 217–220
Jatho, Ch., *see* Bruck, K. 25 (1989) 61–64

Jervis, L., D. Sciuto, A reconfiguration algorithm for wafer-scale integration of systolic arrays 21 (1987) 83–90
Jin, Shi-Yao, Z. Shuan, Shi-Sheng Yie, A vectorized superminicomputer: VAX-11/780 with vector processing 24 (1988) 743–746
Jinadasa, N., Controlling the hidden costs in microcomputer system design 4 (1978) 4–8
Jinsi, S., *see* Reijns, G.L. 21 (1987) 405–412
Jobbágy, Á., A microprocessor-based medical measuring system 6 (1980) 321–324
Jocić, A., *see* Djurić, M. 16 (1985) 245–248
Jocković, M., An application of Petri nets in the control system of the FTC 24 (1988) 681–686
Johansson, A., *see* Ahlbom, G. 9 (1982) 267–270
Johansson, L-Å., Virtual memory management for microcomputers in real-time applications 5 (1979) 235–238
Johansson, L.T., A computer architecture suited to multiplex wiring systems in cars 19 (1987) 1–4
Jolly, C.P., *see* Fox, A. 24 (1988) 339–346
Jones, H., A survey of current work in microprogramming 1(4) (1975) 8–12
Jones, J., Towards an HLL orientated microprocessor instruction set 6 (1980) 158–160
Jones, L., An introduction to microprogramming 1(1) (1974) 5–7
Jovanov, E.,T. Aleksić, Z. Stojkov, D. Starčević, A sorting processor for microcomputers 23 (1988) 273–278
Jové, T., *see* Cortadella, J. 24 (1988) 573–580
Judmann, K.P., *see* Hojka, N. 3(2) (1977) 66–68
Judmann, K.P., *see* Schauer, H. 5 (1979) 113–117
Jul, E., Microcomputer comparison: Concurrent Pascal on Intel 8080 6 (1980) 144–148
Jullière, M., L. Marce, H. Place, Teaching of digital control with microprocessors 6 (1980) 111–114
Jung, S.-K., *see* Halang, W.A. 23 (1988) 129–134
Juntunen, T.T., *see* Kivelae, J.E. 18 (1986) 631–635
Juntunen, T.T., J.E. Kivelae, A. Reinikka, M. Sipola, J-P. Soininen, K. Tiensyrjä, T. Tikkanen, Real-time structured analysis in system level design of embedded ASICs 24 (1988) 449–454
Jurkiewicz, A., *see* Zabludowski, A. 13 (1984) 171–178
Just, J.R., *see* Eberbach, E. 16 (1985) 239–244
Just, J.R., E. Eberbach, The synthesis of control algorithms for fault-tolerant distributed computer systems 20 (1987) 25–32
Justice, N.A., Analogue to microprocessor in easy stages 4 (1978) 47–52
Jyllilae, H., T. Lehtinen, S. Nikkilae, Virtual token access method and its implementations 18 (1986) 161–171

Kajiwara, M., *see* Okada, K. 3(4) (1977) 79–82
Kalouptsidis, N., *see* Theodoridis, S. 21 (1987) 137–140
Kalyayev, A.V., Multiprocessor homogeneous calculating structures with distributed memory and universal commutation 5 (1979) 73–81
Kamath, C., V.C. Bhavsar, Implementation and performance prediction of some parallel algorithms on the Plexus microcomputer network 10 (1982) 25–32
Kaminska, B., J. Zabrodzki, The concept of proper operation region in digital integrated circuit testing, the 8080A microprocessor as an example 12 (1983) 285–290
Kaneda, Y., *see* Wada, K. 21 (1987) 275–282
Kaneda, Y., *see* Kuo Pan, S.M. 25 (1989) 9–14
Kanopoulos, N., N. Vasanthavada, A monolithic scan-line bit producer for real-time image rasterization 21 (1987) 57–64

Kanopoulos, N., P. Marinos, Design of a bus-monitor for real-time applications 24 (1988) 717–722
Kärkkäinen, P., *see* Niemelä, U. 21 (1987) 427–432
Karppinen, M., *see* Saukkonen, S. 9 (1982) 319–324
Kas, I., *see* Shi, J. 23 (1988) 339–344
Kasaš, Z., *see* Lazić, D. 16 (1985) 261–266
Kästner, H., *see* Grätsch, W. 8 (1981) 237–246
Kaszuba, R.M., Some issues on management of application connection establishment 23 (1988) 373–378
Katbab, A., A multiprocessor architecture for robot arm control 24 (1988) 673–680
Kato, M., *see* Watanabe, M. 3(4) (1977) 83–85
Kato, S., I. Fukumoto, T. Sekiya, K. Itoh, K. Ikeda, K. Yajima, M. Saito, Application of microcomputer to integrated sleep monitor 3(4) (1977) 86–87
Katzenelson, J., M. Werner, Microprocessors: New devices: Classical problems 3(2) (1977) 22–25
Kawashima, N., *see* Hirakawa, Y. 22 (1988) 65–74
Kayser, D., Machine representation of knowledge for computer-assisted instruction 6 (1980) 209–214
Kazimierczak, J., A technique to transform programs into circuits 22 (1988) 125–140
Keikkilä, T., *see* Niemelä, U. 21 (1987) 427–432
Kelbler, J., A method of address tracing by firmware 22 (1988) 141–146
Kemppainen, P., *see* Saukkonen, S. 9 (1982) 319–324
Kemppainen, P., *see* Auer, A. 24 (1988) 51–56
Kerin, K., V. Milutinović, Analyzing the Risc in gallium arsenide 17 (1986) 119–128
Kerner, H., Microprogramming by data flow 12 (1983) 181–184
Kerner, H., H. Rainel, EDDA, a language based on Petri nets and the dataflow principle for the development of parallel programs 18 (1986) 299–305
Kessel, W., M. Klages, Multi-microcomputer system based on VME-Bus 16 (1985) 273–276
Kick, B., Logic synthesis in th design of the VLSI-/370 microprocessor 24 (1988) 43–48
Kida, Y., *see* Masada, E. 3(4) (1977) 71–74
Kiefer, K.J., *see* Fox, E.R. 22 (1988) 385–392
Kilpatrick, P.L., *see* Crookes, D. 21 (1987) 559–566
Kilpatrick, P.L., *see* Milligan, P. 23 (1988) 253–258
Kilpatrick, P.L., *see* Scott, N.S. 24 (1988) 535–540
Kilpatrick, P.L., *see* Scott, N.S. 25 (1989) 247–251
Kirilov, A.S., *see* Bruck, K. 23 (1988) 357–358
Kirilov, A.S., *see* Bruck, K. 25 (1989) 61–64
Kita, Y., *see* Mori, R. 3(4) (1977) 40–49
Kita, Y., *see* Mori, R. 5 (1979) 155–165
Kivelae, J.E., H.H. Heusala, T.T. Juntunen, J.A. Lahti, J.-P. Soininen, S. Säynäjäkangas, The dependency notation as a graphical description language for logic design and silicon compilation 18 (1986) 631–635
Kivelae, J.E., *see* Juntunen, T.T. 24 (1988) 449–454
Klages, M., *see* Kessel, W. 16 (1985) 273–276
Klar, R., N. Luttenberger, VLSI-based monitoring of the inter-process-communication in multi-microcomputer systems with shared memory 18 (1986) 195–204
Klavžar, S., *see* Novak, F. 22 (1988) 273–276
Klein, R., W.E. Tchon, Nonvolatile semiconductor memory 10 (1982) 129–138
Klein, R.D., A reliable low-cost graphic system 5 (1979) 44–47
Klein-Hessling, G., *see* Endriss, H. 23 (1988) 75–80
Klenk, U., Microcomputers in linguistic data processing: Context-free parsing 9 (1982) 281–284
Knieriemen, T., *see* Hinkel, R. 24 (1988) 411–418
Knödler, B., W. Rosenstiel, A Prolog preprocessor for Warren's abstract instruction set 18 (1986) 71–79

Knoppers, P., A.J. van de Goor, O.M. Gunhildsbu, P. Stravers, Transputer network with flexible topology 24 (1988) 275–280
Kober, R., H. Kopp, Ch. Kuznia, SMS 101: A structured multimicroprocessor system with deadlock-free operation scheme 2(2) (1976) 56–64
Kober, R., K. Nagel, Solving linear equations with the SMS 201 parallel processor 5 (1979) 53–54
Koinakov, G.P., L.S. Dunchev, V.I. Furnadziev, A method for real time numerical integration 14 (1984) 79–84
Kok, H., *see* Tesink, K. 6 (1980) 51–59
Koke, H., Integration of personal computers with DEC VAX systems 20 (1987) 73–76
Kokubu, A., *see* Mori, R. 5 (1979) 155–165
Kolb, H.J., *see* Bolch, G. 12 (1983) 267–278
Kolody, D.I., Evaluation and debug of microprocessor systems using a reconfigurable logic state analyzer 3(3) (1977) 48–52
Kolpakov, I.F., *see* Chernykh, E.V. 20 (1987) 65–67
Kölsch, R.T., *see* Andresen, H. 4 (1978) 322–325
Komorida, K., State of microprocessor applications in an electronic equipment manufacturer 5 (1979) 166–169
Konoplev, K., *see* Koushtuev, A. 22 (1988) 151–156
Kontos, J., Syntax-directed plan recognition with a microcomputer 9 (1982) 277–280
Kopp, H., *see* Kober, R. 2(2) (1976) 56–64
Korenini, J., *see* Trobec, R. 21 (1987) 75–82
Korenini, J., *see* Trobec, R. 25 (1989) 353–358
Kornerup, P., Current work in Aarhus, Denmark, related to microprogramming 1(3) (1975) 11–13
Kornstein, H., Developments in development systems 3(3) (1977) 77–87
Korthauer, E., L. Richter, Are RISCs subsets of CISCs? A discussion of reduced versus complex instruction sets 14 (1984) 1–8
Koushtuev, A., V. Bondarenko, K. Konoplev, Voice announcement for vehicle operators 22 (1988) 151–156
Kovashki, I., *see* Nikolov, L. 21 (1987) 189–196
Koyama, S., Y. Isurugi, R. Miura, M. Sakurama, T. Sano, A realization of a DDA system for continuous dynamical system simulation with a universal multimicroprocessor system 'Harps' 3(4) (1977) 75–78
Kozlav, L.G., *see* Malinovsky, B.N. 3(1) (1977) 67–72
Kozlowski, W.E., *see* Krawczyk, H. 20 (1987) 39–44
Kozlowski, W.E., H. Krawczyk, T. Bartkowski, System diagnosability in some restricted hybrid fault situations: Comparative studies on fault diagnosis models 21 (1987) 497–504
Kranenburg, P., Communication kernels: A high level synchronization primitive supporting error handling 23 (1988) 233–238
Kraus, U.E., *see* Reijns, G.L. 21 (1987) 405–412
Krawczyk, H., W.E. Kozlowski, Fault diagnosis in distributed systems with incomplete tests 20 (1987) 39–44
Krawczyk, H., *see* Kozlowski, W.E. 21 (1987) 497–504
Kreglewski, J., *see* Brzeziński, J. 14 (1984) 195–200
Kreutzer, S.E., S.L. Hakimi, System-level fault diagnosis: A survey 20 (1987) 323–330
Kreuzer, A., *see* Chroust, G. 8 (1981) 247–256
Kreyßig, J., H. Schukat, H.Ch. Zeidler, H. Diel, H. Weber, An intelligent disk controller: A processor system for file management and query functions 25 (1989) 55–60
Krikelis, A., R.M. Lea, An associative string processor architecture for parallel processing application 24 (1988) 747–754
Kroha, P., *see* Blažek, Z. 21 (1987) 39–46

Krolak, S.T., V. Antipa, DEOS: A dynamically extendible object-oriented system 24 (1988) 241–248
Kroon, J.G.M., G.E. Houtekamer, G.L. Reijns, C.J. van Spronsen, Performance evaluation package for UNIX III systems 21 (1987) 347–356
Krstajić, P.J., *see* Lazić, D. 16 (1985) 261–266
Krüger-Sprengel, B., *see* Matthäus, C. 24 (1988) 233–238
Krushinsky, D., *see* Bruck, K. 23 (1988) 357–358
Krzyzagorski, P., Avoiding deadlock with a dual-port memory 25 (1989) 139–142
Kuchcinski, K., *see* Peng, Z. 18 (1986) 335–340
Kuchcinski, K., Z. Peng, Parallelism extraction from sequential programs for VLSI applications 23 (1988) 87–92
Kückendahl, P., *see* Rauscher, R. 24 (1988) 527–534
Kung, L.Y., *see* Kuo, Y.H. 21 (1987) 647–654
Kung, Ling-Yeung, *see* Kuo, Yau-Hwang 23 (1988) 21–26
Kung, Ling-Yeung, *see* Kuo, Yau-Hwang 23 (1988) 27–32
Kuo, S., *see* Wada, K. 21 (1987) 275–282
Kuo, Y.H., J.P. Hsu, L.Y. Kung, A graph-based algorithm for the automated datapath synthesis of ASM 21 (1987) 647–654
Kuo, Yau-Hwang, Being-Yuh Hwang, Ling-Yeung Kung, Control store minimization on the design of microprogrammed controllers 23 (1988) 21–26
Kuo, Yau-Hwang, Ruey-Rong Wang, Ling-Yeung Kung, Logic design using the PLAs with limited I/O pins and product terms 23 (1988) 27–32
Kuo Pan, S.M., Y. Kaneda, A table oriented database-prolog system 25 (1989) 9–14
Kurmann, H., B. Lent, R. Marti, Declarative programming of the embedded control systems based on or-ed dataflow operational principle 24 (1988) 663–672
Kuznia, Ch., *see* Kober, R. 2(2) (1976) 56–64
Kuznia, Ch., SMS 201: A powerful parallel processor with 128 microcomputers 5 (1979) 48–52
Kwan, C.L., *see* Michel, C. 4 (1978) 167–180
Kwee, E., *see* Grossmann, B. 12 (1983) 185–192

La Manna, M., L. Simoncini, An implementation of optimistic policy for concurrency control in a computer network for real-time applications 14 (1984) 9–16
Laface, P., *see* Garetti, P. 9 (1982) 201–214
Lagasse, J.P., *see* Beaufils, R. 1(4) (1975) 19–25
Lagemann, K., *see* Buschke, R. 21 (1987) 471–478
Lagemann, K., *see* Buschke, R. 24 (1988) 461–468
Lagier, M.L., R. Husson, A new architecture for process control computers: Narcis project 2(4) (1976) 34–39
Lahti, J.A., *see* Kivelae, J.E. 18 (1986) 631–635
Lam, H.C.P., *see* Ng, K.W. 23 (1988) 267–272
Lamma, E., *see* Cremonini, R. 21 (1987) 531–538
Lane, J.H., R.A. Niederland, T.L. Rasset, W.A. Geideman, GaAs implementation of a 32-bit RISC 21 (1987) 31–38
Lang, T., M. Valero, M-users B-servers arbiter for multiple-busses multiprocessors 10 (1982) 11–18
Langendoerfer, H., R. Cordes, R. Buck-Emden, Multimedia filing & retrieval based on optical and magnetic mass-storage technologies 18 (1986) 505–512
Lantis, B., *see* Arato, P. 3(1) (1977) 34–48
Larsen, D.G., *see* Rony, P.R. 3(2) (1977) 57–62
Larsen, S., J. Molgaard, Status report on the use of microprocessors in the Danish electronic industry 5 (1979) 120
Larsson, T., Semantics of a hardware specification language 18 (1986) 637–643

Lataire, P., R. Meyers, G. Maggetto, J.L. van Eck, Automatic test unit for electrical batteries 5 (1979) 380–383
Lawitzky, G., *see* Endriss, H. 23 (1988) 75–80
Lawrenz, W., Controller network development: But how?! 21 (1987) 111–118
Lawson Jr., H.W., Microprogramming activities in Sweden 1(2) (1975) 15–42
Lawson Jr., H.W., Additional microprogramming activities in Sweden 1(3) (1975) 16
Lawson Jr., H.W., *see* Carlstedt, G. 4 (1978) 239–242
Lawson Jr., H.W., *see* Berndt, H. 4 (1978) 293–296
Lawson Jr., H.W., *see* Berndt, H. 4 (1978) 338–339
Lawson Jr., H.W., Activities in microprocessing and microprogramming in Sweden: A status report 5 (1979) 138–142
Lawson Jr., H.W., An approach to improving computer literacy 9 (1982) 123–132
Lawson Jr., H.W., Some future directions in computer education 14 (1984) 249–250
Lazarev, V.G., *see* Doniants, V.N. 16 (1985) 101–106
Lazarev, V.G., *see* Doniants, V.N. 25 (1989) 113–118
Lazić, D., P.J. Krstajić, Z. Kasaš, Weighing multiplexing in data acquisition systems 16 (1985) 261–266
Lazzerini, B., C.A. Prete, DISDEB: An interactive high-level debugging system for a multi-microprocessor system 18 (1986) 401–408
Le Gars, J., Efficient manufacturing of complex digital boards through the use of a low-cost universal tester 5 (1979) 38–39
Le Roch, M.F., *see* Girault, C. 2(4) (1976) 19–28
Lea, R.M., *see* Krikelis, A. 24 (1988) 747–754
Lebee, P., K.M. Hou, M. Guillemont, G. Fontenier, A new machine architecture for distributed operating systems 22 (1988) 187–204
Lecciso, R., S. Mainetti, S. Morasca, Software metrics: A critical evaluation and an application to Pascal 18 (1986) 605–615
Lecouffe, M.-P., *see* Goncalves, G. 23 (1988) 149–152
Lecouffe, M.-P., *see* Devesa, N. 24 (1988) 613–620
Leenstra, J., *see* Spaanenburg, L. 18 (1986) 341–346
Lehmann, A., *see* Grossmann, B. 12 (1983) 185–192
Lehtinen, T., *see* Jyllilae, H. 18 (1986) 161–171
Leich, A., H. Leich, V.I. Prikhodko, M. Rudalics, A microprogrammed bit-slice arithmetics processor for fast transformation of graphical items 14 (1984) 215–222
Leich, H., F.V. Levchanovsky, V.I. Prikhodko, A multi-microprocessor based intelligent graphics terminal 12 (1983) 175–180
Leich, H., B. Naumann, V.I. Prikhodko, M. Rudalics, Realization of GKS functions on a microprogrammed display module 14 (1984) 205–210
Leich, H., *see* Leich, A. 14 (1984) 215–222
Leith, L., F. O'Shea, Microcomputer graphics in education 6 (1980) 205–208
Lemaire, B., *see* Monchaud, S. 3(2) (1977) 73–75
Lemas, M., Microcomputer digest 1(3) (1975) 44–53
Lemas, M., Microcomputer bibliography and manufacturers 1(4) (1975) 58
Lemmert, K., *see* Hartenstein, R.W. 18 (1986) 99–105
Lempp, P., Integrated computer support in the software engineering environment EPOS 18 (1986) 223–232
Lenders, P., J. Tiberghien, Debugging aids for real time microprocessor systems 4 (1978) 220–221
Leng, P.H., *see* Charlton, C.C. 13 (1984) 105–114
Leng, P.H., *see* Charlton, C.C. 22 (1988) 255–262
Lent, B., A variable priority arbiter for resource allocation in asynchronous multiprocessor systems 9 (1982) 299–308
Lent, B., Data flow driven computer for embedded control systems 19 (1987) 385–400
Lent, B., *see* Kurmann, H. 24 (1988) 663–672

León, G., J. Cea, A. de la Fuente, F. Rodriguez, Semi-automatic guided synthesis of concurrent systems specifications 21 (1987) 541–548
León, G., S. Marchena, Formal conversion between LOTOS specification and GALILEO nets 24 (1988) 483–490
Leonard, G.L., Robots and Forth 14 (1984) 165–168
Lepp, A., *see* Olman, V. 25 (1989) 143–148
Leppälä, K., Interpretive execution of program code increases software robustness in embedded computer systems 18 (1986) 63–68
Leppälä, K., Utilization of parallelism in transputer-based real-time control systems 21 (1987) 629–636
Lerallut, J.F., J. Duchene, G. Fontenier, Microprocessor-based graphic preprocessing knot 5 (1979) 375–379
Lesjak, I., *see* Trobec, R. 20 (1987) 55–58
Levchanovsky, F.V., *see* Leich, H. 12 (1983) 175–180
Leven, D., *see* Wallach, Y. 6 (1980) 237–242
Levi, P., The system architecture of data acquisition networks 5 (1979) 350–357
Levi, P., Realization of a simple serial process data highway (PDV-bus) station 6 (1980) 369–372
Levi, P., Transaction language for manufacturing control 9 (1982) 221–230
Levy, J., *see* Cohen, E.J. 24 (1988) 709–714
Lewi, J., *see* Ballieu, G. 8 (1981) 179–188
Lewi, J., *see* Duponcheel, L. 21 (1987) 231–240
Lewis, T.G., *see* Ma, R.P. 11 (1983) 277–286
Li, H., *see* Maresca, M. 24 (1988) 737–742
Li, K., *see* Van Deursen, W.P. 4 (1978) 44–46
Li, Q., *see* Rishe, N. 25 (1989) 33–38
Li, T., K.F. Smith, Exploiting equilateral-triangular architectures for VLSI systolic implementation 22 (1988) 115–124
Libri, P., *see* Cioffi, G. 18 (1986) 47–51
Lieb, H., *see* Mahler, H. 23 (1988) 33–36
Lieberherr, K.J., A two-dimensional hardware design language for VLSI 18 (1986) 131–141
Liebig, H., Microprogramming with microprocessors 12 (1983) 43–52
Liedl, J., *see* Tomann, S. 7 (1981) 185–190
Lievens, J., *see* Taeymans, J. 3(3) (1977) 14–21
Linardis, P., *see* Pombortsis, A. 18 (1986) 463–467
Linders, J.G., *see* Balakrishnan, M. 22 (1988) 217–230
Lindner, H., *see* Clemen, R. 18 (1986) 441–444
Lines, B.M., A model for assessing the performance of a local area network employing technical office protocol (TOP) as part of MAP/TOP network in a computer integrated manufacturing (CIM) research project, for the transmission of real time interactive speech 23 (1988) 199–202
Linster, C.U., *see* Bolch, G. 12 (1983) 267–278
Lioy, A., *see* Gai, S. 18 (1986) 435–440
Lioy, A., *see* Gai, S. 25 (1989) 183–187
Lipp, H.M., Logic design strategies 10 (1982) 153–162
Lippitsch, P., R. Posch, MFD2: Using serial I/O for a low cost flexible disk interface 17 (1986) 153–158
Lisca, L., *see* Annunziata, M. 18 (1986) 515–524
Lisca, L., *see* Binaghi, E. 23 (1988) 157–166
Lister, P.F., *see* Halsall, F. 6 (1980) 359–362
Llabería, J.M., *see* González, A. 24 (1988) 565–572
Lobodzinski, W., M. Orzulowski, Z. Rudolf, A multi-channel temperature controller for diffusion furnace 6 (1980) 325–329
Loeb, J.Z., D. Dack, Microprocessor workshop and educational laboratory 3(2) (1977) 51–56

Loehlein, W., *see* Clemen, R. 18 (1986) 441–444
Loewenstein, P., A. Fox, Closing the semantic gap 24 (1988) 767–772
Lohstroh, J., Devices and circuits for bipolar (V)LSI 10 (1982) 101–118
Lombardi, F., Reconfiguration in microprocessor schemes 13 (1984) 315–324
Lombardi, F., t-Diagnosability for fault tolerant parallel systems 16 (1985) 7–16
Lombardi, F., Diagnosis by comparison with faulty comparators 18 (1986) 271–274
Lombardi, F., *see* Rathael, S. 19 (1987) 49–58
Lombardi, F., *see* Huang, W.-K. 22 (1988) 277–300
Lombardi, F., D. Sciuto, Linear testability conditions for two-dimensional arrays 25 (1989) 85–90
Londeix, B., K.H. Fan, Specification using LOTOS on the test facility of a transmission system 21 (1987) 241–248
Longoni, A., *see* Docchio, F. 6 (1980) 316–320
Lopéz, C., *see* Santos, A. 22 (1988) 1–10
Lopez, G., *see* De Blasi, M. 24 (1988) 607–612
Lori, S., *see* Doniants, V.N. 14 (1984) 109–116
Lovaszi, M., *see* Renyi, I. 9 (1982) 67–76
Love, H.H., C.A. Finnila, Programming and applications of complex associative processing systems 6 (1980) 381–387
Lu, X.D., P.C. Treleaven, A special-purpose VSLI chip: A dynamic pipeline up-down counter 10 (1982) 1–10
Luchtmeyer, R.C.C., *see* Spaanenburg, L. 16 (1985) 83–88
Luk, B.L., F. Naghdy, J. Billingsley, Stochastic force sensing application in robotics 24 (1988) 419–424
Luka, B., *see* Marić, S. 23 (1988) 203–206
Luque, E., A. Ripoll, J.J. Ruzz, Dynamic microprogramming in computer architecture redefinition 6 (1980) 98–103
Luque, E., A. Ripoll, Microprogramming: A tool for vertical migration 8 (1981) 219–228
Luque, E., *see* De Blasi, M. 14 (1984) 145–148
Luque, E., *see* De Blasi, M. 15 (1985) 195–202
Luque, E., A. Sorribes, A. Ripoll, Coprocessor for real-time dynamic vertical migration 20 (1987) 197–202
Lusiani, P., DALLAS: Design Automation Language for Logic Array Structures 20 (1987) 173–177
Luttenberger, N., *see* Klar, R. 18 (1986) 195–204
Lutz, J., *see* Dal Cin, M. 20 (1987) 301–308
Luzio, F., Behavioral description of microcomputers with the aid of the PMS notation 4 (1978) 103–108
Lykos, P., *see* Rine, D. 14 (1984) 255–266

Ma, R.P., T.G. Lewis, The design of a resource allocation scheme for microcode generation 11 (1983) 277–286
Maccianti, T., *see* Zulian, F. 20 (1987) 223–226
Mackrodt, W., Considerations on language interpretation for microprocessor systems 7 (1981) 110–118
Macres, P., G. Coulouris, On the use of 'top stack sets' for the storage management of a paged stack machine 1(1) (1974) 45–52
Madan, B.B., *see* Balakrishnan, M. 17 (1986) 19–28
Madan, B.B., *see* Balakrishnan, M. 19 (1987) 305–318
Maddaleno, F., *see* Gregoretti, F. 18 (1986) 409–416
Maddaleno, F., *see* Hersch, R.D. 24 (1988) 503–510
Maderna, A., N. Scarabottolo, A. Daprà, Software of a modular general-purpose controller designed for industrial processes 6 (1980) 399–402
Maderna, A., *see* Distante, F. 16 (1985) 107–112
Maderna, A., *see* Distante, F. 18 (1986) 363–370

Maekawa, M., K. Sakamura, A. Tanaka, C. Ishikawa, Firmware realization of an operating system 8 (1981) 189–202
Maekawa, M., Multiwindow screens without window overlapping 18 (1986) 539–546
Maekawa, S., *see* Wada, K. 21 (1987) 275–282
Maggetto, G., *see* Lataire, P. 5 (1979) 380–383
Maggiolini, P., *see* Ciborra, C. 4 (1978) 232–238
Magnenat-Thalmann, N., *see* Faquet, G. 9 (1982) 17–26
Magnusson, B.-G., Impact of microprocessors on telecommunication systems 6 (1980) 11–16
Mahler, H., M. Albert, U.G. Baitinger, PICASSO: An Experimental CAD Tool for the Parametrization of PLA Control Structures 20 (1987) 179–182
Mahler, H., H. Lieb, U.G. Baitinger, Processor control part synthesis using effective partitioning algorithms 23 (1988) 33–36
Mahr, W., R. Patzelt, Improvements of the multiprocessing capabilities of microprocessor busses 4 (1978) 207–219
Mainetti, S., *see* Lecciso, R. 18 (1986) 605–615
Majumdar, A.K., *see* Upendra Rao, S. 17 (1986) 29–40
Majumdar, A.K., *see* Upendra Rao, S. 22 (1988) 159–174
Majumdar, A.K., *see* Balakrishnan, M. 22 (1988) 217–230
Maksimović, T., *see* Vlajnić, M. 21 (1987) 657–664
Malbaša, V., *see* Malbaški, D. 16 (1985) 295–300
Malbaški, D., V. Malbaša, D. Obradović, Analysis and practical considerations of an improved multimicroprocessor system 16 (1985) 295–300
Malek, M., K.H. Yau, Cost-effective error detection codes in multicomputer networks 20 (1987) 331–343
Malhotra, P., R.E. Seviora, Knowledge bases for digital circuit understanding 25 (1989) 15–20
Malinovsky, B.N., V.P. Bojun, L.G. Kozlav, Real-time information processing and special-purpose microprocessors 3(1) (1977) 67–72
Manasiev, L., R. Stainov, A system service for distributed programming and execution in communication-oriented distributed operating media 23 (1988) 279–282
Manasiev, L., *see* Stainov, R. 25 (1989) 319–321
Mandado, E., J. Bernárdez, New developments in fast microprogrammable control units 14 (1984) 211–214
Mandado, E., J. Bernárdez, J. Farina, Hardware for automatic nested loop control in microprogrammable systems 20 (1987) 203–208
Mangir, T., *see* Duran, J. 20 (1987) 309–322
Maniecki, M., Universal real-time kernel 14 (1984) 161–164
Männer, R., W. Schneider, B. de Luigi, K. Posner, A general-purpose multi-micro-system with high fault-tolerance and unlimited system capacity 6 (1980) 388–390
Manning, P.D., *see* Purvis, A. 25 (1989) 271–276
Mantica, P., *see* Annunziata, M. 17 (1986) 285–296
Mantoani, L., *see* Alderighi, M. 23 (1988) 95–102
Mantoani, L., *see* Alderighi, M. 25 (1989) 367–373
Manwaring, M.L., *see* Sridhar, R. 18 (1986) 263–268
Mapps, D.J., *see* Donnelly, T. 23 (1988) 333–338
Mapps, D.J., *see* Jackson, T.J. 25 (1989) 281–285
Marce, L., *see* Jullière, M. 6 (1980) 111–114
Marcelli, A., *see* Grimaldi, P. 25 (1989) 27–32
Marchena, S., *see* León, G. 24 (1988) 483–490
Mardapittas, C., *see* Guiheux, H. 24 (1988) 425–432
Maresca, M., *see* Giordano, A. 19 (1987) 5–18
Maresca, M., H. Li, A VLSI implementation of polymorphic-torus architecture 24 (1988) 737–742

Maresca, P., *see* Cantone, G. 18 (1986) 319–331
Mariani, J., *see* Hutchison, D. 13 (1984) 97–104
Marić, S., P. Drinić, R. Čajavec, B. Luka, Microprocessor real-time emulation 23 (1988) 203–206
Marietti, P., *see* Ciccarella, G. 22 (1988) 353–366
Marinescu, M., *see* Courtois, B. 7 (1981) 312–325
Marinos, L., *see* Papazoglou, M.P. 23 (1988) 311–316
Marinos, L., M.P. Papazoglou, Enhancing intermodel transformations in a distributed object oriented database system 24 (1988) 249–256
Marinos, P., *see* Kanopoulos, N. 24 (1988) 717–722
Maritsas, D.G., *see* Arvillias, A.C. 6 (1980) 161–167
Maritsas, D.G., *see* Papazoglou, M.P. 15 (1985) 179–190
Marko, R.A., *see* Cohen, E.J. 24 (1988) 709–714
Marks, K.M., K.F. Goser, An expert system shell for standardization of VLSI process data base and knowledge base 21 (1987) 523–530
Marlin Jr., J.W., J.F. Niss, Microcomputer management of instruction: The advanced learning system 11 (1983) 117–126
Marola, G., M. Schiaffino, A microprocessor-based device for traffic investigation 6 (1980) 304–307
Mars, P., *see* Nichols, S.J. 25 (1989) 327–331
Marsan, M.A., F. Gregoretti, Memory interference models for a multi-microprocessor system with a shared bus and a single external common memory 7 (1981) 124–133
Martella, G., *see* Gobbi, G. 7 (1981) 104–109
Marti, R., *see* Kurmann, H. 24 (1988) 663–672
Martin, J.D.T., *see* Beer, M.D. 21 (1987) 295–300
Martin, P., *see* Mermet, J. 1(1) (1974) 35–43
Martinelli, P., A. Torsoli, Prospects of the microprocessor in the biomedical instrumentation field 4 (1978) 318–321
Martínez, J.A., *see* Fernández, C. 12 (1983) 245–248
Martins, J.S.B., G. Noguez, A Unix distributed application support suitable for mini and microcomputer based systems 21 (1987) 205–210
Marwedel, P., An algorithm for the synthesis of processor structures from behavioural specifications 18 (1986) 251–262
Masada, E., Y. Kida, M. Tamura, Algorithmic consideration in designing a microcontroller 3(4) (1977) 71–74
Mason, A.G., *see* Whitehead, D.G. 16 (1985) 331–334
Mason, J.S., *see* Dagless, E.L. 3(2) (1977) 46–50
Masud, M., *see* Al-Rabeh, A.H. 17 (1986) 79–88
Mattasoglio, A., M. Zagolin, G.P. Rossi, A simple terminal concentrator as a service of a local area network 11 (1983) 295–300
Matthäus, C., B. Krüger-Sprengel, C. Glowacz, U. Hübner, H.T. Vierhaus, CMOS fault modeling, test generation and design for testability 24 (1988) 233–238
Mauney, J., *see* Agrawal, D.P. 24 (1988) 79–84
Mayer-Lindenberg, F., Three interactive programming languages for microcomputers 13 (1984) 31–40
Mayer-Lindenberg, F., An interactive programming environment for personal microsystems supporting distributed processing 13 (1984) 255–266
Mayer-Lindenberg, F., Some applications of the Fifth programming environment 14 (1984) 169–172
Mayer-Lindenberg, F., Throughput simulation for a Fifth system employing several similar processing elements 16 (1985) 227–230
Mayer-Lindenberg, F., FIFTH on the transputer 19 (1987) 367–374

Mayer-Lindenberg, F., Practical multiprocessor and realtime programming in the Fifth programming environment 24 (1988) 73–78
Mayne, K.D., J.E. Pache, Microprocessor applications bibliography 2(4) (1976) 72–100
McCluskey, E.J., Comparing causes of system failures 18 (1986) 9–22
McDonald, J., Pro-log microprocessor design course 3(2) (1977) 28–30
McKendry, M.S., The use of monitors in microprocesssor software development 4 (1978) 257–264
Medina, D., V. Milutinović, An overview and a selected bibliography of the VLSI/microprocessor-based data modem design 16 (1985) 143–162
Melgara, M., M. Paolini, R. Roncella, An algorithm for asynchronous NMOS/CMOS network analysis in a CAD tool for physical fault simulation 14 (1984) 117–124
Mello, P., *see* Cremonini, R. 21 (1987) 531–538
Mendez Rodriguez, J.A., *see* Sanchez Rodriguez, M.J. 16 (1985) 163–168
Menu, J., E. Sanchez, P. Sommer, HIP, a general heap processor 20 (1987) 217–222
Meriaux, M., A cellular architecture for image synthesis 13 (1984) 179–188
Mermet, J., P. Martin, A. Guyot, A microprogram metacompiler 1(1) (1974) 35–43
Meyers, R., *see* Lataire, P. 5 (1979) 380–383
Mezzalama, M., P. Torasso, A micro-processor executive for real-time process control 4 (1978) 109–114
Mezzalama, M., P. Prinetto, A strategy for simulating bit-slice based microprogrammable systems 7 (1981) 334–343
Mezzalama, M., P. Prinetto, Microprogram simulation using a structured microcode model 13 (1984) 299–314
Mezzalama, M., *see* Somenzi, F. 15 (1985) 119–128
Mezzalira, L., F.A. Schreiber, A microcomputerized interface for computer communication 1(4) (1975) 37–46
Mezzalira, L., *see* Bisiani, R. 2(2) (1976) 11–13
Mezzalira, L., R. Negrini, Some implementations at the microcomputer laboratory of Politecnico di Milano 3(3) (1977) 29–36
Mezzalira, L., *see* Cavalcoli, A. 6 (1980) 87–89
Mezzalira, L., N. Scarabottolo, Acquiring real-time information from a multitasking microcomputer: A proposed method 11 (1983) 203–206
Micarelli, A., *see* Cioffi, G. 18 (1986) 47–51
Michel, C., Hardwired logic and microprocessors 2(2) (1976) 1–4
Michel, C., Microprocessors and hardware logic 3(1) (1977) 73–80
Michel, C., C.L. Kwan, Hardwired logic and microprocessors 4 (1978) 167–180
Michel, G., *see* Dang, M. 18 (1986) 173–181
Micheli, C., *see* Bertora, F. 7 (1981) 169–176
Milenković, Č., D. Starčević, B. Mučibabić, PC-based multimedia messaging systems 21 (1987) 119–126
Miller, L.L., A microprocessor based parallel search system for fast data retrieval 22 (1988) 29–38
Miller, L.L., *see* Hurson, A.R. 22 (1988) 243–255
Milligan, P., *see* Crookes, D. 21 (1987) 559–566
Milligan, P., N.S. Scott, D. Crookes, P.L. Kilpatrick, P.J. Morrow, Network topology: A critical factor in the implementation of algorithms intended for efficient execution on a transputer network 23 (1988) 253–258
Milligan, P., *see* Scott, N.S. 24 (1988) 535–540
Milligan, P., *see* Scott, N.S. 25 (1989) 247–251
Milutinović, V., MOS microprocessor-based medium-speed data modem 5 (1979) 100–103
Milutinović, V., K. Waldschmidt, A high-level language architecture for time-critical dedicated microprocessing 12 (1983) 33–42
Milutinović, V., A high level language architecture: Bit-slice-based processor and associated system software 12 (1983) 143–152

Milutinović, V., A 4800 bit/s microprocessor-based CCITT compatible data modem 15 (1985) 57–74
Milutinović, V., *see* Medina, D. 16 (1985) 143–162
Milutinović, V., *see* Kerin, K. 17 (1986) 119–128
Mínguez, F., *see* Castillo, A. 12 (1983) 207–210
Minotti, M., *see* Distante, F. 16 (1985) 107–112
Minotti, M., *see* Distante, F. 18 (1986) 363–370
Mir, M., M.H. Imam, Optimal placement for hierarchical VLSI 25 (1989) 177–182
Mirabel, M., Microprocessors and instrumentation for intensive care units and surgical halls 4 (1978) 310–313
Miskolczi, J., *see* Ambrozy, G. 4 (1978) 92–102
Mital, Z., CDDT: Concurrent system operation display and debugging tool 23 (1988) 173–178
Miura, R., *see* Koyama, S. 3(4) (1977) 75–78
Miyamoto, M., *see* Wada, K. 21 (1987) 275–282
Mladenovic, Z., *see* Vlajnić, M. 25 (1989) 47–50
Mok, K.Y., *see* Ng, K.W. 19 (1987) 203–218
Mok, K.Y., *see* Ng, K.W. 23 (1988) 267–272
Molgaard, J., *see* Larsen, S. 5 (1979) 120
Molgaard, J., Keynote address: 'Impact of computer utilization upon the world's labor force by K. Nygaard' 5 (1979) 203–205
Molitor, P., Layer assignment by simulated annealing 16 (1985) 345–350
Monchaud, S., J.F. Pedenon, B. Lemaire, Behaviour display system for a microprogrammed processor 3(2) (1977) 73–75
Monchaud, S., R. Prat, A low-cost microprogram development system loader and tester 5 (1979) 225–234
Monchaud, S., Interfacing microprocessors and simple sensors in robotics 10 (1982) 341–344
Monchaud, S., A. Palotási, R. Prat, Microprocessor tasks for a laser range finding peripheral 19 (1987) 193–202
Monchaud, S., *see* Shi, J. 23 (1988) 339–344
Monk, J., The microprocessor application group in the U.K. 2(3) (1976) 62
Monrad-Krohn, L., Report on recent microcomputer developments in Norway 2(2) (1976) 53–55
Monrad-Kronh, L., Design philosophy of mycro-1 1(3) (1975) 30–34
Montuschi, P., *see* Valenzano, A. 25 (1989) 265–270
Moon, S.C., *see* Shin, D.C. 25 (1989) 233–238
Moon, Yiu-Sang, Wing-Hong Cheung, Design and implementation of a distributed processing environment with personal computers connected to a time-sharing system 15 (1985) 85–94
Moons, H., P. Verbaeten, Software development in server-oriented systems: The HERMIX approach 24 (1988) 179–186
Morasca, S., *see* Lecciso, R. 18 (1986) 605–615
Morasso, P., V. Tagliasco, Microprocessors and bioengineering 4 (1978) 305–309
Mori, R., I. Morishita, Y. Kita, Y.Okada, Microcomputer applications 3(4) (1977) 40–49
Mori, R., H. Tajima, M. Tajima, Y. Okada, Microprocessors in Japan 3(4) (1977) 50–57
Mori, R., M. Tajima, Y. Okada, H. Tajima, Japanese microprocessors: 1978 5 (1979) 150–154
Mori, R., Y. Kita, I. Morishita, A. Tojoo, A. Kokubu, K. Ohkawa, S. Nishikawa, Y. Okada, S. Uchida, Microprocessor applications in Japan 5 (1979) 155–165
Morishita, I., *see* Mori, R. 3(4) (1977) 40–49
Morishita, I., *see* Mori, R. 5 (1979) 155–165
Morrison, I.W., A. Burns, The design and prototype implementation of a 'structure attribute' model for tool interfacing within an IPSE 18 (1986) 233–240
Morrow, P.J., *see* Crookes, D. 21 (1987) 559–566
Morrow, P.J., *see* Milligan, P. 23 (1988) 253–258

Morrow, P.J., *see* Scott, N.S. 24 (1988) 535–540
Morrow, P.J., *see* Scott, N.S. 25 (1989) 247–251
Mortensen, B.G., Use of concurrent Pascal in industrial systems programming 14 (1984) 155–160
Mučibabić, B., *see* Milenković, Č. 21 (1987) 119–126
Mueller, R.A., J. Varghese, Applying algebraic simulation to machine-independent microcode synthesis 11 (1983) 107–116
Mühlbacher, J., *see* Chroust, G. 5 (1979) 65–72
Mühlbacher, J.R., *see* Duncan, F.G. 6 (1980) 44–50
Mühlbacher, J.R., *see* Duncan, F.G. 6 (1980) 78–80
Mühlemann, K., Software model of the M6300 microprocessor 3(2) (1977) 76–79
Mündemann, F., W. Hahn, Algorithmic specification of Mofset's for multi-level computer design languages 16 (1985) 67–72
Muñoz, E., *see* Santos, A. 22 (1988) 1–10
Murray, K.A., A.J. Wellings, Issues in the design and implementation of a distributed operating system for a network of transputers 24 (1988) 169–178
Musetti, A., Microprocessors and instrumentation for chemical pathology 4 (1978) 314–317
Musstopf, G., *see* Berndt, H. 4 (1978) 293–296
Musstopf, G., *see* Berndt, H. 4 (1978) 338–339
Myhrhaug, B., Plug-in subroutines for microcomputers 1(3) (1975) 10

Nagel, K., *see* Kober, R. 5 (1979) 53–54
Naghdy, F., G. Naghdy, D. Smith, J. Billingsley, Parallel control of a waste-water treatment plant using a real-time multi-tasking operating system 21 (1987) 603–612
Naghdy, F., *see* Luk, B.L. 24 (1988) 419–424
Naghdy, G., *see* Naghdy, F. 21 (1987) 603–612
Nakata, I., *see* Watanabe, M. 3(4) (1977) 83–85
Nanninga, G.J., *see* Van de Goor, A.J. 24 (1988) 325–334
Napiatek, H.J., *see* Gorski, J. 21 (1987) 213–222
Narraway, J.J., Probability machines 25 (1989) 197–201
Natali, A., *see* Corradi, A. 12 (1983) 159–166
Naumann, B., *see* Leich, H. 14 (1984) 205–210
Naur, P., Programming as theory building 15 (1985) 253–262
Navabi, Z., Generating gate level two phase dynamic MOS logic from AHPL 16 (1985) 89–94
Navabi, Z., K. Doroudi, Compiling an RT level hardware description language into layout of NMOS cells 18 (1986) 123–129
Navaux, P., P. Fernandes, M. Tazza, SARA: A processor interconnection performance analysis tool 24 (1988) 197–204
Nave, R., Implementation of transcedental functions on a numerics processor 11 (1983) 221–226
Nebel, W., *see* Blödel, J. 23 (1988) 15–20
Nedic, S., M. Vlajnić, A. Purkovic, Real time microprocessor based signal processing in modem 4800 BPS 16 (1985) 169–174
Nedic, S., *see* Vlajnić, M. 25 (1989) 47–50
Neelamkavil, F., Random number generation on microcomputers: State of the art 22 (1988) 147–150
Negrini, R., *see* Mezzalira, L. 3(3) (1977) 29–36
Negrini, R., *see* Antola, A. 18 (1986) 275–288
Negrini, R., *see* Bruschi, O. 20 (1987) 133–140
Nemec, J., *see* Hoffman, H. 3(3) (1977) 53–59
Neri, F., *see* Gai, S. 18 (1986) 435–440
Neri, F., *see* Ajmone Marsan, M. 18 (1986) 469–477

Neri, G., *see* Faldella, E. 5 (1979) 285–294
Ng, Chi-ho, A proposal for using a microprocessor as a digital test system controller 4 (1978) 162–166
Ng, K.W., An intelligent CRT terminal for Chinese characters 8 (1981) 23–32
Ng, K.W., K.Y. Mok, The high level language and operating system support features of advanced microprocessors: Part I: High level language support features 19 (1987) 203–218
Ng, K.W., The high level language and operating system support features of advanced microprocessors. Part II: Operating system support features 19 (1987) 277–290
Ng, K.W., DISCDA: A digital system controller design assistant 23 (1988) 57–62
Ng, K.W., H.C.P. Lam, K.Y. Mok, A microprogrammed machine for the concurrent euclid language 23 (1988) 267–272
Nguyen, S.H., *see* Bourbakis, N.G. 25 (1989) 341–346
Ni, P.N., K. Huang, Parallel testing for system diagnosis and a scheduling algorithm 20 (1987) 283–286
Niar, S., *see* Goncalves, G. 23 (1988) 149–152
Nichols, S.J., R.T. Clarke, P. Mars, Design of a high speed simulation tool for WAN using parallel processing 25 (1989) 327–331
Nicks, C., *see* Hersch, R.D. 24 (1988) 503–510
Nicola, E., *see* Ciminiera, L. 20 (1987) 59–64
Nicolaidis, M., Evaluation of a self-checking version of the MC 68000 microprocessor 20 (1987) 235–247
Nicoud, J.D., Hardware standards for $\mu$P and peripherals 1(3) (1975) 3–7
Nicoud, J.D., Common instruction mnemonics for microprocessors 1(3) (1975) 22–29
Nicoud, J.D., Linking memories to microprocessors 2(1) (1976) 5–9
Nicoud, J.D., Microprocessor education 3(2) (1977) 3–4
Nicoud, J.D., Equipment for a microprocessor laboratory 3(2) (1977) 69–72
Niederland, R.A., *see* Lane, J.H. 21 (1987) 31–38
Niemelä, U., T. Keikkilä, P. Kärkkäinen, Integrated sensors to an assembly robot using a distributed control network 21 (1987) 427–432
Nieuwenhuis, L.J.M., Mirtex: A real-time, multi-tasking executive for microprocessors 12 (1983) 193–198
Nikkilae, S., *see* Jyllilae, H. 18 (1986) 161–171
Nikolov, L., *see* Yankov, B. 16 (1985) 221–226
Nikolov, L., I. Kovashki, Design and implementation of a portable kernel for real-time applications 21 (1987) 189–196
Nilsson, S.A., W. Escher, An editing and file handling system based on microcomputer with a floppy disc 2(3) (1976) 54–61
Nishikawa, S., *see* Mori, R. 5 (1979) 155–165
Niss, J.F., *see* Marlin Jr., J.W. 11 (1983) 117–126
Noguez, G., *see* Martins, J.S.B. 21 (1987) 205–210
Nonn, P., *see* Husson, R. 3(2) (1977) 39–45
Novak, F., L. Gyergyek, Distributed system diagnosability based on self-testing system nodes 21 (1987) 489–496
Novak, F., S. Klavžar, L. Gyergyek, On system diagnosis for transient fault situations 22 (1988) 273–276
Nuñez, A., A survey of GaAs computer designs 21 (1987) 665–670
Nuñez, A., R. Sarmiento, P.P. Carballo, Some results in GaAs processor design using LSI integrated circuits 25 (1989) 127–132
Nuñez, M.A., *see* Carracedo, J. 12 (1983) 217–222
Nunez Ordonez, A., *see* Sanchez Rodriguez, M.J. 16 (1985) 163–168
Nussey, I., New opportunities in manufacturing 21 (1987) 3–4
Nyssen, M., *see* Taeymans, J. 3(3) (1977) 14–21

Nyssen, M., L. Bousse, A. Broekaert, A Mubus DMA-interface for CCD optical arrays 5 (1979) 243–248
Nyssens, Ph., Input/output microprogram handling within the Unidata 7.720 2(3) (1976) 15–23

Oberreiter, W., A. Schuler, A. Chowdhury, Improvement of performance and efficiency of industrial process control systems 5 (1979) 295–301
Obradović, D., *see* Malbaški, D. 16 (1985) 295–300
Obrebska, M., Efficiency and performance comparison of different design methodologies for control parts of microprocessors 10 (1982) 163–178
O'Connell, J.A., Computer assisted learning: Practical objectives for future projects 7 (1981) 92–96
Ogdin, J., Microprocessor scorecard 1(2) (1975) 43–77
Ohba, N., T. Saito, Y. Hoshiko, Signal processing on a data-flow processor 14 (1984) 17–28
Ohkawa, K., *see* Mori, R. 5 (1979) 155–165
Ohno, Y., *see* Gal, T. 8 (1981) 103–110
Okada, K., M. Kajiwara, Functionally distributed telephone exchange control system using microprocessors 3(4) (1977) 79–82
Okada, Y., *see* Mori, R. 3(4) (1977) 40–49
Okada, Y., *see* Mori, R. 3(4) (1977) 50–57
Okada, Y., *see* Mori, R. 5 (1979) 150–154
Okada, Y., *see* Mori, R. 5 (1979) 155–165
Okada, Y., *see* Umeyama, S. 10 (1982) 325–332
Okamoto, K., M. Isomura, Application of microprogramming in numerical machine controllers 6 (1980) 288–295
Okkonen, A., *see* Auer, A. 24 (1988) 51–56
Oklobdzija, V.G., Issues in CPU-coprocessor communication and synchronization 24 (1988) 695–700
Olman, V., R. Raud, A. Lepp, Probability based timing verification 25 (1989) 143–148
Olsen, K.A., *see* Eliassen, F. 14 (1984) 45–54
Oomen, J., A microprocessor controlled cardiac output meter 5 (1979) 265–269
Opler, A., Fourth-generation software 8 (1981) 145–148
Organick, E.I., New directions in computer architecture 5 (1979) 190–202
Ortega, M., J. Troya, Live nodes distribution in parallel branch and bound algorithms 25 (1989) 301–306
Ortega, R., Experimental evaluation of four microprocessor-based advanced control algorithms 10 (1982) 229–246
Orzulowski, M., *see* Lobodzinski, W. 6 (1980) 325–329
O'Shea, F., *see* Leith, L. 6 (1980) 205–208
Oshisanwo, A.O., *see* Dasiewicz, P. 21 (1987) 251–258
Osmany, J., O.J. Davies, A fault tolerant controller for high integrity systems 20 (1987) 287–299
Ostertag, E., Association of a 16-bit microprocessor with an optical multichannel analyzer 5 (1979) 309–314

Pache, J.E., *see* Mayne, K.D. 2(4) (1976) 72–100
Padovani, C., *see* Ciccarella, G. 22 (1988) 353–366
Page, R.M.R., *see* Reddaway, S.F. 24 (1988) 655–660
Painke, H., VLSI-/370 microprocessor overview 24 (1988) 23–28
Paleari, D., *see* Casero, E. 4 (1978) 283–292
Palotási, A., *see* Monchaud, S. 19 (1987) 193–202
Panogiotopoulos, D., N.G. Bourbakis, The VLSI design of a two-dimensional image processing array 14 (1984) 125–132
Paolini, M., *see* Melgara, M. 14 (1984) 117–124

Papa, M., *see* Borsotti, A. 9 (1982) 77–84
Papachristou, C.A., A PLA microcontroller using horizontal firmware 14 (1984) 223–230
Papachristou, C.A., D. Cornett, Generation and implementation of state machine controllers: A VLSI approach 16 (1985) 73–82
Papakonstantinou, G., F. Gritzali, A generator for Intel 8080 microcomputer data management systems 6 (1980) 226–231
Papazoglou, M.P., P.I. Georgiadis, D.G. Maritsas, A.A. Dickie, Multiprocessor interrupting and synchronizing concepts in the parallel Simula machine and their representation by Petri nets 15 (1985) 179–190
Papazoglou, M.P., P.E. Pintelas, A hierarchical multimicroprocessor system for object oriented languages 19 (1987) 129–142
Papazoglou, M.P., P.E. Pintelas, A versatile kernel proposal for a multimicroprocessor system environment 22 (1988) 11–22
Papazoglou, M.P., A portable query language for small scale systems 23 (1988) 299–304
Papazoglou, M.P., L. Marinos, Requirements for coupling small scale DBMSs in a distributed office environment 23 (1988) 311–316
Papazoglou, M.P., *see* Marinos, L. 24 (1988) 249–256
Papazoglou, M.P., A. Pawlak, W. Wrona, Multiprocessor modelling: An example of object-oriented development 25 (1989) 213–219
Papazoglou, M.P., *see* Bourbakis, N.G. 25 (1989) 341–346
Parchoux, R., P. Desgoutte, J.P. Comte, Microprocessor studies at Claude Bernard University 3(2) (1977) 31–33
Pardo, O., Y. Be'ery, SPL: A different high level language for systolic processors 25 (1989) 79–83
Pareto, L., *see* Ancona, M. 24 (1988) 85–92
Parodi, G., *see* Di Zitti, E. 25 (1989) 259–263
Paschalis, A.M., C. Halatsis, G. Philokyprou, Concurrently totally self-checking microprogram control unit with duplication of microprogram sequencer 20 (1987) 271–281
Pasman, W.J.A., Implementation of Pascal on an 8080 microcomputer 5 (1979) 363–369
Pastor, E., *see* Carracedo, J. 12 (1983) 217–222
Pataricza, A., An improved signature analysis based CPU self test 23 (1988) 167–172
Patel, M.R., *see* Behbahani, A.M. 18 (1986) 355–362
Patnaik, L.M., *see* Ghosal, D. 19 (1987) 179–192
Patzelt, R., *see* Mahr, W. 4 (1978) 207–219
Patzelt, R., Digital measurement systems, standards and future developments 6 (1980) 283–287
Pavan, M., *see* Spalla, M. 23 (1988) 379–384
Pavešić, N., *see* Ribarić, S. 22 (1988) 333–346
Pawlak, A., *see* Bakowski, P. 20 (1987) 167–171
Pawlak, A., *see* Grabowiecki, T. 23 (1988) 37–44
Pawlak, A., *see* Grabowiecki, T. 25 (1989) 157–162
Pawlak, A., *see* Papazoglou, M.P. 25 (1989) 213–219
Pawlina, A., C. Serrelli, Software mechanisms for inter-process communication in a class of multiple microprocessor systems 7 (1981) 229–240
Payer, M., Verification and validation of hierarchical CMOS gate array layouts 21 (1987) 445–452
Payer, M., *see* Rauscher, R. 24 (1988) 527–534
Pedenon, J.F., *see* Monchaud, S. 3(2) (1977) 73–75
Pedersen, B.G., Design of real-time systems: A method and a tool 24 (1988) 379–386
Peels, A.J.H.M., A model of a generalized chip structure 9 (1982) 295–298
Pellegrin, J., *see* Dewitte, J. 5 (1979) 93–99
Pellegrino, M., *see* Doniants, V.N. 14 (1984) 109–116
Peng, Z., K. Kuchcinski, Synthesis of control structures from Petri net descriptions 18 (1986) 335–340

Peng, Z., *see* Kuchcinski, K. 23 (1988) 87–92
Peng, Z., Let's design asynchronous VLSI systems 24 (1988) 347–352
Peracaula, J., Microcomputer developments in Spain: A preliminary report 1(4) (1975) 1
Perdigão, F., *see* Viera de Sá, L. 23 (1988) 221–226
Perez, J., *see* Sansonnet, J.P. 9 (1982) 143–154
Periasamu, M., M.V. Pitke, A multilanguage terminal with microprocessor control 16 (1985) 31–44
Perotto, J.-F., *see* Piguet, C. 21 (1987) 463–470
Petitpierre, D., *see* Faquet, G. 9 (1982) 17–26
Pfahler, P., Automated datapath synthesis: A compilation approach 21 (1987) 577–584
Philips, B., *see* Dierickx, W. 16 (1985) 211–214
Phillipps, A.J., *see* Davila, J.M. 23 (1988) 179–184
Philokyprou, G., *see* Paschalis, A.M. 20 (1987) 271–281
Piacentini, M., *see* Brofferio, S. 17 (1986) 139–152
Piccinini, G.L., *see* Civera, P.L. 21 (1987) 267–274
Piguet, C., *see* Ansorge, M. 18 (1986) 427–434
Piguet, C., E. Dijkstra, A. Theys, M. Stauffer, J.-F. Perotto, A design methodology of microprogrammed controllers for custom CMOS ICs 21 (1987) 463–470
Pihlgren, G., Increasing performance in a Cobol system by adding firmware 6 (1980) 403–405
Pi'il, E.I., *see* Doniants, V.N. 14 (1984) 109–116
Pinciroli, F., The use of microprocessors in biomedical applications: An overview 4 (1978) 304
Pino, J.A., Information retrieval using microcomputers 18 (1986) 623–628
Pintelas, P.E., *see* Papazoglou, M.P. 19 (1987) 129–142
Pintelas, P.E., *see* Papazoglou, M.P. 22 (1988) 11–22
Pitke, M.V., *see* Periasamu, M. 16 (1985) 31–44
Piuri, V., *see* Cattaneo, G. 15 (1985) 233–252
Piuri, V., *see* Distante, F. 18 (1986) 363–370
Piuri, V., E. Tourouta, About fault-tolerant allocation of tasks in multiprocessor architectures and system dimensioning 19 (1987) 335–349
Piuri, V., M. Berzieri, A. Bisaschi, A. Fabi, Residue arithmetic for a fault-tolerant multiplier: The choice of the best triple of bases 20 (1987) 15–23
Piuri, V., E. Tourouta, An approach to fault tolerant allocation of concurrent communicating processes in multiprocessor architectures and hardware dimensioning 21 (1987) 339–346
Piuri, V., R.Stefanelli, G. Traverso, Error detection in serial multipliers and in systolic arrays: An approach based upon A*N codes 23 (1988) 63–74
Piuri, V., Fault-tolerant hexagonal arithmetic array processors 24 (1988) 629–636
Place, H., *see* Jullière, M. 6 (1980) 111–114
Plessmann, K.W., L. Tassakos, Concurrent, object-oriented program design in real-time systems 24 (1988) 257–266
Plessmann, K.W., *see* Tassakos, L. 25 (1989) 221–227
Plotkin, A., D. Tabak, Modular, object-oriented microcomputer architecture 12 (1983) 153–158
Podlazov, V.S., *see* Prangishvili, I.V. 7 (1981) 220–228
Polillo, R., *see* Casero, E. 4 (1978) 283–292
Polzer, R., *see* Bolch, G. 12 (1983) 267–278
Pombortsis, A., P. Linardis, C. Halatsis, Study of a class of conflict resolution strategies for synchronous multiprocessor interconnection networks 18 (1986) 463–467
Pons, J.F., *see* Courtois, B. 7 (1981) 312–325
Poppensieker, J., P. Schulz, CAP-RTL: A universal microprocessor software development system 11 (1983) 217–220

Posch, R., *see* Lippitsch, P. 17 (1986) 153–158
Posner, K., *see* Männer, R. 6 (1980) 388–390
Poujoulat, G.H., The CORAIL building block system 2(4) (1976) 29–33
Poznański, Z., *see* Szynka, J. 16 (1985) 301–304
Prangishvili, I.V., G.G. Stetsyura, V.S. Podlazov, Decentralized control of processor interactions in concentrated and distributed multi-microprocessor systems 7 (1981) 220–228
Prat, R., *see* Monchaud, S. 5 (1979) 225–234
Prat, R., *see* Monchaud, S. 19 (1987) 193–202
Prat, R., *see* Shi, J. 23 (1988) 339–344
Pratt, S., *see* Coleman, M.G. 23 (1988) 305–310
Pratt, S.J., Alchemy model: An operating system model for a microcomputer-based, heterogeneous processing configuration 23 (1988) 291–298
Preece, C., *see* Wingate, G.A.S. 24 (1988) 775–782
Prete, C.A., *see* Lazzerini, B. 18 (1986) 401–408
Price, H.C., The future is a computer? 6 (1980) 81–82
Prikhodko, V.I., *see* Leich, H. 12 (1983) 175–180
Prikhodko, V.I., *see* Leich, H. 14 (1984) 205–210
Prikhodko, V.I., *see* Leich, A. 14 (1984) 215–222
Prikhodko, V.I., *see* Bruck, K. 23 (1988) 357–358
Prikhodko, V.I., *see* Bruck, K. 25 (1989) 61–64
Prinetto, P., *see* Mezzalama, M. 7 (1981) 334–343
Prinetto, P., *see* Mezzalama, M. 13 (1984) 299–314
Prinetto, P., *see* Somenzi, F. 15 (1985) 119–128
Prinetto, P., *see* Cabodi, G. 18 (1986) 39–45
Prinetto, P., *see* Cabodi, G. 18 (1986) 559–566
Pro, J., *see* Duchene, J. 4 (1978) 333–337
Prohorov, S., *see* Souček, B. 11 (1983) 23–30
Pronzato, M., *see* Ancona, M. 24 (1988) 85–92
Proserpio, A., *see* Binaghi, E. 23 (1988) 157–166
Proverbio, S., M.G. Sami, L. Troiano, Evaluation of a functional reliability model for microprocessors 8 (1981) 83–92
Puech, Ph., *see* Beaufils, R. 1(4) (1975) 19–25
Pulli, P., Embedded microcontroller operating system with state-machine support 18 (1986) 59–62
Puppo, E., *see* Ancona, M. 20 (1987) 183–188
Purkovic, A., *see* Nedic, S. 16 (1985) 169–174
Purkovic, A., *see* Vlajnić, M. 25 (1989) 47–50
Purvis, A., R. Berry, P.D. Manning, A multi-transputer based audio computer with MIDI and analogue interfaces 25 (1989) 271–276

Qu, Yan-zhen, *see* Wan, Yu-liang 18 (1986) 479–487
Quemada Vives, J., *see* Vinyes Sanz, J. 12 (1983) 111–114

Rahman, A., *see* Sadiq-Hussain, S. 10 (1982) 37–42
Rahman, M., *see* Soomro, A.A. 22 (1988) 97–114
Rainel, H., *see* Kerner, H. 18 (1986) 299–305
Raja, D., R. Shanthi, S.R. Rao, Computerised telephone metering system 23 (1988) 345–350
Rajaraman, V., *see* Siva Ram Murthy, C. 20 (1987) 113–117
Ramesh, V., *see* Rao, S.S.S.P. 13 (1984) 115–120
Rammig, F.J., M. Schrewe, G. Vorloeper, A transputer-based accelerator for multilevel digital simulation 24 (1988) 299–306
Ramos, I., J. Aramberri, Cross-macroassembler for micro MCS-80 in a HP-2116-B minicomputer 2(4) (1976) 65–71

Ramos, I., *see* Garitagoitia, J.R. 3(3) (1977) 22–28
Rana, S.P., *see* Arora, R.K. 19 (1987) 219–226
Rao, P.V.S., *see* Sanyal, S. 22 (1988) 325–332
Rao, S.R., *see* Raja, D. 23 (1988) 345–350
Rao, S.S.S.P., V. Ramesh, Micro-store optimization based on micro-operation utilisation profile 13 (1984) 115–120
Rao, S.S.S.P., J.R. Isaac, Micro-store width optimization through bit steering 16 (1985) 17–30
Rasset, T.L., *see* Lane, J.H. 21 (1987) 31–38
Rateb, M.M., M.N. Elyousfi, A statistical approach to solution of Middle-East telex network problems 6 (1980) 34–36
Rateb, M.M., M.N. Elyousfi, Automatic removal of false reversals in telex networks 6 (1980) 37–40
Rathael, S., F. Lombardi, A software testbed for the design and evaluation of distributed computer systems 19 (1987) 49–58
Raud, R., A language environment for ASIC design 24 (1988) 219–226
Raud, R., *see* Olman, V. 25 (1989) 143–148
Rauscher, R., *see* Grass, W. 20 (1987) 161–165
Rauscher, R., J. Westendorf, A system for verification of interactive microprogram transformations 23 (1988) 3–8
Rauscher, R., P. Kückendahl, M. Payer, A system for estimation of numerical stability for matrix operations in systolic arrays 24 (1988) 527–534
Ravaglia, S., *see* Bruschi, O. 20 (1987) 133–140
Ravn, A.P., Use of concurrent Pascal in systems programming teaching 10 (1982) 33–36
Raymond, J., *see* Banerji, D.K. 1(5) (1975) 25–29
Raymont, P., Intelligent interactive instructional systems 14 (1984) 267–272
Recoque, A., MITRA 15: An example of handling peripheral units by specific microprogramming 2(3) (1976) 9–14
Reddaway, S.F., R.M.R. Page, High speed data searching with a processor array 24 (1988) 655–660
Reddi, A.V., Microcomputer memory performance in a resource sharing distributed information processing environment 17 (1986) 129–138
Reddi, A.V., Shared mini/microcomputer memory performance at remote computer network nodes in large scale distributed computing systems 19 (1987) 143–152
Reichl, L., *see* Assmuth, R. 2(3) (1976) 24–40
Reignier, H., *see* Fernin, P. 24 (1988) 315–324
Reijns, G.L., C.J. van Spronsen, W. Rombaut, R. van de Burg, Workload generator for the performance analysis of some Philips minicomputers 18 (1986) 675–681
Reijns, G.L., *see* Kroon, J.G.M. 21 (1987) 347–356
Reijns, G.L., U.E. Kraus, S. Jinsi, W.C. Hildering, Compact-disk image display and handling system 21 (1987) 405–412
Reina, L., *see* Brusamolino, M. 13 (1984) 331–340
Reinikka, A., *see* Juntunen, T.T. 24 (1988) 449–454
Renyi, I., LSI technology for digital image processing 6 (1980) 232–236
Renyi, I., M. Lovaszi, Parallel picture processing using microprogrammable bit-slice microprocessors 9 (1982) 67–76
Reorda, S., *see* Cabodi, G. 18 (1986) 559–566
Repetti, A., *see* Ancona, M. 24 (1988) 85–92
Repko, M., Low-speed data communications and your micro 4 (1978) 39–43
Revilla, M.L., *see* Rivera, E. 13 (1984) 189–198
Reyneri, L., *see* Del Corso, D. 19 (1987) 153–166
Rial, A., P.G. Depledge, A general purpose fault-tolerant microcomputer system based upon single-board microcomputers 11 (1983) 31–40

Ribarić, S., N. Pavešić, Parallel character recognition system: Theory, simulation and synthesis 22 (1988) 333–346
Richard, A.A., A.C. Traub, R. Vanzetti, Real-time control of nugget formation in spot welds 6 (1980) 296–303
Richardson, C.P., *see* Egan, K. 7 (1981) 119–123
Richter, L., Microprogramming and microprocessing activities in Germany 1(4) (1975) 2–7
Richter, L., Status report on microprogramming and microprocessing activities in Germany 3(4) (1977) 19–24
Richter, L., Y. Wallach, Remarks on a real-time, master-slaves operating system 7 (1981) 304–311
Richter, L., *see* Korthauer, E. 14 (1984) 1–8
Rijks, E., J. Vermeesch, M. Gossens, J. Tiberghien, Integration of a hardware module for tracing local variables in real-time software 21 (1987) 639–646
Rijllart, A.. An MC68000 stand-alone Camac microprocessor system 12 (1983) 291–298
Rine, D., Planning computer literacy evaluations and lasting changes in introductory computer instruction programs 11 (1983) 1–14
Rine, D., P. Lykos, Applying computer science and engineering to pre-college grades (elementary and secondary) education 14 (1984) 255–266
Ripoll, A., *see* Luque, E. 6 (1980) 98–103
Ripoll, A., *see* Luque, E. 8 (1981) 219–228
Ripoll, A., *see* De Blasi, M. 15 (1985) 195–202
Ripoll, A., *see* Luque, E. 20 (1987) 197–202
Rishe, N., D. Tal, Q. Li, Architecture for a massively parallel database machine 25 (1989) 33–38
Rispoli, L.M., Microprocessors in Italian industry: State of the art and probable trends 5 (1979) 130–132
Risse, Th., *see* Dal Cin, M. 20 (1987) 301–308
Rivera, E., M.L. Revilla, A cross-impact simulation and analysis of events through time with a microcomputer 13 (1984) 189–198
Rivera, F.F., *see* Zapata, E.L. 24 (1988) 647–654
Rivoira, S., *see* Garetti, P. 9 (1982) 201–214
Robach, C., G. Saucier, C. Aléonard, Microprocessor systems testing: A review and future prospects 5 (1979) 31–37
Rodda, L., G.R. Sechi, Simulation as a design tool: Implementation of systems by means of different technologies 15 (1985) 203–212
Rodda, L., R. Savioni, G.R. Sechi, A hierarchical architecture with independent processors for real-time systems 15 (1985) 277–288
Rodriguez, F., *see* León, G. 21 (1987) 541–548
Roesner, W., The logic design language and verification environment for the VLS-/370 24 (1988) 35–42
Rojatti, P., M. Spalla, Design and implementation of a UNIX portable communication handler for a wide area network 20 (1987) 47–54
Romani, F., A pipeline architecture oriented towards efficient multitasking 2(4) (1976) 40–47
Rombaut, W., *see* Reijns, G.L. 18 (1986) 675–681
Roncella, R., *see* Melgara, M. 14 (1984) 117–124
Rony, P.R., D.G. Larsen, Teaching microcomputer interfacing to non-electrical engineers 3(2) (1977) 57–62
Rooney, M., Higher level languages: Write it once, run it on any microprocessor 6 (1980) 28–33
Rosati, M., *see* Ciciani, B. 14 (1984) 29–34
Rosello, I., A. Rubio, A mechanism for detection and recovery from transient failures in industrial controllers 15 (1985) 11–16
Rosenstiel, W., *see* Knödler, B. 18 (1986) 71–79

Rosenstiel, W., Optimizations in high level synthesis 18 (1986) 347–352
Rossi, G.P., *see* Casero, E. 4 (1978) 283–292
Rossi, G.P., *see* Mattasoglio, A. 11 (1983) 295–300
Rossi, G.P., C. Simone, A multitasking operating system with explicit treatment of recovery points 14 (1984) 55–66
Rotoloni, P., *see* De Monte, U. 6 (1980) 17–19
Roubos, J.B., *see* Van Bruchem, D.J.J. 23 (1988) 367–372
Roveda, V., A. Werbrouck, Cluster analysis with a fast microcomputer 22 (1988) 347–352
Rozman, I., *see* Welzer, T. 25 (1989) 375–380
Rubio, A., *see* Rosello, I. 15 (1985) 11–16
Rubio, A., Design of easily testable iterative systems 20 (1987) 141–146
Rudalics, M., *see* Leich, H. 14 (1984) 205–210
Rudalics, M., *see* Leich, A. 14 (1984) 215–222
Rudolf, Z., *see* Lobodzinski, W. 6 (1980) 325–329
Ruiz, L.M., *see* Fadon, E.L. 4 (1978) 133–140
Rushinek, A., S.F. Rushinek, Microcomputer application diagnostic system for identification and evaluation of user satisfaction factors of purchasing software 17 (1986) 185–194
Rushinek, S.F., *see* Rushinek, A. 17 (1986) 185–194
Ruz, J., *see* Bautista, A. 9 (1982) 27–38
Ruz, J., *see* Hermida, R. 12 (1983) 99–110
Ruzz, J.J., *see* Luque, E. 6 (1980) 98–103
Ryba, M., *see* Blödel, J. 23 (1988) 15–20
Rzehak, H., *see* Bender, H. 18 (1986) 153–159

Saatci, A., *see* Ergincan, F. 17 (1986) 159–168
Sabouni, I., *see* Dang, M. 24 (1988) 103–110
Sada, E., A small operating system for the management of sequential processes in microprocessor systems 6 (1980) 20–23
Sadiq-Hussain, S., A. Rahman, A microprocessor-based true R.M.S. meter 10 (1982) 37–42
Saeter, T., Software techniques for integrating text and graphics in VLSI CAD tools 24 (1988) 455–460
Sait, S.M., *see* Soomro, A.A. 22 (1988) 97–114
Saito, M., *see* Kato, S. 3(4) (1977) 86–87
Saito, T., *see* Ohba, N. 14 (1984) 17–28
Saito, Y., *see* Funakubo, H. 3(4) (1977) 65–70
Sakamura, K., *see* Maekawa, M. 8 (1981) 189–202
Sakowski, W., *see* Grabowiecki, T. 23 (1988) 37–44
Sakurama, M., *see* Koyama, S. 3(4) (1977) 75–78
Saletti, V., G. Adreoni, A microprocessor based protocol converter for the direct connection of a mainframe to a packet switched network 9 (1982) 263–266
Salomon, T., *see* Faldella, E. 5 (1979) 285–294
Salsano, A., *see* Fazio, G. 12 (1983) 279–284
Samarotto, R., *see* Ciminiera, L. 20 (1987) 59–64
Sami, M.G., Microprogramming and microprocessors in Italy 1(2) (1975) 10–11
Sami, M.G., Microprocessing in Italy: The national research council project for informatics: Distributed systems 5 (1979) 128–129
Sami, M.G., Reliability and self-diagnosis aspects of microprocessor controlled instrumentation systems 6 (1980) 343–345
Sami, M.G., *see* Proverbio, S. 8 (1981) 83–92
Sami, M.G., *see* Annaratone, M. 11 (1983) 195–202
Sami, M.G., *see* Gentile, G. 14 (1984) 99–108
Sami, M.G., *see* Doniants, V.N. 16 (1985) 101–106
Sami, M.G., *see* Bonzio, F.C. 21 (1987) 91–98

Sanchez, E., *see* Menu, J. 20 (1987) 217–222
Sánchez, J.M., *see* Vaquero, A. 11 (1983) 127–132
Sánchez Pérez, J.M., *see* Vaquero Sánchez, A. 5 (1979) 370–374
Sanchez Rodriguez, M.J., J.A. Mendez Rodriguez, A. Nunez Ordonez, The concentrating/diffusing system for a communications cluster controller 16 (1985) 163–168
Sandberg, U., T. Cegrell, Computer systems for real-time process control and supervision: A framework for education based on a systems engineering approach 24 (1988) 829–834
Sandler, M.B., S. Eghtesadi, Transputer based implementations of the Hough transform for computer vision 24 (1988) 403–408
Sano, T., *see* Koyama, S. 3(4) (1977) 75–78
Sansoè, C., *see* Del Corso, D. 19 (1987) 153–166
Sansonnet, J.P., *see* Beaufils, R. 4 (1978) 275–282
Sansonnet, J.P., D. Botella, J. Perez, Function distribution in a list-directed architecture 9 (1982) 143–154
Santos, A., C. Lopéz, E. Muñoz, A text-to-multivoice convertor for Spanish: Real time operation analysis 22 (1988) 1–10
Sanyal, S., P.V.S. Rao, Design of a high reliability self diagnosing computer using bit slice microprocessors 22 (1988) 325–332
Sapiecha, K., D. Banaszek, R.Jarocki, Backward error recovery with dynamic alternate selection 16 (1985) 217–220
Sapiecha, K., T. Czichon, Test generation for circuits described in procedural hardware description languages (HDLs) 18 (1986) 371–379
Sarmiento, R., *see* Nuñez, A. 25 (1989) 127–132
Saucier, G., *see* Robach, C. 5 (1979) 31–37
Saukkonen, S., M. Karppinen, P. Kemppainen, CAMP: An environment for efficient microprocessor software production 9 (1982) 319–324
Savioni, R., *see* Rodda, L. 15 (1985) 277–288
Savvides, I.N., *see* Bourbakis, N.G. 25 (1989) 163–170
Sawin III, D.H., Microprocessor systems 1(4) (1975) 26–36
Säynäjäkangas, S., *see* Kivelae, J.E. 18 (1986) 631–635
Scaini, P., *see* Brioschi, A. 2(2) (1976) 21–37
Scarabottolo, N., *see* Maderna, A. 6 (1980) 399–402
Scarabottolo, N., A. Bacci, M. Bedina, F. Distante, Implementation guidelines of a modular general-purpose multi-microcomputer 9 (1982) 309–314
Scarabottolo, N., *see* Mezzalira, L. 11 (1983) 203–206
Scarabottolo, N., *see* Cattaneo, G. 15 (1985) 233–252
Scarabottolo, N., *see* Antola, A. 17 (1986) 205–218
Scarabottolo, N., *see* Antola, A. 18 (1986) 275–288
Scarabottolo, N., *see* Antola, A. 20 (1987) 3–9
Schaal, H., *see* Assmuth, R. 2(3) (1976) 24–40
Schallenberger, B., *see* Endriss, H. 23 (1988) 75–80
Schatorjé, A.H.J., *see* Bruinshorst, B.T.J. 6 (1980) 41–43
Schauer, H., G. Chroust, Microprogramming and microprocessing in Austria 3(4) (1977) 5–9
Schauer, H., Micropascal: A portable language processor for programming education 5 (1979) 89–92
Schauer, H., K.P. Judmann, G. Chroust, Microprocessing and microprogramming in Austria 5 (1979) 113–117
Schecher, H., H. Anlauff, Avoiding gaps in a three-level pipelined micro-control-unit 9 (1982) 155–160
Scheel, A., *see* Eicher, W. 22 (1988) 379–384
Schellingerhout, B., *see* Van Bruchem, D.J.J. 23 (1988) 367–372
Schettler, H., VLSI-/370 microprocessor chip technology 24 (1988) 29–34

Schiaffino, M., *see* Marola, G. 6 (1980) 304–307
Schindel, U., U. Jäger, D. Wolff, Modeling and simulation 10 (1982) 139–152
Schmitt, F.J., Module design for VLSI: Heuristics to optimize layout topology 20 (1987) 189–193
Schmitt, F.J., A heuristic approach to analyzing VLSI modules 23 (1988) 51–56
Schneider, K., Explanatory note on the Motorola MC 6820 PIA device 1(5) (1975) 46–49
Schneider, K., Development tools for bit-slice microprocessors 4 (1978) 14–17
Schneider, W., *see* Männer, R. 6 (1980) 388–390
Scholten, J., J. Hofstede, G.J.M. Smit, Proposal for an architecture for TUMULT based on a serial data link 21 (1987) 613–620
Schomberg, H., Image processing on a transputer-based perfect shuffle machine 25 (1989) 277–280
Schotsman, G.J., Employment and microelectronics 7 (1981) 291–303
Schreiber, F.A., *see* Mezzalira, L. 1(4) (1975) 37–46
Schrewe, M., *see* Rammig, F.J. 24 (1988) 299–306
Schröder, G., *see* Bemmerl, T. 21 (1987) 181–188
Schrott, G., A generalized task concept for multiprocessor real-time systems 20 (1987) 85–90
Schukat, H., *see* Kreyßig, J. 25 (1989) 55–60
Schuler, A., *see* Oberreiter, W. 5 (1979) 295–301
Schülke, G., *see* Adler, H.D. 6 (1980) 308–310
Schulz, K.-J., Design and implementation of a portable communication transport system for Unix network applications in C++ 25 (1989) 323–326
Schulz, P., *see* Poppensieker, J. 11 (1983) 217–220
Schulz, U., Hierarchical physical design system for VLSI-/370 microprocessor 24 (1988) 131–136
Schumacher, R., *see* Adler, H.D. 6 (1980) 308–310
Schumny, H., Measurement and control: Survey and actual example 6 (1980) 281–282
Schüssler, W., *see* Bolch, G. 12 (1983) 267–278
Schuster, B., *see* Hahn, W. 24 (1988) 291–298
Schutte, H., Memory interface of the 2650 microprocessor 3(3) (1977) 68–76
Schwarz, M., Governmental responsibility for technology: Microelectronics policies in Western Europe 8 (1981) 67–82
Schweinzer, H., G. Chroust, Symposium 'Microelectronics 75' 2(2) (1976) 66–67
Schweinzer, H., *see* Flego, D. 21 (1987) 325–330
Schweinzer, H., Functional programming of a robot motion control 21 (1987) 419–426
Scirè, G., *see* Brofferio, S. 1(4) (1975) 13–18
Scirè, G., *see* De Monte, U. 6 (1980) 17–19
Scirè, G., *see* Brofferio, S. 6 (1980) 115–119
Scirè, G., *see* Brofferio, S. 8 (1981) 93–102
Sciuto, D., A reconfiguration algorithm for delay minimization in VLSI/WSI array processors 20 (1987) 127–132
Sciuto, D., *see* Jervis, L. 21 (1987) 83–90
Sciuto, D., *see* Distante, F. 24 (1988) 63–70
Sciuto, D., *see* Lombardi, F. 25 (1989) 85–90
Scobie, J., A computer-based multi-scaler system 8 (1981) 11–14
Scott, N.S., *see* Crookes, D. 21 (1987) 559–566
Scott, N.S., *see* Milligan, P. 23 (1988) 253–258
Scott, N.S., L.C. Waring, P. Milligan, D. Crookes, P.L. Kilpatrick, P.J. Morrow, A comparison of programming paradigms for the parallel computation of Racah coefficients: An application of transputers to computational atomic physics 24 (1988) 535–540
Scott, N.S., D. Crookes, P. Milligan, P.L. Kilpatrick, P.J. Morrow, A case study in improving programming productivity on transputer networks 25 (1989) 247–251

Sebbel, H., Input/output microprogramming for the 7.755 central processing unit of Siemens system 7.000 2(3) (1976) 47–53
Sechi, G.R., *see* Rodda, L. 15 (1985) 203–212
Sechi, G.R., *see* Rodda, L. 15 (1985) 277–288
Sechi, G.R., *see* Annunziata, M. 17 (1986) 285–296
Sechi, G.R., *see* Annunziata, M. 18 (1986) 515–524
Sechi, G.R., *see* Alderighi, M. 23 (1988) 95–102
Sechi, G.R., *see* Binaghi, E. 23 (1988) 157–166
Sechi, G.R., *see* Binaghi, E. 25 (1989) 287–292
Sechi, G.R., *see* Alderighi, M. 25 (1989) 367–373
Sedlak, H., U. Golze, An RSA cryptography processor 18 (1986) 583–590
Seed, N.L., *see* Houghton, A.D. 24 (1988) 309–314
Sega, R., *see* Tasic, J. 25 (1989) 107–112
Segall, Z., *see* Barzilai, Z. 6 (1980) 149–157
Segall, Z., *see* Gregoretti, F. 19 (1987) 85–100
Segers, M.T.M., *see* Stevens, M.P.J. 1(5) (1975) 30–39
Sekiya, T., *see* Kato, S. 3(4) (1977) 86–87
Senadji, M., *see* Hou, K.M. 19 (1987) 401–406
Seppänen, V., *see* Auer, A. 24 (1988) 51–56
Serio, F., *see* Antoy, S. 4 (1978) 222–226
Serra, A., *see* Arghiracopulos, S. 6 (1980) 336–339
Serrelli, C., 8 x 300 microprocessor-software development tools: A realization 4 (1978) 265–274
Serrelli, C., *see* Pawlina, A. 7 (1981) 229–240
Seviora, R.E., P. Dasiewicz, A blackboard based robot position estimator 18 (1986) 89–95
Seviora, R.E., *see* Dasiewicz, P. 18 (1986) 591–595
Seviora, R.E., *see* Malhotra, P. 25 (1989) 15–20
Shallom, I., H. Azaria, Architectural concepts of an optimal instruction set selection procedure machine 16 (1985) 113–120
Shanthi, R., *see* Raja, D. 23 (1988) 345–350
Sharma, N.K., *see* Arora, R.K. 10 (1982) 247–254
Shen, J.P., S.P. Tomas, A roving monitoring processor for detection of control flow errors in multiple processor systems 20 (1987) 249–269
Shi, J., S. Monchaud, R. Prat, I. Kas, Control of motors by microprocessors for space positioning and localization 23 (1988) 339–344
Shields, R., *see* Taylor, M.J. 16 (1985) 313–318
Shimojima, T., M. Teramoto, V60 real-time operating system 21 (1987) 197–204
Shin, D.C., S.C. Moon, A concurrency control scheme for nested transactions 25 (1989) 233–238
Shuan, Z., *see* Jin, Shi-Yao 24 (1988) 743–746
Silva, V., *see* De Sá, L. 25 (1989) 41–46
Simon, J.C., Should computer education be science or technology oriented? 14 (1984) 251–254
Simoncini, L., *see* La Manna, M. 14 (1984) 9–16
Simone, C., *see* Rossi, G.P. 14 (1984) 55–66
Simpson, C.O., Realizing the economic promise of microprocessors 4 (1978) 9–13
Simpson, D.S., *see* Zedan, H. 25 (1989) 253–257
Simpson, R.J., T.J. Terrell, Digital filtering using the NEC $\mu$PD7720 signal processor 14 (1984) 67–78
Sipola, M., *see* Juntunen, T.T. 24 (1988) 449–454
Siva Ram Murthy, C., V. Rajaraman, A multi-microprocessor architecture for solving partial differential equations 20 (1987) 113–117
Skordalakis, E., A generalized cross assembly system for microcomputers 5 (1979) 82–88
Skordalakis, E., Towards a more flexible microlanguage for bit-sliced microcomputers 7 (1981) 46–57

Skordalakis, E., Towards a quick implementation of assemblers for the proposed IEEE microprocessors Assembly language draft standard 12 (1983) 89–98
Sloan, M.E., Microcomputer education in the United States 3(2) (1977) 5–7
Smirnov, V.A., *see* Chernykh, E.V. 20 (1987) 65–67
Smit, G.J.M., *see* Scholten, J. 21 (1987) 613–620
Smit, G.J.M., P.G. Jansen, The communication processor of TUMULT-64 24 (1988) 519–524
Smit, J., *see* Spaanenburg, L. 16 (1985) 83–88
Smith, B.V., *see* Atkins, P. 21 (1987) 161–170
Smith, D., *see* Naghdy, F. 21 (1987) 603–612
Smith, K.F., *see* Li, T. 22 (1988) 115–124
Soda, G., *see* Cesarini, F. 22 (1988) 301–310
Soininen, J.-P., *see* Kivelae, J.E. 18 (1986) 631–635
Soininen, J-P., *see* Juntunen, T.T. 24 (1988) 449–454
Somenzi, F., S. Gai, M. Mezzalama, P. Prinetto, Testable design with PLA macros 15 (1985) 119–128
Somenzi, F., *see* Gai, S. 16 (1985) 335–340
Sommer, P., *see* Menu, J. 20 (1987) 217–222
Sommerville, J.F., Towards machine-independent microprogramming 5 (1979) 219–224
Sonza Reorda, M., *see* Cabodi, G. 18 (1986) 39–45
Soomro, A.A., M. Rahman, S.M. Sait, A general real-time decoder based on AMD2900 devices 22 (1988) 97–114
Sorland, M.J., The microcomputer model 6 (1980) 354–358
Sorribes, A., *see* Luque, E. 20 (1987) 197–202
Sorribes, J., *see* De Blasi, M. 14 (1984) 145–148
Sosnowski, J., Transient fault tolerance in a data acquisition system 16 (1985) 255–260
Sosnowski, J., Concurrent checking of program flow using single-chip microcomputers 24 (1988) 783–790
Souček, B., S. Prohorov, Event-train correlation and real-time microcomputer systems 11 (1983) 23–30
Soukup, H., *see* Becker, B. 20 (1987) 153–157
Soupos, B., *see* Christodoulakis, D.N. 21 (1987) 383–390
Spaanenburg, L., M. Beunder, F.A. Beune, S.H. Gerez, B. Holstein, R.C.C. Luchtmeyer, J. Smit, A. van der Werf, H. Willems, Mod/R: A knowledge assisted approach towards top-down only CMOS VLSI design 16 (1985) 83–88
Spaanenburg, L., A. Broekema, J. Leenstra, C. Huys, VSLI-top-down design based on the separation of hierarchies 18 (1986) 341–346
Spaanenburg, L., Design of a 1-chip IBM-3270 protocol handler 25 (1989) 189–194
Spalla, M., *see* Annaratone, M. 9 (1982) 315–318
Spalla, M., *see* Brusamolino, M. 13 (1984) 331–340
Spalla, M., *see* Gai, S. 16 (1985) 335–340
Spalla, M., *see* Rojatti, P. 20 (1987) 47–54
Spalla, M., A. Fornasiero, F. Della Rosa, M. Pavan, Task to task communication and resource sharing in a geographically distributed network 23 (1988) 379–384
Sponga, L., *see* Dang, M. 24 (1988) 103–110
Spracklen, C.T., *see* Fox, A. 24 (1988) 339–346
Spree, H., Partially integrated input/output channels 2(3) (1976) 41–46
Springolo, M., *see* Bartolommei, B. 10 (1982) 311–324
Sprumont, L., *see* Taeymans, J. 3(3) (1977) 14–21
Sridhar, R., M.L. Manwaring, An automatic microcode generator for high level language machines 18 (1986) 263–268
Stadler, K., *see* Chroust, G. 8 (1981) 247–256
Stainov, R., *see* Manasiev, L. 23 (1988) 279–282

Stainov, R., L.Manasiev, A distributed communication service 25 (1989) 319–321
Stamatis, D., C. Halatsis, Encapsulation constructs in Prolog 21 (1987) 515–522
Stankovic, J.A., Improving system structure and its effect on vertical migration 8 (1981) 203–218
Starčević, D., *see* Milenković, Č. 21 (1987) 119–126
Starčević, D., *see* Jovanov, E. 23 (1988) 273–278
Staszny, G., *see* Alló, G. 19 (1987) 19–26
Staszny, G., *see* Alló, G. 19 (1987) 27–36
Stauffer, M., *see* Piguet, C. 21 (1987) 463–470
Steels, L., Massive parallelism for artificial intelligence 21 (1987) 17–20
Steenhaut, O., *see* Taeymans, J. 3(3) (1977) 14–21
Steenhaut, O., *see* Vermeesch, J. 21 (1987) 129–136
Stefanelli, R., *see* Doniants, V.N. 14 (1984) 109–116
Stefanelli, R., *see* Doniants, V.N. 16 (1985) 101–106
Stefanelli, R., *see* Piuri, V. 23 (1988) 63–74
Stefanelli, R., *see* Doniants, V.N. 25 (1989) 113–118
Stetsyura, G.G., *see* Prangishvili, I.V. 7 (1981) 220–228
Stevens, M.P.J., M.T.M. Segers, J.G.H. Alma, A universal traffic controller based on a microprocessor 1(5) (1975) 30–39
Stojkov, Z., *see* Jovanov, E. 23 (1988) 273–278
Stone, R.F., *see* Zedan, H. 25 (1989) 253–257
Storti Gajani, G., *see* Bonzio, F.C. 21 (1987) 91–98
Strasbourger, E., *see* Barzilai, Z. 6 (1980) 149–157
Stravers, P., *see* Knoppers, P. 24 (1988) 275–280
Streit, H., *see* Eicher, W. 22 (1988) 379–384
Stuart, J.P., Microprocessor activity in the UK 1(3) (1975) 14–15
Šubelj, M., *see* Trobec, R. 20 (1987) 55–58
Suk, D., G. Carlstedt, Hierarchical emulation of well-coded programs through the use of streams of instructions buffered in on-chip associative memories and distributed control 13 (1984) 153–170
Sulej, M., *see* Szymanski, B. 10 (1982) 255–260
Szanto, G., J. van den Bos, Function-microcomputers for computer graphics 6 (1980) 410–416
Szturmowicz, M., *see* Thor, M. 16 (1985) 325–330
Szymanski, B., M. Sulej, Design of a specialized hardware for data selection and transformation 10 (1982) 255–260
Szynka, J., Z. Poznański, Cellular system based on single-chip microcomputers 16 (1985) 301–304

Tabak, D., *see* Azaria, H. 6 (1980) 373–380
Tabak, D., *see* Zager, A. 11 (1983) 177–186
Tabak, D., *see* Azaria, H. 11 (1983) 187–194
Tabak, D., *see* Plotkin, A. 12 (1983) 153–158
Tabak, D., *see* Azaria, H. 12 (1983) 199–206
Tabak, D., *see* Etkin, J. 14 (1984) 181–186
Tabak, D., The Intel 80386 and new 32-bit microprocessors 19 (1987) 59–74
Tabak, D., *see* Davila, J.M. 23 (1988) 179–184
Taeymans, J., *see* Tiberghien, J. 3(2) (1977) 80–83
Taeymans, J., O. D'Hondt, J. Cornelis, M. Nyssen, L. Sprumont, J. Lievens, J. Tiberghien, O. Steenhaut, A microprocessor based automatics film editing device 3(3) (1977) 14–21
Taeymans, J., P.F. De Bruyne, The use of a microcomputer as front end processor for automatic measurement 9 (1982) 231–236
Tagliabue, M., *see* Ferrari, R. 20 (1987) 91–97
Tagliasco, V., *see* Morasso, P. 4 (1978) 305–309

Tajima, H., *see* Mori, R. 3(4) (1977) 50–57
Tajima, H., *see* Mori, R. 5 (1979) 150–154
Tajima, M., *see* Mori, R. 3(4) (1977) 50–57
Tajima, M., *see* Mori, R. 5 (1979) 150–154
Tal, D., *see* Rishe, N. 25 (1989) 33–38
Tal, D., I. Erenyi, J.C. Comfort, A fault-tolerant systolic data flow machine 25 (1989) 91–95
Tamura, K., *see* Umeyama, S. 10 (1982) 325–332
Tamura, M., *see* Masada, E. 3(4) (1977) 71–74
Tanaka, A., *see* Maekawa, M. 8 (1981) 189–202
Tasic, J., R. Sega, Systolic arrays in adaptive digital filters 25 (1989) 107–112
Tassakos, L., *see* Plessmann, K.W. 24 (1988) 257–266
Tassakos, L., K.W. Plessmann, pdvPOOL: A real-time object-oriented multiprocessor system 25 (1989) 221–227
Taylor, B.G., Macintosh in the laboratory 21 (1987) 101–110
Taylor, M.J., W.A. Corbett, J.R. Coughlan, R. Shields, Current work on the development of a distributed microcomputer-based patient record system 16 (1985) 313–318
Taylor Simpson, L., *see* Agrawal, D.P. 24 (1988) 79–84
Tazza, M., *see* Navaux, P. 24 (1988) 197–204
Tchogovadze, G.G., Some steps towards intelligent computer tutoring systems 16 (1985) 1–6
Tchon, W.E., *see* Klein, R. 10 (1982) 129–138
Téllez-Girón, R., K. Ergang, A. Hunger, A combined testing approach for instruction-set-based data processing devices 20 (1987) 147–152
Temme, K.-H., R. Felix, J. Herrmann, An expert system for chip-architecture planning 23 (1988) 47–50
Tempelmeier, T., Microprocessors in factory automation: A case study of an automated guided vehicle system and its integration into a hierarchical control structure 18 (1986) 647–656
Tempelmeier, T., Performance analysis of a microprogrammed real-time operating system with an interrupt-and-abort discipline 19 (1987) 233–252
Ter Meer, P., Data compression by an accordion unit 6 (1980) 314–315
Teramoto, M., *see* Shimojima, T. 21 (1987) 197–204
Terenzi, F., The computer audio research laboratory at the center for music experiment and related research 24 (1988) 443–446
Terrell, T.J., *see* Simpson, R.J. 14 (1984) 67–78
Terzoli, M., *see* Gentile, G. 14 (1984) 99–108
Tesink, K., H. Kok, Digital hardware design and test method 6 (1980) 51–59
Thalmann, D., *see* Faquet, G. 9 (1982) 17–26
Theodoridis, S., N. Kalouptsidis, Parallel algorithm for MSE estimation of 2-D noncausal image models 21 (1987) 137–140
Theys, A., *see* Piguet, C. 21 (1987) 463–470
Thomesse, J.P., *see* Husson, R. 3(2) (1977) 39–45
Thomesse, J.P., *see* Cochet-Muchy, A. 7 (1981) 13–19
Thompson, L.R., Microprocessor status report 3(4) (1977) 34–39
Thor, M., M. Szturmowicz, Interrupts in modular microprogramming 16 (1985) 325–330
Thorelli, L.E., *see* Carlstedt, G. 4 (1978) 239–242
Thorne, M.P., *see* Clarke, A.T. 24 (1988) 549–554
Tiberghien, J., J. Taeymans, CASS8: A cross assembler suitable for most of the 8 BIT microprocessors 3(2) (1977) 80–83
Tiberghien, J., *see* Taeymans, J. 3(3) (1977) 14–21
Tiberghien, J., *see* Lenders, P. 4 (1978) 220–221
Tiberghien, J., Status report on microprocessors in Belgium 5 (1979) 118–119
Tiberghien, J., *see* Tommelein, A. 15 (1985) 47–56

Tiberghien, J., *see* Rijks, E. 21 (1987) 639–646
Tiberghien, J., *see* Dirkx, E. 24 (1988) 149–152
Tiberghien, J., *see* Van der Jeugt, G. 24 (1988) 205–208
Tibo, R., *see* Venturino, G. 5 (1979) 270–279
Tiensyrjä, K., *see* Juntunen, T.T. 24 (1988) 449–454
Tikkanen, T., *see* Juntunen, T.T. 24 (1988) 449–454
Ting, Gong Wei, *see* Chao, Yen 20 (1987) 227–232
Tirado, J., *see* Hermida, R. 12 (1983) 99–110
Tirado, J.F., *see* Bautista, A. 9 (1982) 27–38
Tirado Fernandez, J.F., *see* Fadon, E.L. 4 (1978) 133–140
Tireford, H., Linking Fortran and Assembly language programs 4 (1978) 27–29
Toetenel, W.J., *see* Huijsman, R.D. 21 (1987) 301–310
Tojoo, A., *see* Mori, R. 5 (1979) 155–165
Tolub, S., Y. Wallach, Sorting on a MIMD-type parallel proessing system 4 (1978) 155–161
Tomann, S., J. Liedl, Reliability in microcomputer arrays 7 (1981) 185–190
Tomas, S.P., *see* Shen, J.P. 20 (1987) 249–269
Tomek, I., HARD: A multilevel hardware simulation package for teaching 19 (1987) 417–422
Tomescu, D., C. Baleanu, A. Botta, The circular synchronous bus 7 (1981) 344–350
Tomescu, D., C. Baleanu, The circular synchronous computer and its relation to functional programming 10 (1982) 333–340
Tommelein, A., J. Tiberghien, A comparison of compiler-generated code 15 (1985) 47–56
Tong, M., *see* Clemen, R. 18 (1986) 441–444
Torasso, P., *see* Mezzalama, M. 4 (1978) 109–114
Torsoli, A., *see* Martinelli, P. 4 (1978) 318–321
Tourouta', E., On the reliability assessment of a fault-tolerant pipe-line multi-microprocessor system 6 (1980) 243–248
Tourouta, E., *see* Piuri, V. 19 (1987) 335–349
Tourouta, E., *see* Piuri, V. 21 (1987) 339–346
Toursel, B., *see* Goncalves, G. 23 (1988) 149–152
Toursel, B., *see* Devesa, N. 24 (1988) 613–620
Trangez, I., A multi channel event-counter/timer 7 (1981) 249–259
Traub, A.C., *see* Richard, A.A. 6 (1980) 296–303
Traverso, G., *see* Piuri, V. 23 (1988) 63–74
Treleaven, P.C., *see* Lu, X.D. 10 (1982) 1–10
Trobec, R., I. Lesjak, M. Šubelj, A resident X.25 software development 20 (1987) 55–58
Trobec, R., J. Korenini, L. Gyergyek, A regular WSI-node architecture 21 (1987) 75–82
Trobec, R., L. Gyergyek, J. Korenini, Two-dimensional parallel system diagnostic 25 (1989) 353–358
Troiano, L., *see* Proverbio, S. 8 (1981) 83–92
Trottenber, U., Suprenum: A MIMD multiprocessor system for large scale scientific computing 18 (1986) 3–7
Troya, J., *see* Ortega, M. 25 (1989) 301–306
Troya, J.M., *see* Vaquero, A. 11 (1983) 127–132
Troya Linero, J.M., *see* Vaquero Sánchez, A. 5 (1979) 370–374
Tsai, S.R., L.M. Tseng, M.D. Beer, A microprocessor-based logical machine system 19 (1987) 375–384
Tsai, S.R., L.M. Tseng, C.N. Chen, On the architectural support for logical machine systems 22 (1988) 81–96
Tseng, L.M., *see* Tsai, S.R. 19 (1987) 375–384
Tseng, L.M., *see* Tsai, S.R. 22 (1988) 81–96
Tsuchiya, M., FREM: Firmware requirements engineering methodology 8 (1981) 167–178
Tubaro, S., *see* Brofferio, S. 17 (1986) 139–152
Tvrdy, I., Formal modelling of telematic services using LOTOS 25 (1989) 313–317
Tyrrell, A.M., *see* Carpenter, G.F. 22 (1988) 175–186

Tyszer, J., Multiple fault diagnosis for interconnection networks for distributed systems 24 (1988) 731–734

Uchida, S., *see* Mori, R. 5 (1979) 155–165
Ujhelyi, T., *see* Demeczky, J. 6 (1980) 330–335
Umeyama, S., Y. Okada, K. Tamura, New processor interconnection strategies for a large multi-processor system 10 (1982) 325–332
Ungerer, T., ASTOR: An architecture for special purpose processing units with distributed control and message passing 23 (1988) 227–232
Ungerer, T., *see* Eichenseher, I. 24 (1988) 363–370
Ungerer, T., *see* Zehendner, E. 25 (1989) 209–212
Upendra Rao, S., A.K. Majumdar, Analysis of a few list scheduling algorithms for compaction of horizontal microprograms 17 (1986) 29–40
Upendra Rao, S., A.K. Majumdar, Global microcode compaction: A performance evaluation by simulation 22 (1988) 159–174
Urero, F., *see* Cattaneo, G. 15 (1985) 233–252

Vaccaro, R., *see* Binaghi, E. 25 (1989) 287–292
Vajda, F., Gleanings from the Hungarian microprocessor literature 5 (1979) 125–127
Vajda, F., Supermicros: Objectives and approaches 17 (1986) 1–18
Vajda, F., Concurrent systems, programming primitives and languages: A comparative study 18 (1986) 185–194
Valenzano, A., *see* Demartini, C. 16 (1985) 231–238
Valenzano, A., *see* Ciminiera, L. 20 (1987) 59–64
Valenzano, A., P. Montuschi, L.Ciminiera, Implementation of algorithms for graphic surface modeling using transputers 25 (1989) 265–270
Valero, M., *see* Lang, T. 10 (1982) 11–18
Valmari, A., Reachability analysis-based validation of embedded systems 21 (1987) 393–404
Valmari, A., PC-RIMST: A tool for validating concurrent program designs 24 (1988) 809–818
Van Bruchem, D.J.J., J.B. Roubos, B. Schellingerhout, Implementation of the connection oriented network service on a LAN 23 (1988) 367–372
Van Campenhout, J.M., *see* Debaere, E.H. 18 (1986) 213–220
Van de Burg, R., *see* Reijns, G.L. 18 (1986) 675–681
Van de Goor, A.J., *see* Knoppers, P. 24 (1988) 275–280
Van de Goor, A.J., G.J. Nanninga, Speech synthesis system with unlimited vocabulary for the Dutch language 24 (1988) 325–334
Van den Bos, J., *see* Szanto, G. 6 (1980) 410–416
Van den Eede, L., *see* Vorstermans, J. 18 (1986) 525–538
Van der Jeugt, G., E. Dirkx, J. Tiberghien, Protocol description and simulation in the OCCAM programming language 24 (1988) 205–208
Van der Werf, A., *see* Spaanenburg, L. 16 (1985) 83–88
Van Deursen, W.P., K. Li, Microprocessor-controlled videogames 4 (1978) 44–46
Van Eck, J.L., *see* Lataire, P. 5 (1979) 380–383
Van Esbroeck, P., *see* Gruszecki, M. 18 (1986) 667–674
Van Gennip, J.P.C., Interfacing VME-bus to a fault-tolerant architecture in the industrial environment 18 (1986) 289–295
Van Horebeek, I., *see* Duponcheel, L. 21 (1987) 231–240
Van Katwijk, J., *see* Huijsman, R.D. 21 (1987) 301–310
Van Prooijen, J.P.A., Personal computing in The Netherlands 6 (1980) 90–91
Van Puymbroeck, W., *see* Duponcheel, L. 21 (1987) 231–240
Van Rhee, L.A., Microcomputer control of a source scanner for measurements of radioactive foils 6 (1980) 311–313
Van Rumste, M., The iAPX432, a next generation microprocessor 11 (1983) 69–106
Van Spronsen, C.J., Microprocessor status report: The Netherlands 5 (1979) 133–137

Van Spronsen, C.J., From processor architectures to education: Editorial introduction 12 (1983) 141–142
Van Spronsen, C.J., *see* Frietman, E.E.E. 14 (1984) 173–176
Van Spronsen, C.J., *see* Reijns, G.L. 18 (1986) 675–681
Van Spronsen, C.J., *see* Kroon, J.G.M. 21 (1987) 347–356
Van Veldhuizen, E.D., The 8X330 floppy disk controller chip 5 (1979) 11–15
Van Viegen, M., A distributed data acquisition and control system 12 (1983) 211–216
Van Wormhoudt, M.C., *see* El Hamalaway, M.A. 3(1) (1977) 59–66
Vangen, R.F., *see* Fox, E.R. 22 (1988) 385–392
Vanneschi, M., On the microprogrammed implementation of some computer architectures 2(2) (1976) 14–20
Vanzetti, R., *see* Richard, A.A. 6 (1980) 296–303
Vaquero, A., J.M. Troya, J.M. Sánchez, C. Fernández, M. Iglesias, A microfilmed information retrieval system 11 (1983) 127–132
Vaquero Sánchez, A., J.M. Sánchez Pérez, J.M. Troya Linero, Microprocessor-based learning systems 5 (1979) 370–374
Varghese, J., *see* Mueller, R.A. 11 (1983) 107–116
Vasanthavada, N., *see* Kanopoulos, N. 21 (1987) 57–64
Vasconcelos, N.Q., F.M.G. França, E.S.T. Fernandes, Design of an EDISON virtual machine: From a HLL specification to a microprogrammed implementation 20 (1987) 209–216
Vasconcelos, N.Q., E.S.T. Fernandes, An environnment for concurrent programming in Pascal 25 (1989) 381–386
Velardi, P., *see* Cioffi, G. 11 (1983) 15–22
Velardi, P., B. Ciciani, Recovery blocks for communication systems 11 (1983) 287–294
Velardi, P., A. Forcina, Reliability analysis of multipath interconnection networks 17 (1986) 255–266
Veldstra, B., J.M.H. Dassen, A small operating system for a 8080 microcomputer system 2(4) (1976) 54–64
Veler, Y., *see* Azaria, H. 16 (1985) 127–132
Veler, Y., *see* Azaria, H. 20 (1987) 99–105
Venturino, G., R. Tibo, Microcomputer in stripping columns control 5 (1979) 270–279
Verbaeten, P., *see* Moons, H. 24 (1988) 179–186
Verbaeten, P., *see* Berbers, Y. 24 (1988) 187–194
Verbaeten, P., *see* De Decker, D. 25 (1989) 239–243
Verdoscia, L., *see* Binaghi, E. 25 (1989) 287–292
Verebély, P., GD80: A multi-microprocessor architecture for computer graphics 6 (1980) 406–409
Vermeesch, J., P. Bulckaert, M. Defrise, O. Steenhaut, A pipelined VLSI-based structure for the reconstruction of three-dimensional images from projections 21 (1987) 129–136
Vermeesch, J., *see* Rijks, E. 21 (1987) 639–646
Vernazza, T., *see* Antoy, S. 4 (1978) 222–226
Vernazza, T., *see* Di Manzo, M. 10 (1982) 19–24
Vernazza, T., *see* Giordano, A. 19 (1987) 5–18
Verrall, M., PCTE: The kernel of software engineering environments 24 (1988) 161–166
Verrua, L., *see* Del Corso, D. 13 (1984) 21–30
Viera de Sá, L., F. Perdigão, A multiprocessing system for the TMS32020 23 (1988) 221–226
Vierhaus, H.T., *see* Matthäus, C. 24 (1988) 233–238
Vignati, G., *see* Cagnola, A. 16 (1985) 61–66
Vincent-Carrefour, A., *see* Auguin, M. 21 (1987) 171–178
Vinyes Sanz, J., J. Quemada Vives, A. Fernandez del Campo, A building access control system with alarm monitoring 12 (1983) 111–114

Vivian, R.H., DPCM studies using edge prediction and adaptive quantisation laws for the transmission of still pictures over the ISDN 21 (1987) 141–150
Vlahavas, J., C. Halatsis, A RISC Prolog machine architecture 21 (1987) 259–266
Vlajnić, M., *see* Nedic, S. 16 (1985) 169–174
Vlajnić, M., WSI implementation of systolic arrays for GaAs 20 (1987) 121–126
Vlajnić, M., T. Maksimović, Coprocessor design for GaAs technology 21 (1987) 657–664
Vlajnić, M., A. Purkovic, Z. Mladenovic, S. Nedic, Universal DSP Board 25 (1989) 47–50
Vlasblom, A.A., *see* Houtekamer, G.E. 18 (1986) 491–497
Vogt, R., Improving the reliability of bus systems: Fault isolation and fault tolerance 21 (1987) 333–338
Voigt, K., *see* Bruck, K. 23 (1988) 357–358
Vojvodić, I.V., P.A. Vranеš, Distributed intelligence and bus hierarchy system implementation for a highly intelligent workstation 23 (1988) 351–354
Vollebergh, J.J.A., Microelectronics and the evolution of society 7 (1981) 3–12
Von Puttkamer, E., A microprogrammed Lisp machine 12 (1983) 9–14
Von Puttkamer, E., W. Eicher, A new strategy for interpreting LISP 18 (1986) 81–88
Von Puttkamer, E., *see* Eicher, W. 22 (1988) 379–384
Von Puttkamer, E., *see* Hinkel, R. 24 (1988) 411–418
Vorloeper, G., *see* Rammig, F.J. 24 (1988) 299–306
Vorstermans, J., L. van den Eede, An image processor for a multi-image processing system 18 (1986) 525–538
Vranеš, P.A., An implementation of a high-speed gray-to-binary code converter 16 (1985) 95–100
Vranеš, P.A., *see* Vojvodić, I.V. 23 (1988) 351–354
Vranes, S., A heuristic algorithm for real-time application allocation to multimicrocomputer 25 (1989) 347–351
Vuille, J.P., Standards for $\mu$P systems? 1(3) (1975) 8–9
Vuille, J.P., Standards for $\mu$P systems 1(5) (1975) 12–14
Vuille, J.P., Standards for $\mu$P systems 2(1) (1976) 10–15
Vuille, J.P., Standards for $\mu$P systems 2(2) (1976) 5–6
Vuille, J.P., Standards for $\mu$P systems 3(1) (1977) 49–53

Wackersreuther, G., *see* Bolch, G. 12 (1983) 267–278
Wada, K., M. Miyamoto, S. Kuo, Y. Kaneda, S. Maekawa, Intermediate code for the sequential Prolog machine PEK 21 (1987) 275–282
Waldschmidt, K., Microelectronics technology: An introduction 10 (1982) 71–76
Waldschmidt, K., *see* Milutinović, V. 12 (1983) 33–42
Wallach, Y., *see* Tolub, S. 4 (1978) 155–161
Wallach, Y., D. Leven, Alternating sequential/parallel versions of the simplex algorithm 6 (1980) 237–242
Wallach, Y., *see* Richter, L. 7 (1981) 304–311
Wallach, Y., Fast computations for some matrix functions 22 (1988) 23–28
Wallach, Y., E. Yaprak, Use of local-area networks as alternating sequential-parallel systems 24 (1988) 95–102
Wan, Yu-liang, Yan-zhen Qu, Research and design of a communication subnet for supporting distributed systems 18 (1986) 479–487
Wang, Ruey-Rong, *see* Kuo, Yau-Hwang 23 (1988) 27–32
Waring, L.C., *see* Scott, N.S. 24 (1988) 535–540
Wasmeier, M., Speech recognition and speech synthesis 5 (1979) 40–43
Watanabe, M., M. Kato, I. Nakata, The macro assembler generator for microcomputers 3(4) (1977) 83–85
Wattrelot, F., *see* Dubus, J.P. 9 (1982) 133–142
Weber, H., *see* Kreyßig, J. 25 (1989) 55–60
Weber, M., *see* Hartenstein, R.W. 21 (1987) 65–72
Wellings, A.J., *see* Murray, K.A. 24 (1988) 169–178

Welters, U., *see* Hartenstein, R.W. 21 (1987) 585–590
Welzer, T., I. Rozman, J. Györkös, Automated normalization tool 25 (1989) 375–380
Wendt, S., Functional description of the integrated processor circuit Intel 8080 2(1) (1976) 30–37
Werbrouck, A., *see* Roveda, V. 22 (1988) 347–352
Werner, M., *see* Katzenelson, J. 3(2) (1977) 22–25
Westendorf, J., *see* Rauscher, R. 23 (1988) 3–8
Whalen, S.P., *see* Fox, E.R. 22 (1988) 385–392
Whitehead, D.G., A.G. Mason, Low resolution vision sensing 16 (1985) 331–334
Wiese, G.G., The total software upward/downward compatible R 65XX microcomputer family 5 (1979) 7–10
Wilkes, M.V., The best way to design an automatic calculating machine 8 (1981) 141–144
Wilkinson, D.M., *see* Charlton, C.C. 22 (1988) 255–262
Willems, H., *see* Spaanenburg, L. 16 (1985) 83–88
Willems, Y.D., *see* Ballieu, G. 8 (1981) 179–188
Wilmink, J., Microprogramming and microprocessors in the Netherlands 1(2) (1975) 12–14
Wilmink, J., Status report The Netherlands 3(4) (1977) 25–32
Wilson, D.R., S.C. Winter, On the analysis and design of manufacturing systems 21 (1987) 433–440
Wilson, D.R., *see* Fernin, P. 24 (1988) 315–324
Wilson, D.R., *see* Boudillet, O. 24 (1988) 389–396
Wilson, D.R., *see* Guiheux, H. 24 (1988) 425–432
Wilson, R., *see* Donnelly, T. 23 (1988) 333–338
Wingate, G.A.S., C. Preece, Transient fault recovery assessment in 8 and 16 bit microprocessor based controllers in embedded systems 24 (1988) 775–782
Winiarczyk, R., *see* Gorawski, M. 15 (1985) 75–84
Winkelhage, F., G. Eschenröder, Present status and future development of information management 15 (1985) 1–10
Winsborrow, R.P.J., I.T. Belcher, G.C. Cox, A general purpose communications processor 3(3) (1977) 3–8
Winter, S.C., On the physical interconnection of a CTD multiprocessor 18 (1986) 597–601
Winter, S.C., *see* Wilson, D.R. 21 (1987) 433–440
Winter, S.C., *see* Fernin, P. 24 (1988) 315–324
Winter, S.C., *see* Boudillet, O. 24 (1988) 389–396
Winter, S.C., *see* Guiheux, H. 24 (1988) 425–432
Wirth, N., Oberon: A system for workstations 24 (1988) 3–8
Wiszniewski, B.W., MOVE: A verifying tool for modular software 14 (1984) 149–154
Wolff, D., *see* Schindel, U. 10 (1982) 139–152
Worland, P., Turn Basic into Pascal 7 (1981) 260–265
Wrona, W., *see* Papazoglou, M.P. 25 (1989) 213–219
Wurm, F.X., *see* Bolch, G. 12 (1983) 267–278

Yajima, K., *see* Kato, S. 3(4) (1977) 86–87
Yamaguchi, T., *see* Funakubo, H. 3(4) (1977) 65–70
Yamamoto, M., K. Hakozaki, A Cobol oriented high level language machine 3(4) (1977) 58–64
Yankov, B., S. Bonev, L.Nikolov, A portable compiler for microcomputers 16 (1985) 221–226
Yaprak, E., *see* Wallach, Y. 24 (1988) 95–102
Yau, K.H., *see* Malek, M. 20 (1987) 331–343
Yie, Shi-Sheng, *see* Jin, Shi-Yao 24 (1988) 743–746
Yoshida, K., *see* Fujita, S. 3(4) (1977) 88–89
Young, H.C., Code scheduling methods for some architectural features in PIPE 22 (1988) 39–64

Zabludowski, A., A. Jurkiewicz, Analysis of a multi-microprocessor system with time-shared bus 13 (1984) 171–178
Zabrodzki, J., *see* Kaminska, B. 12 (1983) 285–290
Zager, A., D. Tabak, Flexible architecture microcomputer design 11 (1983) 177–186
Zagolin, M., *see* Mattasoglio, A. 11 (1983) 295–300
Zaks, R., Microcomputer systems: A survey 1(1) (1974) 9–13
Zaks, R., An assessment of microcomputers in Europe 6 (1980) 72–77
Zakzouk, E.E.-D., *see* El-Shirbeeny, E.-H.T. 5 (1979) 384–386
Zaluska, E.J., *see* Anido, M.L. 24 (1988) 581–588
Zamboni, M., *see* Gregoretti, F. 18 (1986) 409–416
Zamboni, M., *see* Civera, P.L. 21 (1987) 267–274
Zapata, E.L., R. Doallo, F.F. Rivera, M.A. Ismail, A VLSI systolic architecture for fuzzy clustering 24 (1988) 647–654
Zappa, G., *see* Cesarini, F. 22 (1988) 301–310
Zaroliagis, C., *see* Christodoulakis, D.N. 21 (1987) 383–390
Zedan, H., Achieving atomicity in OCCAM 23 (1988) 261–266
Zedan, H., On the analysis of OCCAM real-time distributed computations 24 (1988) 491–500
Zedan, H., R.F. Stone, D.S. Simpson, PCHAN: A notion for dynamic resource management in distributed Occam systems 25 (1989) 253–257
Zehendner, E., A module-based assembly language with parallel processing constructs and its implementation in the ASTOR architecture 23 (1988) 215–220
Zehendner, E., *see* Eichenseher, I. 24 (1988) 363–370
Zehendner, E., T. Ungerer, A simulation method for parallel computer architectures 25 (1989) 209–212
Zeidler, H.Ch., *see* Kreyßig, J. 25 (1989) 55–60
Zetta, L., *see* Annunziata, M. 18 (1986) 515–524
Zhen, Du Chang, CAI system for Basic 12 (1983) 227–230
Zimmer, G., MOS-technology 10 (1982) 77–100
Zincke, G.D., Why is microprogramming difficult? Some thoughts about a generally accepted problem 8 (1981) 149–152
Zinky, J.A., *see* Etkin, J. 25 (1989) 307–312
Zobel, R.N., Real-time systems education in computer science at Manchester University 24 (1988) 835–840
Zoccolante, L., *see* Ferrari, R. 20 (1987) 91–97
Zulian, F., T. Maccianti, Design of a VLSI 32-bit microprocessor to be used on the CPU (IDP) of the Honeywell DPS4 follow-on computer 20 (1987) 223–226

North-Holland
Microprocessing and Microprogramming 25 (1988) 455–490



# SUBJECT INDEX TO VOLUMES 1–25 [1]


Abstract data type 11 (1983) 69
– 21 (1987) 231, 241, 515
Abstract data type implementation 23 (1988) 95
Abstract datatyping 23 (1988) 239
Abstraction level graphics editor 21 (1987) 585
Abstraction levels 25 (1989) 184
Accelerator 14 (1984) 187
Accelerator card for PC 25 (1989) 51
Access method 18 (1986) 161
Access protocol 18 (1986) 153
– 22 (1988) 353
Access time 18 (1986) 205
Access unit 13 (1984) 97
Accordion unit 6 (1980) 314
Acquisition of expected behaviour 21 (1987) 471
ACT-ONE 21 (1987) 241
Activities sequences 24 (1988) 681
Actor 22 (1988) 187
Actor computation 18 (1986) 307
A/D converter 7 (1981) 160
– 25 (1989) 335
Ada 12 (1983) 79
– 21 (1987) 301
– 25 (1989) 74
Ada tasking 21 (1987) 301
Adaptable systems 8 (1981) 219
Adaptive algorithm 25 (1989) 108
Adaptive control 22 (1988) 301
Adaptive digital filters 25 (1989) 107
Adaptive equalizer 25 (1989) 108
Adaptive quantisation 21 (1987) 141
Addition 22 (1988) 75
Address allocation 19 (1987) 291
Address selection 8 (1981) 179
Addressing mode 23 (1988) 267
Advanced applications 25 (1989) 39
Advanced learning system 11 (1983) 117
Advanced microprocessors 19 (1987) 277
– 21 (1987) 665
AHPL description 16 (1985) 89
AI applications 21 (1987) 249, 505
AI architecture 21 (1987) 249, 505
Alarm monitoring 12 (1983) 111
Algebra of processes 21 (1987) 549
Algebraic simulation 11 (1983) 107
Algebraic specification 21 (1987) 231, 241
Algorithm complexity 10 (1982) 163
Algorithm for computer graphics 25 (1989) 61
Algorithm for filters 25 (1989) 98
Algorithm for graphic surface modeling 25 (1989) 265
Algorithm implementation 23 (1988) 253
Algorithm methods for yield enhancement and reliability 25 (1989) 113
Algorithms 22 (1988) 115
Algorithms in microcontroller design 3(4) (1977) 71
Allocation of processes 21 (1987) 339
Alpha-sensitivity 10 (1982) 119
Alphanumerical text 9 (1982) 133
Alternating sequential-parallel system 6 (1980) 237
– 24 (1988) 95
Alternative education forms 6 (1980) 221
ALU kernel 21 (1987) 313
AM 2901 7 (1981) 58
AM 2903 9 (1982) 267
A*N codes 23 (1988) 63
Analogue interfaces 25 (1989) 271
Analogue to microprocessor 4 (1978) 47
Analysis-based validation 21 (1987) 393
Analysis of manufacturing systems 21 (1987) 433
Analysis of processor architecture for GaAs 21 (1987) 665
Analytical device models 10 (1982) 139
Angular errors 23 (1988) 327
Anisochronous transmission 12 (1983) 111
Answer mailbox 7 (1981) 229
APL 9 (1982) 259
Appliance control 6 (1980) 17
Application connection establishment 23 (1988) 373
Application layer protocol 24 (1988) 111
Application oriented language 19 (1987) 1
Applications support 21 (1987) 205
Applicative language 21 (1987) 549

Apply evaluation 18 (1986) 81
Arbiter design 10 (1982) 11
Arbitration 19 (1987) 5
Arbitration delay 13 (1984) 21
Architectural model 17 (1986) 285
Architecture 21 (1987) 319
– 25 (1989) 119, 204, 209
Architecture adaptation 8 (1981) 219
Architecture description language 16 (1985) 121
Architecture for GaAs 21 (1987) 665
Architecture for image processing 21 (1987) 65
Architecture for serial link 21 (1987) 613
Architecture multimicroprocessor 21 (1987) 325
Architectures for logical programming 19 (1987) 385
Arithmetic oriented multiprocessor system 6 (1980) 330
Arm and hand prosthesis controlled by microcomputer 3(4) (1977) 65
Array of multiprocessors 22 (1988) 29
Array of transputers 25 (1989) 259
Artificial intelligence 13 (1984) 221
– 16 (1985) 1, 17, 249, 251, 505, 507, 623
ASM 21 (1987) 647
Assembly language 2(2) (1976) 38
– 23 (1988) 215
Assembly programs 1(3) (1975) 22
– 4 (1978) 27
Assembly robot 21 (1987) 427
Associative search 13 (1984) 153
Associativity 8 (1981) 257
ASTOR 23 (1988) 227
ASTOR architecture 23 (1988) 215
Asymmetric invalidation 24 (1988) 723
Asynchronism 20 (1987) 287
Asynchronous arbiters 9 (1982) 299
– 11 (1983) 15
Asynchronous control 12 (1983) 181
Asynchronous message passing 18 (1986) 307
Asynchronous operations 3(1) (1977) 7
Asynchronous process 11 (1983) 31
Asynchronous process communication 21 (1987) 481
Asynchronous VLSI systems 24 (1988) 347
Atomic action 14 (1984) 9
– 23 (1988) 261
Attitudes toward computers 11 (1983) 1
Attribute grammars 9 (1982) 277
Audio computer 25 (1989) 271
Audio interfaces 17 (1986) 139
Audio supercomputer 25 (1989) 272
Author languages 15 (1985) 263
Authoring systems 14 (1984) 267
Automated datapath synthesis 21 (1987) 577
Automated guided vehicle system (AGVS) 18 (1986) 647
Automated normalization tool 25 (1989) 375
Automated production 18 (1986) 173
Automated synthesis 21 (1987) 647
Automated test pattern generation 15 (1985) 119
Automatic control 10 (1982) 261
Automatic hardware synthesis 18 (1986) 243
Automatic measurement 9 (1982) 231
Automatic process switching 23 (1988) 267
Automatic protocol generation 24 (1988) 111
Automatic software generator 25 (1989) 367
Automatic synthesis of digital systems 18 (1986) 347
Automatic tuning 15 (1985) 195
Autonomous mobile robot 23 (1988) 135
– 24 (1988) 411
Autoregressive model 9 (1982) 267

B-HIVE multicomputer 24 (1988) 79
B-trees 22 (1988) 243
Backplanes P896/D2 standards specification 6 (1980) 363
Backprojecting 21 (1987) 129
Backtracking 24 (1988) 607
Backward error recovery 16 (1985) 217
– 17 (1986) 205
– 21 (1987) 481
Balanced load queueing model 19 (1987) 401
Bandwidth reduction 12 (1983) 231
Base-table 22 (1988) 11
BASIC 7 (1981) 260
– 13 (1984) 189
BCD number system 19 (1987) 119
BCD packed decimal numbers 19 (1987) 119
Behavioral automatic tester 16 (1985) 107
– 18 (1986) 363
Behavioral description language 18 (1986) 243
Behavioral microcomputer description 4 (1978) 103
Behavioral objectives 11 (1983) 117

Behavioral simulation 24 (1988) 213
Behavioral testing techniques 24 (1988) 63
Behaviour display system 3(2) (1977) 73
Bezier curves and surfaces 22 (1988) 115
Bi-directional switches 14 (1984) 109
Bilateral array 25 (1989) 87
Binary arithmetic 19 (1987) 119
Binary coded decimal (BCD) arithmetic unit 7 (1981) 97
Binary decision machine 2(1) (1976) 16
Binary list 18 (1986) 371
Binary number system 19 (1987) 119
Binary search algorithm 24 (1988) 95
Binary to decimal conversion 19 (1987) 119
Bioengineering 4 (1978) 305
Biomedical applications 4 (1978) 304
Biomedical engineering 21 (1987) 129
Bipolar devices 10 (1982) 101
Bipolar integrated circuits 10 (1982) 101
Bit reduction 16 (1985) 17
Bit slice 9 (1982) 267
– 17 (1986) 205
Bit slice arithmetic processor 14 (1984) 215
Bit slice based systems 7 (1981) 58
Bit slice devices 22 (1988) 97
Bit slice elements 12 (1983) 175
– 14 (1984) 205
Bit slice microprocessors 4 (1978) 14
– 9 (1982) 67
Bit slice processor 6 (1980) 51
– 10 (1982) 311
Bit slice systems 12 (1983) 43
Bit sliced microcomputers 7 (1981) 46
Bit steering 16 (1985) 17
Bit steering algorithm 23 (1988) 21
Boolean differences 15 (1985) 119
Boolean equation 24 (1988) 767
Boolean functions 16 (1985) 341
Boolean functions implementation 4 (1978) 87
Bootstrap principle 16 (1985) 221
BOSS 20 (1987) 161
Bottom-up assembly 16 (1985) 83
Bottom-up design 10 (1982) 153
Bounded buffer algorithm 15 (1985) 21
Branch and bound algorithm 25 (1989) 301
Branch execution 24 (1988) 573
Branch instructions execution 24 (1988) 565
Branch target buffer 24 (1988) 573
BTL-electrical equipment testing language 6 (1980) 391
Buffer memories 17 (1986) 139
Buffer pool 7 (1981) 229
Building access control 12 (1983) 111
Building expert systems 21 (1987) 531
Built-in test equipment (BITE) 6 (1980) 168
Bus access time 12 (1983) 167
Bus allocation 7 (1981) 32
Bus analyzer processor 19 (1987) 401
Bus contention 7 (1981) 124
– 13 (1984) 73
Bus hierarchy 23 (1988) 351
Bus monitor 22 (1988) 367
Bus priority resolver 13 (1984) 325
Bus subdivision 17 (1986) 277
Bus system 21 (1987) 333
Butterfly 25 (1989) 339
Butterfly network 24 (1988) 353
BW-monitor design 24 (1988) 717

C++ 25 (1989) 323
C-MOVE architecture 11 (1983) 227
– 13 (1984) 267
C-testability 22 (1988) 277
Cache interleaving 18 (1986) 205
Cache parameters 24 (1988) 541
Cache simulation 22 (1988) 141
Caching 13 (1984) 153
CAD 3(1) (1977) 34
– 10 (1982) 71
– 14 (1984) 117
– 23 (1988) 75
CAD systems 23 (1988) 51
CAD techniques 10 (1982) 139
CAD tool box 18 (1986) 99
CAD tool integration 21 (1987) 585
CAD tools 20 (1987) 179
– 23 (1988) 37
– 24 (1988) 767
CAD user interfaces 21 (1987) 585
CAE workstations 24 (1988) 541
CAI 11 (1983) 117
CAI Basic system 12 (1983) 227
CAMAC 21 (1987) 101
CAMAC crate controller 23 (1988) 153
CAMAC microprocessor 12 (1983) 291
Cambridge ring 13 (1984) 97
CAN 21 (1987) 111
Capability based addressing system 11 (1983) 69
Cardiac output meter 5 (1979) 265
Carrier sense multiple access scheme 18 (1986) 469
Carry detection 22 (1988) 75
CASS8 cross assembler 3(2) (1977) 80
CCD optical arrays 5 (1979) 243
Cell library 18 (1986) 123
Cellular automata 13 (1984) 179

(Cellular automata) 22 (1988) 277
Cellular logic array 13 (1984) 179
Cellular processing array 25 (1989) 113
Cellular processing arrays fault tolerance 25 (1989) 113
Cellular system 16 (1985) 301
Central processor 18 (1986) 591
Central system element (CSE) 7 (1981) 229
Centralized architectures 15 (1985) 277
Centralized control 17 (1986) 285
Character generator ROM 9 (1982) 93
CHILL concurrency 15 (1985) 233
Chill parallel constructs 12 (1983) 159
Chinese characters 8 (1981) 23
Chinese encoding system 8 (1981) 23
Chip architecture 14 (1984) 67
– 23 (1988) 47
Chip design 23 (1988) 51
Chip image 24 (1988) 29
Chip layout 21 (1987) 31
Chip planning 18 (1986) 99
Chromatography 21 (1987) 153
CIM 21 (1987) 433
– 23 (1988) 199
Circuit extraction 21 (1987) 445
Circuit level 16 (1985) 67
Circuit optimization 25 (1989) 171, 173
Circuit verification 21 (1987) 445
Circuits analysis 10 (1982) 139
CISC architectures 14 (1984) 1
CISC like instruction set 25 (1989) 121
Clique partition 21 (1987) 647
Cluster analysis 22 (1988) 347
Clusters 19 (1987) 129
CMOS 20 (1987) 153
– 24 (1988) 233, 339
CMOS circuit verification 21 (1987) 445
CMOS gate array 21 (1987) 445
CMOS IC 21 (1987) 463
CMOS logic circuits 25 (1989) 171
CMOS switch graphs 21 (1987) 445
CMOS systolic implementation 25 (1989) 97
CMOS technology 18 (1986) 441
– 21 (1987) 57
– 24 (1988) 637
CMOVE architecture 6 (1980) 373
Code expansion 8 (1981) 149
Code generation 7 (1981) 13, 37
– 15 (1985) 47
– 21 (1987) 541
Code optimization 9 (1982) 161
– 17 (1986) 29
– 19 (1987) 367
– 22 (1988) 39
Code scheduling 22 (1988) 39
Codeblock 21 (1987) 319
Coding 22 (1988) 97
Collision-free technique 12 (1983) 167
Colour picture converter 19 (1987) 27
Combined architecture 25 (1989) 287
Combined programming 21 (1987) 419
Common channel data processing 7 (1981) 220
Common databases 25 (1989) 4
Communicating circuit 18 (1986) 173
Communicating systems 11 (1983) 287
Communication 22 (1988) 187
Communication circuit 24 (1988) 103
Communication issues 25 (1989) 239
Communication kernels 23 (1988) 233
Communication link 9 (1982) 309
Communication memory 24 (1988) 503
Communication networks 10 (1982) 325
Communication protocols 8 (1981) 93
– 11 (1983) 195
Communication strategy 24 (1988) 11
Communication subnet 18 (1986) 479
Communication transport system 25 (1989) 323
Communication virtual processor 24 (1988) 511
Communications cluster controller 16 (1985) 163
Communications Q-bus interface 20 (1987) 55
Compact disk 21 (1987) 405
Compaction 17 (1986) 61
Companion relation 16 (1985) 17
Compatibility class 13 (1984) 115
– 16 (1985) 17
Compilation 7 (1981) 13, 110
Compilation algorithm 18 (1986) 123
Compilation approach 21 (1987) 577
Compile-time parameterization 18 (1986) 131
Compiler 21 (1987) 275
– 22 (1988) 39, 75
– 24 (1988) 471
Compiler portability 13 (1984) 249
– 16 (1985) 221
Complex arithmetics 21 (1987) 91
Complex associative processing systems 6 (1980) 381
Complex number multiplier 21 (1987) 91
Complexity 21 (1987) 371
Component allocation 21 (1987) 647
Component control 9 (1982) 85
Composite images 19 (1987) 19

Computation problem 20 (1987) 107
Computational model 21 (1987) 541
– 24 (1988) 599
Computer aided design 18 (1986) 417
Computer aided design of digital systems 12 (1983) 15
Computer aided evaluation of simulated results 21 (1987) 471
Computer aided instruction 16 (1985) 31
– 19 (1987) 417
Computer aided learning 16 (1985) 1
Computer aided manufacturing (CAM) 18 (1986) 647
Computer aided microprocessor microprogramming facility (CAMP) 9 (1982) 319
Computer architecture 1(1) (1975) 15
– 9 (1982) 85
– 10 (1982) 333
– 11 (1983) 177
– 13 (1984) 1
– 14 (1984) 145
– 17 (1986) 1
– 18 (1986) 107, 263
– 22 (1988) 39
– 24 (1988) 309
– 25 (1989) 209
Computer architecture implementation 2(2) (1976) 14
Computer architecture performance evaluation 14 (1984) 133
Computer architecture redefinition 6 (1980) 98
Computer architectures for AI 18 (1986) 71
Computer assisted instruction 6 (1980) 209
– 7 (1981) 82
– 11 (1983) 117
Computer assisted learning 6 (1980) 205
– 7 (1981) 92
– 14 (1984) 267
Computer at school (USSR) 8 (1981) 1
Computer audio research laboratory 24 (1988) 443
Computer based education in France 7 (1981) 82
Computer communication interface 1(5) (1975) 37
Computer control 5 (1979) 206
– 10 (1982) 261
– 21 (1987) 433
Computer control systems 22 (1988) 353
Computer design 12 (1983) 43
Computer education 9 (1982) 123
– 12 (1983) 217
– 14 (1984) 249, 251
Computer emulator 4 (1978) 133
Computer evaluation 19 (1987) 49
Computer graphics 6 (1980) 406, 410
Computer graphics algorithms 25 (1989) 61
Computer hardware 22 (1988) 125
Computer hardware description languages 20 (1987) 161
Computer literacy 9 (1982) 123
– 11 (1983) 1
– 14 (1984) 255
Computer managed instruction (CMI) 11 (1983) 117
Computer modeling 13 (1984) 299
Computer music 24 (1988) 443
Computer music programming 25 (1989) 65
Computer networks 19 (1987) 143
– 23 (1988) 373
– 24 (1988) 103, 197
Computer organization 19 (1987) 417
Computer performance 14 (1984) 133
Computer performance assessment 3(1) (1977) 27
Computer pool 25 (1989) 297
Computer science 24 (1988) 835
Computer science education 6 (1980) 78
Computer system organization 10 (1982) 333
Computer-terminal interface 2(1) (1976) 23
Computer vision 24 (1988) 403
Computers and literacy 11 (1983) 1
Computers in education 11 (1983) 1
Computers in society 11 (1983) 1
Concurrence 22 (1988) 187
Concurrency 21 (1987) 623
Concurrency control 14 (1984) 9
Concurrency control scheme 25 (1989) 233
Concurrent architecture 18 (1986) 515
Concurrent Euclid 23 (1988) 267
Concurrent execution 17 (1986) 285
Concurrent language 25 (1989) 288
Concurrent Pascal 6 (1980) 144
– 10 (1982) 33
– 12 (1983) 245
– 14 (1984) 155
Concurrent Pascal intermediate language (CCP) 8 (1981) 229
Concurrent primitives 24 (1988) 257
Concurrent process communication 11 (1983) 69

Concurrent processes 14 (1984) 55
– 21 (1987) 339
Concurrent processes specifications 21 (1987) 541
Concurrent processing 21 (1987) 171, 549
Concurrent processors 15 (1985) 277
Concurrent program design 24 (1988) 51, 809
Concurrent program flow checking 24 (1988) 783
Concurrent programming 21 (1987) 197
– 22 (1988) 233
– 25 (1989) 381
Concurrent signal processing 15 (1985) 141
Concurrent system 18 (1986) 185
– 21 (1987) 393
– 23 (1988) 305
Concurrent system operation display 23 (1988) 173
Concurrent system specification 24 (1988) 483
Conditional probability of events 13 (1984) 189
Conflict resolution strategies 18 (1986) 463
Congruential method 22 (1988) 147
Conical scan tracking radars 23 (1988) 327
Connection assignment 21 (1987) 497
Connection machine 25 (1989) 198
Connection oriented network service 23 (1988) 367
Constructive algebraic specification 21 (1987) 231
Consumer market product 21 (1987) 405
Consumer systems 21 (1987) 413
Context-free parsing 9 (1982) 281
Continuous dynamical system simulation 3(4) (1977) 75
Control address 1(1) (1975) 29
Control algorithm 23 (1988) 57
Control application 3(3) (1977) 51
– 18 (1986) 213, 657
Control design styles 15 (1985) 277
Control flow 18 (1986) 319
Control flow checking 20 (1987) 249
Control logic 1(1) (1975) 21
Control memory 9 (1982) 161
– 23 (1988) 9
Control network 21 (1987) 427
Control network system 21 (1987) 161, 325
Control part design 10 (1982) 163
– 12 (1983) 15
Control process 18 (1986) 173
Control statements 23 (1988) 267
Control store 23 (1988) 21
Control structure 18 (1986) 335
Control structure execution 12 (1983) 9
Control synthesis 18 (1986) 347
Control system 3(3) (1977) 60
– 7 (1981) 169
Control tasks 20 (1987) 65
Control tools 2(2) (1976) 38
Control variable 23 (1988) 207
Controlflow-dataflow evaluation 19 (1987) 385
Controllability 20 (1987) 287
Controlled reduction model 24 (1988) 607
Controller 23 (1988) 9
Controller network development 21 (1987) 463
Convergence of service modes 10 (1982) 299
Conversations 22 (1988) 175
Convolution 20 (1987) 133
Cooker programmers 21 (1987) 413
Coprocessing 21 (1987) 593
Coprocessor 21 (1987) 657
– 23 (1988) 143
– 24 (1988) 599
Coprocessor architecture 21 (1987) 267, 657
Coprocessor for unification 24 (1988) 607
Coprocessor system 15 (1985) 129
Coprocessors design 21 (1987) 657
Coprocessors design for GaAs 21 (1987) 657
CORAIL building block system 2(4) (1976) 29
Corn silo control 9 (1982) 271
Correlation measurement 11 (1983) 23
Correlator array 25 (1989) 89
Cost-driven machine 16 (1985) 239
Cost-effectiveness of microprocessors 4 (1978) 9
Cost efficiency 19 (1987) 1
Cost/performance ratio 16 (1985) 295
CP/M 23 (1988) 191
CPU-coprocessor communication and synchronization 24 (1988) 695
CPU intensive software functions 12 (1983) 185
CPU selftest 23 (1988) 167
CRISC 23 (1988) 239
Criterion mastery testing 11 (1983) 117
Critical delay estimation 16 (1985) 61
Critical signal path 25 (1989) 174
Cross-assembler 1(5) (1975) 25
– 3(4) (1977) 88
Cross-assembly system 5 (1979) 82
Cross-impact analysis 13 (1984) 189
Cross-macroassembler 2(4) (1976) 65
Crosscorrelation 11 (1983) 23
CRT terminal 8 (1981) 23
Cryptography processor 18 (1986) 583

CSA networks 21 (1987) 453
CSMA/CD network 20 (1987) 69
CSP 22 (1988) 233
C_TPDL* 18 (1986) 39
Custom CMOS IC 21 (1987) 463
Cybernectic feedback loops 13 (1984) 227
Cycle stealing 17 (1986) 277
Cyclic buffer 15 (1985) 21

D-algorithm 15 (1985) 119
DA package 18 (1986) 123
Daisy architecture 17 (1986) 285
– 23 (1988) 95
Data acquisition 8 (1981) 11
– 12 (1983) 207
– 16 (1985) 245, 255, 261
– 18 (1986) 515
– 21 (1987) 153
Data acquisition automation 24 (1988) 549
Data acquisition systems 5 (1979) 350
– 8 (1981) 15
Data analysis 11 (1983) 301
Data architecture 23 (1988) 57
Data communication 15 (1985) 85
Data communication network 16 (1985) 249
Data compression 6 (1980) 314
– 21 (1987) 141
Data dependency 17 (1986) 29, 61
Data driven machine 16 (1985) 127
– 20 (1987) 99
Data extraction 21 (1987) 161
Data General Eclipse 4 (1978) 82
Data management kernel 25 (1989) 55
Data management systems 6 (1980) 226
Data modem 5 (1979) 100
Data modem design 16 (1985) 143
Data numerical processing 25 (1989) 133
Data processing 8 (1981) 15
– 12 (1983) 143
– 16 (1985) 277
Data rasterization 21 (1987) 57
Data searching 24 (1988) 655
Data selection 10 (1982) 255
Data storage 16 (1985) 245
Data storage management 16 (1985) 107
Data transformation 10 (1982) 255
Data transmission 12 (1983) 267
– 15 (1985) 57
Data type specification language 21 (1987) 231
Database 10 (1982) 299
– 24 (1988) 599
Database design 25 (1989) 3
Database file structure 25 (1989) 5
Database general model 25 (1989) 3
Database machine 7 (1981) 104
– 10 (1982) 255
– 25 (1989) 33
Database machine architecture 25 (1989) 33
Database management 12 (1983) 99
Database modeling 24 (1988) 249
Database Prolog system 25 (1989) 9
Database software configuration 25 (1989) 5
Dataflow 16 (1985) 163
– 19 (1987) 385
– 21 (1987) 319, 433, 577
– 24 (1988) 663
Dataflow architecture 25 (1989) 133
Dataflow computing system 7 (1981) 119
Dataflow graphs 21 (1987) 577
Dataflow language 7 (1981) 20
Dataflow machine 25 (1989) 91
Dataflow microprogramming 12 (1983) 181
Dataflow principle 18 (1986) 299
Dataflow processor 14 (1984) 17
– 21 (1987) 319
Dataflow processor emulation 21 (1987) 319
Datagram service 20 (1987) 59
Datapath 21 (1987) 647
Datapath geometry 7 (1981) 20
Datapath synthesis 21 (1987) 577
DBMS 9 (1982) 17
– 25 (1989) 3
DC energy meter 5 (1979) 239
DC motor 19 (1987) 227
Deadlock 25 (1989) 139
Deadlock-free operation scheme 2(3) (1976) 56
Debugging 3(1) (1977) 59
– 3(3) (1977) 46
– 4 (1978) 220
– 5 (1979) 93
– 6 (1980) 41
– 8 (1981) 149
– 9 (1982) 77
– 18 (1986) 55
– 21 (1987) 181, 639
– 22 (1988) 243
Debugging tool 23 (1988) 173
Decentralised bus structure 3(3) (1977) 9
Decentralised control 7 (1981) 220
– 22 (1988) 217
Decentralised input-output subsystems 11 (1983) 69
Decimal arithmetic 22 (1988) 75
Decimal to binary conversion 19 (1987) 119

Decision mechanisms 24 (1988) 801
Decision nodes 23 (1988) 207
Decision table 18 (1986) 617
Declarative programming 24 (1988) 663
Decoding 22 (1988) 97
DECVAX systems 20 (1987) 73
Dedicated architecture 21 (1987) 129
Dedicated computer architecture 21 (1987) 129
Dedicated digital systems 5 (1979) 315
Dedicated instruction set 8 (1981) 257
Dedicated microcomputers 6 (1980) 149
Dedicated microprogrammed controller 9 (1982) 161
Dedicated processor 2(3) (1976) 24
– 22 (1988) 187
Deductive databases 24 (1988) 599
Delay minimization 20 (1987) 127
– 21 (1987) 83
Delayed branch 22 (1988) 39
Delft parallel processor 21 (1987) 313
Demodulation 15 (1985) 57
Demodulation and filtering 23 (1988) 327
Demonstration programs 9 (1982) 281
DEOS 24 (1988) 241
Dependency graph 22 (1988) 39
Description languages for hardware 21 (1987) 453
Description of circuits 21 (1987) 569
Description of hardware 21 (1987) 585
Design aids 15 (1985) 203
Design alternatives 23 (1988) 75
Design automation 21 (1987) 639
– 22 (1988) 217
Design automation language 20 (1987) 173
Design complexity 22 (1988) 277
Design considerations 21 (1987) 267
Design for image processing 25 (1989) 44
Design for testability 15 (1985) 119
– 22 (1988) 277
– 24 (1988) 233
Design language 24 (1988) 35
Design method 9 (1982) 259
– 15 (1985) 203, 277
Design methodology 16 (1985) 277
– 18 (1986) 427
– 21 (1987) 463, 569
– 24 (1988) 43
Design of coprocessors 21 (1987) 657
Design of extremely fast memory 21 (1987) 405
Design of instructional systems 14 (1984) 255
Design of machine control systems 21 (1987) 325
Design of manufacturing systems 21 (1987) 433
Design process 24 (1988) 219
Design regularity 16 (1985) 101
Design rule checking 19 (1987) 85
Design specification 24 (1988) 219
– 25 (1989) 157
Design tools 24 (1988) 29
Design transformation 10 (1982) 153
Design verification 10 (1982) 153
– 18 (1986) 99
Desk-size computing resources 21 (1987) 383
Development aids 7 (1981) 58
Development environment for expert system 25 (1989) 21
Development networks 21 (1987) 111
Development system 3(4) (1977) 77
– 14 (1984) 145
– 21 (1987) 639
Development system design 5 (1979) 93
Development system loader and tester 5 (1979) 225
Development tools 21 (1987) 111
Device-modeling 10 (1982) 139
DG Eclipse S/130 6 (1980) 179
Diagnosability 7 (1981) 177
– 20 (1987) 323
– 21 (1987) 489, 497
Diagnosable systems 21 (1987) 497
Diagnosis by comparison 18 (1986) 271
Diagnostic interface 3(1) (1977) 59
Dial-up services 10 (1982) 299
Didactic microcomputer 12 (1983) 217
Diesel engine 14 (1984) 187
Difficulty of microprogramming 8 (1981) 149
Diffusion furnace 6 (1980) 325
Digital circuit understanding 25 (1989) 4
Digital circuits 10 (1982) 101
Digital control 6 (1980) 111
Digital filter 14 (1984) 17, 67
– 21 (1987) 463
– 25 (1989) 97, 107
Digital filtering 16 (1985) 143
Digital hardware design 6 (1980) 51
Digital hardware synthesis 19 (1987) 305
Digital IC testing 12 (1983) 285
Digital image processing 6 (1980) 232
– 19 (1987) 19, 27
Digital measurement systems 6 (1980) 283
Digital processors 23 (1988) 33
Digital recording 25 (1989) 281
Digital signal processing 12 (1983) 267
– 21 (1987) 161

(Digital signal processing) 23 (1988) 221
Digital signal processor 22 (1988) 347
Digital switch 18 (1986) 591
Digital switching 15 (1985) 113
Digital switching system 10 (1982) 311
– 11 (1983) 207
Digital system controller 23 (1988) 57
Digital systems design 24 (1988) 227
Digital systems specification 25 (1989) 151
Digital tape recorder 23 (1988) 333
Digital test system controller 4 (1978) 162
Dimensional design 23 (1988) 305
Dimensions recovery 25 (1989) 113
Direct execution 9 (1982) 143
Direct execution architecture 18 (1986) 263
Direct mainframe-network connection 9 (1982) 263
Direct memory access 13 (1984) 331
– 17 (1986) 277
Disability applications 13 (1984) 227
Disabled education 13 (1984) 221
Disassembler 22 (1988) 243
Discrete Fourier transform 18 (1986) 275
– 20 (1987) 133
Discrete Markov chain 13 (1984) 171
Discrete simulation 15 (1985) 203
Discrete simulation systems 16 (1985) 67
Discrete time controller synthesis 10 (1982) 229
DISE workshop on microprocessor education 3(2) (1977) 8
Disk controller 18 (1986) 491
– 25 (1989) 55
Disk file organization 23 (1988) 191
Disk I/O 17 (1986) 153
Disk storage management 18 (1986) 499
Dispatch tables 23 (1988) 9
Dispatcher 7 (1981) 229
– 22 (1988) 11
Display terminal performance 17 (1986) 129
Distributed applications 7 (1981) 13
Distributed arbitration 13 (1984) 21
Distributed architecture 2(4) (1976) 19
– 9 (1982) 143
– 21 (1987) 591
Distributed associative network 24 (1988) 613
Distributed bus arbiter 7 (1981) 32
Distributed clock 19 (1987) 153
Distributed communication service 25 (1989) 319
Distributed computation 24 (1988) 491
Distributed computer system 16 (1985) 239
Distributed computing system 2(4) (1976) 5
– 10 (1982) 247
– 19 (1987) 143
Distributed control 14 (1984) 181
– 15 (1985) 113
– 20 (1987) 69
– 23 (1988) 227
– 24 (1988) 607
Distributed control network 21 (1987) 427
Distributed control systems software 5 (1979) 358
Distributed data acquisition 12 (1983) 211
Distributed databases 24 (1988) 249
Distributed debugging 25 (1989) 307
Distributed diagnosis 14 (1984) 139
– 21 (1987) 489
Distributed diagnostic 21 (1987) 75
Distributed environments 23 (1988) 233
Distributed global operating system 25 (1989) 295
Distributed intelligence 7 (1981) 92
– 23 (1988) 351
Distributed interconnection 23 (1988) 243
Distributed logic 19 (1987) 407
Distributed memory 5 (1979) 73
Distributed microprocessor control 11 (1983) 207
Distributed office environment 23 (1988) 311
Distributed operating system 10 (1982) 247
– 21 (1987) 205
– 22 (1988) 187
– 24 (1988) 169, 179, 187, 519
Distributed priority networks 13 (1984) 21
Distributed processing 15 (1985) 85
– 21 (1987) 427
Distributed program 19 (1987) 219
– 21 (1987) 287
– 24 (1988) 85
Distributed programming 23 (1988) 279
Distributed programming language 10 (1982) 247
Distributed software fault tolerant real time systems 24 (1988) 801
Distributed switching system 14 (1984) 181
Distributed system 9 (1982) 201
– 10 (1982) 19
– 11 (1983) 195
– 14 (1984) 29
– 18 (1986) 479
– 19 (1987) 49
– 20 (1987) 39
– 21 (1987) 481
– 22 (1988) 65
– 24 (1988) 511, 731, 793

(Distributed system) 25 (1989) 239
Distributed systems programming 22 (1988) 233
Distributed telephone exchange control system 3(4) (1977) 79
Distribution of live nodes 25 (1989) 301
Distribution problem 12 (1983) 267
Distribution program 22 (1988) 263
Divided differences 22 (1988) 115
DMA 13 (1984) 97
Documentation 21 (1987) 223
Domestic appliance controllers 21 (1987) 413
Domestic informatics 7 (1981) 211
Domino effect 14 (1984) 29
DPCM 21 (1987) 141
DS board 25 (1989) 47
DSP 24 (1988) 325
– 25 (1989) 41
Dual port memory 7 (1981) 229
– 17 (1986) 277
– 25 (1989) 139
Duplex 11 (1983) 31
Dynamic alternate selection 16 (1985) 217
Dynamic architecture 12 (1983) 153
Dynamic characterization of A/D converters 25 (1989) 335
Dynamic configuration 18 (1986) 455
Dynamic microoperation priority weight function 22 (1988) 159
Dynamic microprogramming 4 (1978) 322
– 6 (1980) 98
– 8 (1981) 189, 219
– 13 (1984) 153
– 15 (1985) 195
Dynamic MOS 16 (1985) 89
Dynamic RAM 10 (1982) 119
– 17 (1986) 277
Dynamic reconfiguration 21 (1987) 623
Dynamic resource management 25 (1989) 253
Dynamic vertical migration 20 (1987) 197

E78 bus 6 (1980) 92
Easily retargetable crosscompiler 11 (1983) 217
Eclipse S-200 virtual memory 4 (1978) 141
Edge correction 19 (1987) 19
Edge detection 19 (1987) 19
Edge prediction 21 (1987) 141
EDIF 23 (1988) 15
Edit functions 15 (1985) 75
Editing and file handling system 2(4) (1976) 54
Editor specifications 16 (1985) 107
Educated consumer 10 (1982) 299
Education in firmware engineering 8 (1981) 153
Educational software design criteria 12 (1983) 223
Educational technology 15 (1985) 267
EEG analysis 9 (1982) 267
Effects of automation 11 (1983) 1
Efficiency evaluation 10 (1982) 163
Electric utility 19 (1987) 101
Electrical batteries test unit 5 (1979) 380
Electrical measuring instruments 5 (1979) 302
Electronic distributed switching systems (EDSS) 6 (1980) 115
Elimination of instruction address transfer 19 (1987) 407
Embedded arrays 18 (1986) 441
Embedded ASICs 24 (1988) 449
Embedded computer system 18 (1986) 63
Embedded control systems 24 (1988) 663
Embedded control systems programming 24 (1988) 663
Embedded microcomputers 21 (1987) 413
Embedded multiprocessor systems 24 (1988) 85
Embedded P systems 21 (1987) 181
Embedded system 18 (1986) 55
– 21 (1987) 301, 391, 393
– 24 (1988) 51, 775, 809
Emission monitoring system 6 (1980) 308
Emulation 1(1) (1975) 5
– 6 (1980) 179
– 9 (1982) 257
– 13 (1984) 153
– 18 (1986) 55
– 21 (1987) 319
Emulators 14 (1984) 145
Encapsulation 21 (1987) 515
Engineering database 21 (1987) 383
Engineering environment 21 (1987) 361
EPCOS communication system 14 (1984) 173
E2PROM 10 (1982) 129
EPROM 3(3) (1977) 68
– 10 (1982) 129
EPRON 1(1) (1975) 29
Equilateral triangles 22 (1988) 115
Error controlling device 6 (1980) 395
Error correcting code 20 (1987) 15
Error coverage 20 (1987) 331
Error detection 11 (1983) 31
– 23 (1988) 63
– 24 (1988) 629

Error detection codes 20 (1987) 331
Error detection technique 20 (1987) 3
Error handling 17 (1986) 285
Error recovery 11 (1983) 31
– 21 (1987) 481
– 23 (1988) 261
Error recovery routines 16 (1985) 107
Error retracing 24 (1988) 461
Error treatment 7 (1981) 110
Ethernet 13 (1984) 97
Ethernet protocols 20 (1987) 73
Evaluation mechanism 21 (1987) 569
Event flow computation 18 (1986) 27
Event train 11 (1983) 23
Exception handling 20 (1987) 85
Executable models 25 (1989) 307
Execution concurrency 12 (1983) 181
Execution tracing 22 (1988) 243
Expectation generator 25 (1989) 19
Expected behaviour 21 (1987) 471
Expert shell 21 (1987) 523
Expert system 14 (1984) 267
– 18 (1986) 107, 417
– 23 (1988) 47
Expert system development 25 (1989) 21, 27
Exploratory chip design 23 (1988) 75
Extendable instruction set 21 (1987) 23
Extended cube connected cycles 10 (1982) 325
Extremely fast memory 21 (1987) 405

Fail-safe systems 12 (1983) 279
Fail-soft degradation 20 (1987) 11
False polarity reversal 6 (1980) 34
False reversals 6 (1980) 37
Fast control units 14 (1984) 211
Fast digital design simulation 18 (1986) 27
Fast Fourier transform 24 (1988) 353
Fault detection 20 (1987) 249
– 22 (1988) 277
Fault diagnosis 20 (1987) 39, 301, 323
– 21 (1987) 497
– 24 (1988) 527
Fault identification algorithm 21 (1987) 497
Fault identification for t-diagnosable systems 22 (1988) 273
Fault isolation 21 (1987) 333
Fault localization 24 (1988) 461
Fault model concurrent test 20 (1987) 309
Fault modeling 24 (1988) 233
Fault propagation 24 (1988) 723
Fault recovery 24 (1988) 775
Fault simulation 14 (1984) 117
Fault tolerance 6 (1980) 388
– 7 (1981) 177
– 9 (1982) 315
– 11 (1983) 31, 195, 287
– 12 (1983) 153, 279
– 13 (1984) 315
– 14 (1984) 29, 99, 109
– 16 (1985) 133, 239, 255
– 17 (1986) 205
– 19 (1987) 335
– 20 (1987) 11, 113, 271, 287, 301, 323
– 21 (1987) 73, 83, 91, 333, 339, 481
– 23 (1988) 233
– 24 (1988) 519
Fault tolerance computing 15 (1985) 11
Fault tolerance design 25 (1989) 92
Fault tolerance in architectures 18 (1986) 275
Fault tolerant 6 (1980) 243
– 24 (1988) 629
Fault tolerant architecture 18 (1986) 289
– 21 (1987) 75
Fault tolerant clock 11 (1983) 31
Fault tolerant computing 18 (1986) 63
– 22 (1988) 273
Fault tolerant CPU 20 (1987) 3
Fault tolerant dataflow machine 25 (1989) 91
Fault tolerant distributed computer systems 20 (1987) 25
Fault tolerant multiplier 20 (1987) 15
Fault tolerant networks 20 (1987) 331
Fault tolerant programs 20 (1987) 33
Fault tolerant system 16 (1985) 7
– 21 (1987) 197
Fault tolerant systolic implementation 25 (1989) 97
Faulty comparators 18 (1986) 271
Field bus 21 (1987) 111
Field utilisation factor 13 (1984) 115
Fifth compiler 24 (1988) 73
Fifth programming environment 14 (1984) 169
Fifth programming language 13 (1984) 255
File management 25 (1989) 55
File organization 11 (1983) 127
File transfer capacity 20 (1987) 73
Film editing device 3(3) (1977) 14
Filtering convolution algorithm 25 (1989) 98
Financial modelling 19 (1987) 101
Finite automation 22 (1988) 125
Finite state machines 20 (1987) 179
FIR 21 (1987) 463

FIR filter 25 (1989) 97
Firmware 8 (1981) 179, 189, 203
– 17 (1986) 61
Firmware design 8 (1981) 153
Firmware development system 19 (1987) 291
Firmware engineering 8 (1981) 167, 203, 237
– 22 (1988) 243
Firmware (microcode) 12 (1983) 33
Firmware monitor 8 (1981) 237
Firmware monitoring 22 (1988) 141
Firmware specification 8 (1981) 153
Firmware support 23 (1988) 267
Firmware validation 8 (1981) 153
Fixed point processor 24 (1988) 695
Flexible architecture 21 (1987) 65
Flexible disk interface 17 (1986) 153
Flexible manufacturing system 21 (1987) 295
Flexible multiprocessor bus 21 (1987) 325
Floating point arithmetic 23 (1988) 179
Floating point coprocessor 24 (1988) 695
Floating point multiplier 21 (1987) 313
Floating point operations (FLOPS) 5 (1979) 48
Floorplan 25 (1989) 160
Floppy disk controller 14 (1984) 195
Floppy disk 8X330 controller chip 5 (1979) 11
Flow analysis 17 (1986) 61
Flow chart 3(1) (1977) 34
Flow graph 17 (1986) 61
Flowsheet 3(3) (1977) 60
Folding of PLAs 12 (1983) 15
Font editor 11 (1983) 233
Formal description 1(3) (1975) 17
– 9 (1982) 259
Formal description language 24 (1988) 363
Formal description techniques 24 (1988) 51, 809
Formal methods 21 (1987) 231
Formal methods techniques 21 (1987) 211
Formal modelling of telematic services 25 (1989) 313
Formal reasoning 21 (1987) 453
Formal specification 21 (1987) 213, 223, 241
FORTH language 14 (1984) 165
FORTH-like languages 13 (1984) 31
FORTRAN 4 (1978) 27
Fourth generation software 8 (1981) 145
Frame store 19 (1987) 27
Frequency coding 13 (1984) 153
Frobenius and companion matrices 22 (1988) 23
Front end processor 9 (1982) 231
Function language 24 (1988) 227
Function microcomputers 6 (1980) 410
Function tree 19 (1987) 27
Functional blocks 23 (1988) 33
Functional language directed machine 16 (1985) 127
– 20 (1987) 99
Functional microprocessor architecture 24 (1988) 149
Functional modularity 21 (1987) 325
Functional programming 10 (1982) 333
– 21 (1987) 419
Functional programs 24 (1988) 613
Functional reliability model 8 (1981) 83
Future architecture 21 (1987) 7
Fuzzy C-means algorithm 24 (1988) 647
Fuzzy clustering 24 (1988) 647
FWT butterfly 25 (1989) 339

GaAs based design 21 (1987) 655
GaAs computer design 21 (1987) 655
GaAs implementation in RISC 21 (1987) 31
GaAs implementation of 32 bit RICS 21 (1987) 31
GaAs processor design 25 (1989) 127
GaAs RISC architecture 21 (1987) 665
GaAs technology 17 (1986) 119
– 21 (1987) 657
– 22 (1988) 385
Galileo nets 24 (1988) 483
Gallium arsenide (GaAs) 20 (1987) 121
– 21 (1987) 31
Gaps in a pipeline 9 (1982) 155
Gas-liquid chromatography 21 (1987) 153
Gate arrays 20 (1987) 287
– 21 (1987) 445
Gate level modeling 25 (1989) 171
Gate level simulation 19 (1987) 417
Gate level simulator 18 (1986) 47
GD80-multimicroprocessor architecture 6 (1980) 406
General heap processor 20 (1987) 217
General operating systems 14 (1984) 45
General purpose communications processor 3(3) (1977) 3
General purpose controller for industrial processes 6 (1980) 399
General purpose cross-assembler (GPCA) 4 (1978) 222
General purpose simulators 10 (1982) 139
Generalized layout/circuit relations 23 (1988) 15
Generic design 25 (1989) 161

Geographical distributed network 23 (1988) 379
Geometric transformations 25 (1989) 163, 166
Geometrical programming 21 (1987) 419
Gifted education 13 (1984) 221
Global fanout correction 24 (1988) 43
Global microcode compaction 17 (1986) 19
Global operating system 25 (1989) 295
Global optimization 24 (1988) 339
GPIB communications 9 (1982) 215
Graph based algorithm 21 (1987) 647
Graph description language 25 (1989) 165
Graph strings 25 (1989) 166
Graphic display 10 (1982) 261
Graphic objects 11 (1983) 233
Graphic preprocessing knot 5 (1979) 375
Graphic surface modeling 25 (1989) 265
Graphic system 5 (1979) 44
Graphic workstation 23 (1988) 357
Graphical comparison of microprocessors 3(3) (1977) 84
Graphical description language 18 (1986) 631
Graphical items transformation 14 (1984) 215
Graphical kernel system 14 (1984) 205
Graphical programming 21 (1987) 549
Graphics 23 (1988) 359
Graphics card 25 (1989) 53
Graphics editor MLED 21 (1987) 585
Graphics processor 14 (1984) 205
Graphics terminal 14 (1984) 205
Gray to binary code conversion 16 (1985) 95
Greek alphabet 23 (1988) 103
Grinding machine control system 24 (1988) 663
Guarded commands 22 (1988) 233
Guided synthesis 21 (1987) 541

Hadamard encoded images 11 (1983) 227
– 13 (1984) 267
Hadamard transforms 11 (1983) 227
HALIS 12 (1983) 79
Hamiltonian ring structure 19 (1987) 219
Handicapped education 13 (1984) 221
Handling system 21 (1987) 405
Hard disks 14 (1984) 331
Hard real time applications 12 (1983) 79
Hard real time environments 17 (1986) 277
Hardware accelerated simulator 18 (1986) 27
Hardware advances 7 (1981) 104
Hardware description language 16 (1985) 67
– 18 (1986) 99, 123, 371, 549, 559, 567
– 20 (1987) 183
– 21 (1987) 453
– 22 (1988) 205, 217
– 24 (1988) 219, 371
– 25 (1989) 151, 183
Hardware design 1(1) (1975) 15
– 1(2) (1975) 43
– 21 (1987) 443
– 23 (1988) 57
Hardware design language 18 (1986) 131
Hardware development tool 18 (1986) 417
Hardware distribution 21 (1987) 205
Hardware engineering 3(2) (1977) 16
Hardware implementation 23 (1988) 115
Hardware interpreter 21 (1987) 267
Hardware monitor 10 (1982) 19
Hardware processor 23 (1988) 103
Hardware programming language 18 (1986) 355
Hardware realization of systolic algorithms 24 (1988) 527
Hardware simulation environment 24 (1988) 291
Hardware specification language 18 (1986) 637
Hardware standards 1(3) (1975) 3
Hardware verification 21 (1987) 443, 453
Hardwired logic 2(2) (1976) 1
– 3(2) (1977) 73
– 4 (1978) 167
– 12 (1983) 175
Harvard architecture 19 (1987) 407
Harvard microprocessor architecture 18 (1986) 427
Hash functions 11 (1983) 127
Heterogeneity 22 (1988) 187
Heterogeneous microcomputer system connection 12 (1983) 167
Heterogeneous processing configuration 23 (1988) 291
Heuristic algorithm 25 (1989) 347
Hexagonal arithmetic array processors 24 (1988) 629
HF channels 6 (1980) 395
Hidden cost elements 4 (1978) 4
Hierarchic physical design system 24 (1988) 131
Hierarchical architecture 16 (1985) 267
– 18 (1986) 515
Hierarchical control 21 (1987) 295
Hierarchical control design 21 (1987) 445
Hierarchical control system 21 (1987) 295, 427

Hierarchical design 21 (1987) 445
– 24 (1988) 29
– 25 (1989) 177
Hierarchical graph model 20 (1987) 183
Hierarchical ordering 9 (1982) 221
Hierarchical program systems 19 (1987) 27
Hierarchical structure 16 (1985) 287
High integrity systems 20 (1987) 287
High level debugger 24 (1988) 153
High level debugging 21 (1987) 181
High level language 4 (1978) 67
– 7 (1981) 37, 260
– 9 (1982) 85
– 12 (1983) 153
– 18 (1986) 55, 213, 657
– 19 (1987) 277
– 21 (1987) 639
– 24 (1988) 57, 85
High level language architecture 12 (1983) 33, 143
High level language computer architecture 16 (1985) 113, 127
High level language directed architecture 24 (1988) 701
High level language machine 18 (1986) 263
High level language machine architecture 3(4) (1977) 58
High level language orientated instruction set 6 (1980) 158
High level language programs 22 (1988) 385
High level language support 17 (1986) 1
– 19 (1987) 203
High level language systolic arrays 25 (1989) 79
High level optimization 18 (1986) 347
High level programming language 11 (1983) 69
– 17 (1986) 19
High performance workstations 21 (1987) 7
High performance workstations architecture 21 (1987) 7
High speed execution 21 (1987) 275
High speed processor 12 (1983) 267
High speed simulation 25 (1989) 327
Higher level languages 6 (1980) 28
Higher real time language 15 (1985) 191
Home robot 9 (1982) 285
Homogeneous calculating structures 5 (1979) 73
Horizontal firmware 14 (1984) 223
Horizontal microprogramming 17 (1986) 19, 29
– 22 (1988) 159
Host-computer-subnetwork interface 4 (1978) 283
Host mpu 14 (1984) 67
Host/target environment 18 (1986) 387
Hough transform 24 (1988) 403
Human interfaces 24 (1988) 153
Hungarian microprocessor literature 5 (1979) 125
Hybrid fault situation 21 (1987) 497
Hybrid parallel machine 25 (1989) 229
Hypercube architectures 24 (1988) 213
Hypercube network 25 (1989) 34
Hypercube structure 25 (1989) 198
Hypercube topology 18 (1986) 597

iAPX 432 architecture 15 (1985) 233
iAPX 432 system 12 (1983) 159
IBM 370 architecture 8 (1981) 247
IBM 3270 design 25 (1989) 189
IBM 3270 protocol handler 25 (1989) 189
IBM 370 I/O control 2(3) (1976) 24
IBM PC 24 (1988) 325
IBM PC-XT 19 (1987) 227
– 23 (1988) 153
IC design 23 (1988) 37
– 25 (1989) 157
IEEE 438 9 (1982) 231
IEEE 488 instrument bus 9 (1982) 215
IEEE microprocessor assembly language draft standard 12 (1983) 89
Image compression 21 (1987) 49
Image display 21 (1987) 405
Image management 21 (1987) 119
Image management system 21 (1987) 119
Image modelling 21 (1987) 137
Image processing 19 (1987) 5
– 21 (1987) 47, 65, 127, 141
– 23 (1988) 95
– 24 (1988) 309
– 25 (1989) 41, 277
Image processing array 14 (1984) 125
Image processor 18 (1986) 525
Image projection 10 (1982) 261
Image rasterization 21 (1987) 57
Image reconstruction 21 (1987) 129
Image reduction 16 (1985) 287
Image registration system 24 (1988) 315
Image retrieval 15 (1985) 263
Image segmentation 19 (1987) 19
Image synthesis 13 (1984) 179
Image transforms 11 (1983) 227
– 13 (1984) 267

IMP 2000 industrial microprocessor 6 (1980) 24
Imperative programming languages 13 (1984) 153
Implementation 18 (1986) 59
In circuit emulation 17 (1986) 159
In circuit emulator 13 (1984) 331
In service microprocessor training 9 (1982) 289
Indexed files 22 (1988) 243
Industrial control system 4 (1978) 18
Industrial controllers 24 (1988) 775
Industrial local network 18 (1986) 173
Industrial measurements 6 (1980) 330
Industrial microcomputer 6 (1980) 41
Industrial process control 7 (1981) 241
Industrial process control systms 5 (1979) 295
Industrial systems programming 14 (1984) 155
Information compaction 23 (1988) 167
Information hiding 21 (1987) 515
Information industry 9 (1982) 37
Information management 15 (1985) 1
Information resource management 15 (1985) 1
Information retrieval 11 (1983) 127
– 18 (1986) 623
– 22 (1988) 29
Information retrieval system 7 (1981) 104
– 10 (1982) 255
Information services and use 7 (1981) 211
Information society 8 (1981) 67
Information system 11 (1983) 127
– 17 (1986) 129
Information system design 16 (1985) 215
INMOS 23 (1988) 149
Inner product processor 21 (1987) 313
Inner product space 21 (1987) 313
Input mapping 9 (1982) 161
Input/output microprogramming 2(3) (1976) 3
Instruction execution rate 18 (1986) 205
Instruction issue 19 (1987) 407
Instruction mnemonics 1(3) (1975) 22
Instruction modularity 12 (1983) 199
– 14 (1984) 133
Instruction pipelining 25 (1989) 203
Instruction preinterpretation 19 (1987) 407
Instruction reordering 22 (1988) 39
Instruction set 13 (1984) 153
– 17 (1986) 1
– 21 (1987) 223
Instruction-set based data processing devices 20 (1987) 147
Instruction set computer considerations 16 (1985) 113
Instruction set design considerations 20 (1987) 99
Instruction set evaluation 14 (1984) 1
Instruction set extension 15 (1985) 129
Instruction set processor 24 (1988) 149
Instruction streams 13 (1984) 153
Instructional systems 14 (1984) 267
Instructions interpreter level 21 (1987) 463
Instrumentation 21 (1987) 101, 153
Instrumentation systems 6 (1980) 343
Integrated circuit package 6 (1980) 249
Integrated circuit testing 12 (1983) 285
Integrated circuits 10 (1982) 101
Integrated computer support 18 (1986) 223
Integrated development 5 (1979) 265
Integrated sleep monitor 3(4) (1977) 86
Integrated software 17 (1986) 195
Integration database 21 (1987) 523
Integration of data path 18 (1986) 347
Integration of production 21 (1987) 523
Integration of standard tools 21 (1987) 523
Intel 3000 5 (1979) 225
Intel 80386 19 (1987) 59
Intel 8080 5 (1979) 100
– 6 (1980) 144, 226
Intel 8086 6 (1980) 135
Intel 8080, functional description 2(1) (1976) 30
Intel MCS 80 5 (1979) 225
Intelligence computer tutoring systems 16 (1985) 1
Intelligent disk controller 25 (1989) 55
Intelligent graphics terminal 12 (1983) 175
Intelligent interface 23 (1988) 333
Intelligent knowledge-based systems 14 (1984) 267
Intelligent learning system 16 (1985) 1
Intelligent network communicator 16 (1985) 249
Intelligent sensors design 14 (1984) 191
Intelligent terminal 15 (1985) 85
Intelligent workstation 23 (1988) 351
Intemex architecture 13 (1984) 255
Intensional table 25 (1989) 10
Intention lists 14 (1984) 9
Inter-process communication 18 (1986) 195
Inter-processor communications delay 20 (1987) 113
Interactive compiler 13 (1984) 31, 255
Interactive cross compiler 19 (1987) 367
Interactive development system 18 (1986) 55
Interactive graphics 18 (1986) 445

Interactive graphics microprocessor 12 (1983) 301
Interactive high level debugging system 18 (1986) 401
Interactive microprogram transformations 23 (1988) 3
Interactive programming 14 (1984) 169
Interactive programming tool 24 (1988) 73
Interactive software 13 (1984) 105
Interactive speech 23 (1988) 199
Interactive system 13 (1984) 189
Interactive video 14 (1984) 267
– 15 (1985) 263
Interconnection locality 16 (1985) 101
Interconnection memory management unit 20 (1987) 197
Interconnection network 16 (1985) 133
– 24 (1988) 731
– 25 (1989) 289
Interconnection strategies 10 (1982) 325
Interconnection structures 8 (1981) 93
Interconnection topology 21 (1987) 75
Interface 13 (1984) 97
Interfaces in CAD 21 (1987) 585
Interfacing 21 (1987) 99
Intermediate code 19 (1987) 1
– 21 (1987) 275
Intermediate language 7 (1981) 13
– 16 (1985) 221
Intermodel transformation 24 (1988) 249
Internal communication 4 (1978) 326
Internal data format 14 (1984) 215
International banking 24 (1988) 1?
Interpretation algorithm 10 (1982) 163
Interpretation test 7 (1981) 110
Interpreter 18 (1986) 63
– 19 (1987) 1
– 22 (1988) 379
Interpreter system classification 12 (1983) 3
Interprocess communication 19 (1987) 319
Interprocessor communication 3(3) (1977) 9
– 21 (1987) 613
Interprocessor interrupts 23 (1988) 185
Interrupt 16 (1985) 325
– 19 (1987) 233
– 22 (1988) 11
Interrupt service routines 20 (1987) 85
Interrupting mechanisms 15 (1985) 179
Introduction to microprogramming 1(1) (1975) 5
I/O microprogramming for the 7.755 CPU of Siemens 7000 2(3) (1976) 47
I/O pins 23 (1988) 27
IPSE 18 (1986) 233
IRM 15 (1985) 1
ISDN 21 (1987) 141
ISO/OSI Basic reference model 14 (1984) 45
ISO/OSI session layer 20 (1987) 59
Iterative systems 20 (1987) 141

Keller plan 11 (1983) 117
Kernel 20 (1987) 91
– 22 (1988) 11
– 24 (1988) 161
Kernel action time 15 (1985) 191
Kernel calls 15 (1985) 191
Key stream generating mechanisms 6 (1980) 161
Keyboard-oriented microprogram emulator and tester 4 (1978) 76
Knowledge base 21 (1987) 523
– 25 (1989) 15
Knowledge based systems 24 (1988) 315
Knowledge based VLSI design 25 (1989) 163
Knowledge engineering task 25 (1989) 21
Knowledge representation 6 (1980) 209
Knowledge structuring 13 (1984) 227

Lab computerisation 21 (1987) 153
Laboratory 21 (1987) 101
Laboratory classes 9 (1982) 93
LAM processing 23 (1988) 153
LAN 21 (1987) 119, 613
– 23 (1988) 199, 367
– 25 (1989) 287
LAN traffic generator 18 (1986) 435
Language comparison 12 (1983) 79
Language coprocessor 21 (1987) 593
– 24 (1988) 701
Language design 21 (1987) 559
– 23 (1988) 299
Language extension 15 (1985) 191
Language for transputer networks 21 (1987) 559
Language implementation 21 (1987) 559
Language interpretation 7 (1981) 110
Language requirements 12 (1983) 79
Language translation 18 (1986) 263
Large microprocessor system 18 (1986) 667
Large scale integration (LSI) 1(1) (1975) 9
Laser radar 24 (1988) 411
Laser range finder 19 (1987) 193
Lay education 13 (1984) 221
Layer assignment 16 (1985) 345

Layout generation 16 (1985) 61
Layout language 18 (1986) 445
Layout placement 25 (1989) 163
Layout processing VLSI 21 (1987) 65
Layout representation 25 (1989) 160
Layout topology 20 (1987) 189
LCD 21 (1987) 413
LCD display 21 (1987) 413
LEAD test method 1(5) (1975) 15
Learning algorithms 14 (1984) 145
Learning difficulties 6 (1980) 215
Learning systems 5 (1979) 370
Level protocol 23 (1988) 367
Levels of interpretation 10 (1982) 163
Life cycle model 21 (1987) 361
Life cycle of software 21 (1987) 371
Lifetime analysis 21 (1987) 577
Linear directory 23 (1988) 191
Linear equations 5 (1979) 53
– 10 (1982) 25
Linear feedback shift register 18 (1986) 435
– 20 (1987) 309
Linear sequential systems 4 (1978) 167
Linear testability 25 (1989) 85
Link strategy 8 (1981) 93
Linkage editor 6 (1980) 28
LISP 12 (1983) 9
– 22 (1988) 379
LISP 1.5 18 (1986) 81
LISP interpreter 18 (1986) 81
List scheduling algorithms 17 (1986) 29
Live nodes distribution 25 (1989) 301
Local and global microcode compaction 22 (1988) 159
Local area network 11 (1983) 295
– 12 (1983) 167
– 16 (1985) 231, 313
– 18 (1986) 153, 469
– 19 (1987) 1
– 24 (1988) 95, 761
Local clocks 21 (1987) 489
Local computer network 9 (1982) 315
Local detectability 20 (1987) 147
Local instruction buffers 19 (1987) 407
Local microcode compaction 17 (1986) 19
Local network 13 (1984) 97
– 16 (1985) 163
Local optimization 24 (1988) 339
Local selective TAS flip flop 19 (1987) 319
Local terminal networks 9 (1982) 221
Local variables 21 (1987) 639
Lock transformation 25 (1989) 235
Logarithmically connected cycles 10 (1982) 325
Logic array structures 20 (1987) 173
Logic circuits 10 (1982) 101
Logic control 16 (1985) 341
Logic design 9 (1982) 93
– 10 (1982) 153
– 12 (1983) 15
– 15 (1985) 203
– 18 (1986) 631
– 23 (1988) 27
– 24 (1988) 35
Logic extraction 21 (1987) 445
Logic processing unit 16 (1985) 341
Logic programming 21 (1987) 515
Logic simulation 10 (1982) 139
– 15 (1985) 203
– 16 (1985) 335
Logic synthesis 24 (1988) 43, 125, 339
Logical machine 19 (1987) 375
– 22 (1988) 81
Logical programming language 22 (1988) 65
Logical tokens' relations 19 (1987) 385
Loop control 20 (1987) 203
LOTOS 21 (1987) 241
– 24 (1988) 483
– 25 (1989) 313
Low cost universal tester 5 (1979) 38
Low level vision 23 (1988) 135
Low speed data communications 4 (1978) 39
Lower power full custom CMOS microprocessors 18 (1986) 427
LRU 13 (1984) 153
LS algorithms 21 (1987) 137
LSI circuits 5 (1979) 31
LSI integrated circuits 25 (1989) 127
LSSD 15 (1985) 119
LTR 12 (1983) 79

M6300 software model 3(2) (1977) 76
Machine control system 21 (1987) 325
Machine description language 19 (1987) 291
Machine independence 11 (1983) 107
Machine independent 19 (1987) 291
Machine independent microprogramming 5 (1979) 219
Machine learning approach 23 (1988) 81
Machine level language 8 (1981) 229
Macintosh 21 (1987) 101
Macro assembler generator 3(4) (1977) 83
Macro interpreting 1(1) (1975) 29
Macro sequencer 25 (1989) 122
MacVEE 21 (1987) 101
Magnetic tape cassette 5 (1979) 280
Man-machine communications 9 (1982) 221

Man-machine cooperation 17 (1986) 61
Man-robot communication 9 (1982) 285
Manipulative subsystems 13 (1984) 227
Manipulators 23 (1988) 129
Manufacturing system 9 (1982) 221
– 21 (1987) 433
MAPPS 21 (1987) 251
MAP/TOP network 23 (1988) 199
Market analysis 9 (1982) 37
Markov chain models 7 (1981) 124
Markov model 8 (1981) 83
– 13 (1984) 73
Mass storage technologies 18 (1986) 505
Mass storage unit 13 (1984) 331
Massive parallelism 21 (1987) 17
– 25 (1989) 203
Massively database machine 25 (1989) 33
Massively parallel combined architecture 25 (1989) 287
Master-slave systems 22 (1988) 11
Material flow 18 (1986) 647
Matrix equations 24 (1988) 681
Matrix polynomials 22 (1988) 23
MC68000 12 (1983) 291
MCS-80 microcomputer 3(3) (1977) 22
Mead-Conway design method 10 (1982) 1, 209
Mean time between failure 7 (1981) 185
Measured redundancy 23 (1988) 247
Measurements configuration 12 (1983) 207
Medical measuring system 6 (1980) 321
Memory access time 18 (1986) 441
Memory address tracing 22 (1988) 141
Memory allocator 25 (1989) 81
Memory conflict 13 (1984) 171
Memory controller 15 (1985) 75
Memory coupled systems 19 (1987) 129
Memory interface for 2650 microprocessor 3(3) (1977) 68
Memory interference 7 (1981) 124
– 13 (1984) 73
– 19 (1987) 401
Memory management 20 (1987) 217
Memory management unit 24 (1988) 709
Merelin display 5 (1979) 44
Message channel 7 (1981) 229
Message communication 19 (1987) 219
– 22 (1988) 263
Message coupled systems 19 (1987) 129
Message handling 20 (1987) 59
– 21 (1987) 119
Message mailbox 7 (1981) 229
Message passing 18 (1986) 185
– 21 (1987) 213, 481
– 22 (1988) 233
– 23 (1988) 185, 227
– 24 (1988) 519
Message-passing hardware 24 (1988) 503
Meta assembler 12 (1983) 89
– 14 (1984) 201
Meta compiler 1(1) (1975) 35
Meta interpreter 21 (1987) 531
Meta language 4 (1978) 88
Metal oxide semiconductor (MOS) 1(1) (1975) 9
METASM 14 (1984) 201
Metrics of software 21 (1987) 371
Micro-operation 1(1) (1975) 5
– 8 (1981) 179
Microarchitecture 17 (1986) 19
– 19 (1987) 291, 305
– 23 (1988) 143
– 25 (1989) 121
Microcode 4 (1978) 67
– 7 (1981) 37
– 13 (1984) 249
– 24 (1988) 371
Microcode compaction 17 (1986) 29
– 24 (1988) 17
Microcode development 13 (1984) 105
Microcode generation 11 (1983) 277
Microcode generator 18 (1986) 263
Microcode language 8 (1981) 229
Microcode parallelism 13 (1984) 299
Microcode simulation 13 (1984) 299
Microcomputer applications 17 (1986) 129
Microcomputer applications in Japan 3(4) (1977) 40
Microcomputer architecture 1(1) (1975) 9
– 12 (1983) 33
Microcomputer bus 3(3) (1977) 29
– 21 (1987) 333
Microcomputer control applications 10 (1982) 229
Microcomputer control system 14 (1984) 187
Microcomputer course 3(2) (1977) 22
Microcomputer design 11 (1983) 177, 187
Microcomputer development 17 (1986) 159
Microcomputer diagnostic system 17 (1986) 185
Microcomputer education 17 (1986) 77
Microcomputer education in the U.S.A. 3(2) (1977) 26
Microcomputer graphics 6 (1980) 205
Microcomputer interfacing 3(2) (1977) 57
– 9 (1982) 93
Microcomputer memory analysis 17 (1986) 129
Microcomputer network 10 (1982) 25
– 22 (1988) 353

Microcomputer simulation 6 (1980) 354
Microcomputer trends 5 (1979) 344
Microcomputers in Europe 6 (1980) 72
Microcomputers in Norway 2(2) (1976) 53
Microcomputers operating systems 19 (1987) 375
Microcontrol 1(1) (1975) 21
Microcontroller 12 (1983) 43
– 18 (1986) 55, 59
Microdiagnostics 8 (1981) 237
– 9 (1982) 27
– 22 (1988) 325
Microelectronics 7 (1981) 3
– 9 (1982) 37
– 10 (1982) 71
Microelectronics and employment 7 (1981) 291
Microelectronics policies in Western Europe 8 (1981) 67
Microforms 11 (1983) 127
Microinformatics 4 (1978) 232
Microinstruction 1(1) (1975) 5
– 8 (1981) 179
– 13 (1984) 115
– 16 (1985) 17
– 19 (1987) 291
– 23 (1988) 21
Microinstruction cycle 9 (1982) 155
Microinstruction format 9 (1982) 155
Microinstruction sequencing 16 (1985) 325
Microlanguage 7 (1981) 46
MICRON 21 (1987) 101
Micropascal 5 (1979) 89
Microprocessing in Austria 3(4) (1977) 5
– 5 (1979) 113
Microprocessing in Belgium 5 (1979) 118
Microprocessing in Denmark 5 (1979) 120
Microprocessing in F.R. Germany 3(4) (1977) 19
– 5 (1979) 121
Microprocessing in France 3(4) (1977) 10
Microprocessing in Italian industry 5 (1979) 130
Microprocessing in Italy 5 (1979) 128
Microprocessing in Japan 3(4) (1977) 50
– 5 (1979) 150
Microprocessing in Portugal 3(4) (1977) 33
Microprocessing in Sweden 4 (1978) 239
– 5 (1979) 138
Microprocessing in Switzerland 5 (1979) 143
Microprocessing in the Netherlands 3(4) (1977) 25
– 5 (1979) 135
Microprocessing in the U.K. 3(4) (1977) 34
– 5 (1979) 145
Microprocessor 8080 6 (1980) 111
Microprocessor application 15 (1985) 113
– 21 (1987) 151
Microprocessor applications in Japan 5 (1979) 155
Microprocessor applications in Japanese industry 5 (1979) 166
Microprocessor architecture 22 (1988) 385
Microprocessor based data modem 15 (1985) 57
Microprocessor based design 15 (1985) 57
Microprocessor based LAN 20 (1987) 59
Microprocessor based systems 11 (1983) 127
– 17 (1986) 159
Microprocessor bus 4 (1978) 207
Microprocessor debugging 24 (1988) 149
Microprocessor development system 9 (1982) 289
Microprocessor dictionary 4 (1978) 115
Microprocessor education in Austria 3(2) (1977) 66
Microprocessor education in France 3(2) (1977) 31, 38
Microprocessor education in Israel 3(2) (1977) 51
Microprocessor education in Spain 3(2) (1977) 34
Microprocessor education in the U.K. 3(2) (1977) 46
Microprocessor education in the U.S.A. 3(2) (1977) 5
Microprocessor lab equipment 3(2) (1977) 69
Microprocessor network 24 (1988) 85
Microprocessor scorecard 1(3) (1975) 43
Microprocessor-sensor interface in robotics 11 (1983) 341
Microprocessor simulation 24 (1988) 149
Microprocessor software development system 11 (1983) 217
Microprocessor software development tool 4 (1978) 265
Microprocessor standards 1(3) (1975) 8
– 3(1) (1977) 47
Microprocessor system 17 (1986) 1
– 21 (1987) 657
Microprocessor system design 1(4) (1975) 26
Microprocessor systems testing 5 (1979) 31
Microprocessor testing 1(5) (1975) 15
– 12 (1983) 285
Microprocessor training program 3(2) (1977) 12

Microprocessor 8X300 5 (1979) 11
Microprocessors and spectroscopy experiments 14 (1984) 79
Microprocessors applications 21 (1987) 151
Microprocessors applications bibliography 3(1) (1977) 72
Microprocessors applications training 6 (1980) 359
Microprocessors architecture 21 (1987) 171
Microprocessors for chemical pathology 4 (1978) 314
Microprocessors for intensive care units 4 (1978) 310
Microprocessors in biomedical instrumentation 4 (1978) 318
Microprocessors in university electronic lab 3(2) (1977) 63
Microprogram assembler 1(4) (1975) 35
Microprogram compaction 19 (1987) 305
Microprogram control units 20 (1987) 271
Microprogram correctness 2(2) (1976) 21
Microprogram generating system 19 (1987) 305
Microprogram implementation 1(1) (1975) 29
Microprogram improvement 17 (1986) 61
Microprogram optimization 9 (1982) 161
– 16 (1985) 17
Microprogram rollback 20 (1987) 3
Microprogrammable architectures 9 (1982) 161
Microprogrammable microprocessor system 4 (1978) 92
Microprogrammed application 18 (1986) 515
Microprogrammed circuit simulation 15 (1985) 203
Microprogrammed control unit 16 (1985) 325
– 17 (1986) 205
– 20 (1987) 309
Microprogrammed controller 21 (1987) 463
– 23 (1988) 21
Microprogramming applications 15 (1985) 277
Microprogramming definition 1(1) (1975) 21
– 2(4) (1976) 48
Microprogramming education 5 (1979) 89
Microprogramming in Austria 1(2) (1975) 5
Microprogramming in Belgium 1(2) (1975) 6
Microprogramming in Denmark 1(3) (1975) 11
Microprogramming in F.R. Germany 1(4) (1975) 2
Microprogramming in France 1(2) (1975) 9
Microprogramming in Italy 1(2) (1975) 10
Microprogramming in Spain 1(4) (1975) 1
Microprogramming in Sweden 1(2) (1975) 15
– 1(3) (1975) 16
Microprogramming in the Netherlands 1(2) (1975) 12
Microprogramming in the U.K. 1(3) (1975) 14
Microprogramming language 17 (1986) 19
Microprogramming multiprocessor systems 8 (1981) 149
Microrobot 14 (1984) 177
Microsequencer 19 (1987) 305
MICROSIMPLEX 12 (1983) 217
Microsystems architecture 24 (1988) 103
MIDI 25 (1989) 65, 271
Migration functions 19 (1987) 407
MIMD 25 (1989) 79, 205
MIMD parallel processing system 4 (1978) 155
MIMD supercomputer 16 (1985) 133
– 21 (1987) 313
Mini/microcomputer applications 19 (1987) 143
Minimization of register swapping 11 (1983) 277
Minimum variance control 19 (1987) 227
MININET 5 (1979) 285
MIPS 21 (1987) 31
MIRTEX 12 (1983) 193
Mission time improvement 20 (1987) 287
MITRA 15 2(3) (1976) 9
Mixed mode simulation 10 (1982) 139
MLED 21 (1987) 585
MML language 20 (1987) 91
Model curricula 14 (1984) 255
Model of a generalized chip structure 9 (1982) 259
Model of database 25 (1989) 3
Model validation 15 (1985) 203
Modeling and analysis 17 (1986) 129
Modeling and simulation 15 (1985) 203
Modeling distributed systems 25 (1989) 239
Modeling graphic surface 25 (1989) 265
Modeling multiprocessors 25 (1989) 213
Modeling telematic services 25 (1989) 313
Modem functions 16 (1985) 169
MODEST 6 (1980) 41
MOD/R models 16 (1985) 83
Modula-2 9 (1982) 85
– 15 (1985) 47

(Modula-2) 24 (1988) 761
Modular computers 12 (1983) 199
Modular microprogramming 16 (1985) 325
Modularity 17 (1986) 285
– 21 (1987) 287, 325
Modulation 15 (1985) 57
Module estimations 23 (1988) 81
Modules 21 (1987) 515
Molecular architecture 7 (1981) 169
Monitor 4 (1978) 257
– 8 (1981) 189, 237
– 19 (1987) 129
– 22 (1988) 11
Monitor debugger 15 (1985) 75
Monitoring 14 (1984) 145
– 18 (1986) 195
Monitoring and debugging program execution 11 (1983) 203
Monitoring functions 15 (1985) 75
Monitoring tool 18 (1986) 409
Monte Carlo simulation model 13 (1984) 189
MOS circuits 21 (1987) 453
MOS descriptions 21 (1987) 453
MOS devices and processes 10 (1982) 77
MOS inverters 10 (1982) 77
MOS microprocessor 5 (1979) 100
MOS technology 10 (1982) 77
MOS transistor 10 (1982) 77
Motion control 21 (1987) 419
Motor control 23 (1988) 339
Motorola 68000 15 (1985) 47
Motorola MC68000 6 (1980) 135
MPL 4 (1978) 67
Mpu 14 (1984) 67
MUBUS DMA-interface 5 (1979) 243
Multi-image processing system 18 (1986) 525
Multibus 7 (1981) 220
– 23 (1988) 351
Multibus interprocessor protocol 18 (1986) 185
Multichannel integration 21 (1987) 153
Multichannel protocol 18 (1986) 469
Multichannel temperature controller 6 (1980) 325
Multicomputer systems 13 (1984) 11
Multilayered operating system 14 (1984) 45
Multilevel description system 25 (1989) 151
Multilevel design verification system 24 (1988) 137
Multilevel digital simulation 24 (1988) 299
Multilevel HDL 25 (1989) 183
Multilevel interpreter system 8 (1981) 237
Multilevel system software 24 (1988) 63
Multimaster serial bus 19 (1987) 153
Multimedia messaging 21 (1987) 119
Multimedia system 21 (1987) 119
Multimicro system 6 (1980) 388
– 16 (1985) 305
Multimicrocomputer 25 (1989) 347
Multimicrocomputer system 13 (1984) 325
– 16 (1985) 273
– 18 (1986) 195
Multimicroprocessor 9 (1982) 309
– 10 (1982) 19
– 13 (1984) 179
– 16 (1985) 301
– 21 (1987) 171, 287, 311
Multimicroprocessor applications 21 (1987) 171
Multimicroprocessor architecture 7 (1981) 169
– 16 (1985) 287
– 20 (1987) 113
– 21 (1987) 325
Multimicroprocessor environment 24 (1988) 581
Multimicroprocessor structure 9 (1982) 315
– 16 (1985) 267
Multimicroprocessor system 4 (1978) 326
– 7 (1981) 177, 220
– 9 (1982) 299
– 10 (1982) 11
– 13 (1984) 171
– 14 (1984) 195
– 16 (1985) 295
– 18 (1986) 401
– 23 (1988) 123
Multimicroprocessors communication 1(5) (1975) 21
Multimode compilation 13 (1984) 255
Multiobjective programming 25 (1989) 171
Multiple abstraction level 21 (1987) 585
Multiple busses network 10 (1982) 11
Multiple DSPs 25 (1989) 41
Multiple fault diagnosis 24 (1988) 731
Multiple microprocessor design 12 (1983) 237
Multiple microprocessor systems 16 (1985) 277
Multiple microprocessor/microcomputer systems 17 (1986) 267
Multiple output combinational circuits 12 (1983) 15

Multiple processor systems 20 (1987) 249
Multiple script display 16 (1985) 31
Multiple text processing 16 (1985) 31
Multiple valued logic 6 (1980) 249
Multiplex wiring 19 (1987) 1
Multiplexer 25 (1989) 159
Multiplication algorithm 24 (1988) 637
Multiplier 21 (1987) 313
– 25 (1989) 99
Multiprocessing 4 (1978) 207
– 7 (1981) 124
– 10 (1982) 333
– 25 (1989) 203
Multiprocessing system 19 (1987) 179
– 23 (1988) 221
Multiprocessor 11 (1983) 15
– 13 (1984) 73
– 18 (1986) 213, 409, 417, 597
– 19 (1987) 5, 85, 153
– 20 (1987) 301
– 21 (1987) 251, 319, 613, 623
– 22 (1988) 315
– 24 (1988) 103, 389, 503, 599
Multiprocessor architecture 9 (1982) 7
– 15 (1985) 277
– 21 (1987) 613
– 22 (1988) 301
– 24 (1988) 673
Multiprocessor bus 21 (1987) 325
Multiprocessor environment 24 (1988) 819
Multiprocessor microprogrammed system 23 (1988) 157
Multiprocessor modelling 25 (1989) 213
Multiprocessor monitor 19 (1987) 319
Multiprocessor organization 5 (1979) 65
– 6 (1980) 44
Multiprocessor real time systems 20 (1987) 85
Multiprocessor scheduling 21 (1987) 577
Multiprocessor system 7 (1981) 32
– 9 (1982) 201
– 10 (1982) 325
– 12 (1983) 267
– 13 (1984) 11, 21
– 17 (1986) 285
– 18 (1986) 205, 515
– 19 (1987) 335
– 22 (1988) 65
– 25 (1989) 221, 341
Multiprocessor system allocation 21 (1987) 339
Multiprogramming 21 (1987) 213
Multiscaler system 8 (1981) 11
Multistage interconnection networks 17 (1986) 255
Multitasking 2(4) (1976) 40
– 11 (1983) 203
– 15 (1985) 233
– 21 (1987) 629
Multitasking executive 12 (1983) 193
Multitasking kernel 21 (1987) 181
Multitasking operating system 21 (1987) 603
Multitransputer 25 (1989) 271
Multitransputer architectures 24 (1988) 281
Multitransputer net 24 (1988) 291
Multivalued decision diagrams 23 (1988) 207
Multiway branching 23 (1988) 9
Multiwindow screens 18 (1986) 539
Music experiment 24 (1988) 443
Music processing 24 (1988) 435
Music publishing 24 (1988) 549
Music technology 25 (1989) 271
Musical instrument simulator and workstation 24 (1988) 555
Mycro-1 1(3) (1975) 30

N-adjacent code 20 (1987) 271
Nanostore 25 (1989) 124
NARCIS project 2(4) (1976) 34
Natural language interface 21 (1987) 523
Natural language processing 9 (1982) 281
Natural language systems 17 (1986) 79
NEC $\mu$PD7720 14 (1984) 67
Nested transactions 25 (1989) 233
Network 21 (1987) 205
– 22 (1988) 187
Network analysis tools 25 (1989) 307
Network analyzer 21 (1987) 111
Network development tools 21 (1987) 111
Network diagnosis 14 (1984) 139
Network emulator 21 (1987) 111
Network node 23 (1988) 149
Network operating system 16 (1985) 231
– 25 (1989) 295
Network simulator 18 (1986) 145
– 21 (1987) 111
Network topology 10 (1982) 325
– 23 (1988) 253
Networking 25 (1989) 293
Networks for parallel computers 25 (1989) 289
Next address control 9 (1982) 155
NMOS and CMOS technology 10 (1982) 77
NMOS cells 18 (1986) 123

NMOS/CMOS network analysis 14 (1984) 117
Nondeterministic programming 22 (1988) 233
Nonlinear optimization 25 (1989) 178
Nonvolatile semiconductor memory 10 (1982) 129
Normalization tool 25 (1989) 375
Normalized microcomputer system 1(4) (1975) 13
NOVRAM memory 10 (1982) 129
Nuclear electronics 18 (1986) 515
Nuclear industry 7 (1981) 249
Nuclear power plants 6 (1980) 308
Nuclear radiation 8 (1981) 11
Number theory 21 (1987) 49
Numerical device models 10 (1982) 139
Numerical integration 14 (1984) 79
Numerical machine controllers 6 (1980) 288
Numerical processing operator 25 (1989) 133
Numerical processor 11 (1983) 221
Numerical stability 24 (1988) 527

Oberon 24 (1988) 3
Object oriented approach 21 (1987) 383
Object oriented architecture 11 (1983) 69
– 18 (1986) 455
Object oriented development 25 (1989) 213
Object oriented languages 19 (1987) 129
Object oriented modular architecture 12 (1983) 153
Object oriented multiprocessor system 25 (1989) 221
Object oriented program design 24 (1988) 257
Object oriented system 24 (1988) 241
Object recognition 7 (1981) 119
Observability 20 (1987) 287
OC-FP 21 (1987) 549
OCCAM 15 (1985) 21
– 21 (1987) 49, 541, 549, 629
– 22 (1988) 175
– 23 (1988) 261
– 24 (1988) 169, 205, 291, 397, 491, 673, 757
– 25 (1989) 248, 281, 381
Occam distributed communication 25 (1989) 253
Occam implementation 21 (1987) 541
Occam priority model 25 (1989) 253
Occam systems 25 (1989) 253
Off-line testing 19 (1987) 37
Office automation 18 (1986) 623
On-chip redundancy 16 (1985) 101
On-line data acquisition 11 (1983) 23
On-line monitoring 20 (1987) 249
On-line optimization 16 (1985) 319
Operating system 8 (1981) 189
– 9 (1982) 201, 215
– 17 (1986) 1, 79
– 19 (1987) 203, 233
– 20 (1987) 301
– 21 (1987) 197
– 22 (1988) 81, 125
Operating system for 8080 microcomputer 2(4) (1976) 54
Operating system interface 22 (1988) 81
Operating system service routines 20 (1987) 85
Operating system support features 19 (1987) 277
Operator declarations 7 (1981) 37
OPS 21 (1987) 251
Optical arbiter 22 (1988) 65
Optical disks 21 (1987) 119
Optical multichannel analyzer 5 (1979) 309
Optical spectroscopy 6 (1980) 316
Optimal number of processor elements 19 (1987) 401
Optimal placement 25 (1989) 177
Optimistic policy 14 (1984) 9
Optimization 13 (1984) 115
– 21 (1987) 275
– 24 (1988) 541
Optimization algorithm 18 (1986) 363
Optimization techniques 21 (1987) 531
Optimum placement 25 (1989) 163
OR-ed dataflow 19 (1987) 385
– 24 (1988) 663
Organization of information management 15 (1985) 1
Orthogonal extensions in multiprocessor systems 6 (1980) 104
Orthographic text analysis 9 (1982) 133

P4 intermediate code 6 (1980) 51
Package programs 16 (1985) 211
Packed decimal arithmetic 19 (1987) 119
Packet strategy 8 (1981) 93
Packet switched network 9 (1982) 263
Packet switching 1(4) (1975) 19
Packet transmission system 7 (1981) 417
Page fault monitor 8 (1981) 247
Paging 17 (1986) 277
Parallel adaptive algorithm 25 (1989) 108

Parallel algorithm 4 (1978) 227
– 19 (1987) 179
– 21 (1987) 137
– 22 (1988) 301
– 24 (1988) 655
Parallel approach to rule based systems 21 (1987) 507
Parallel architecture 22 (1988) 205
– 24 (1988) 197
– 25 (1989) 34, 204
Parallel arithmetic 8 (1981) 257
Parallel branch and bound algorithm 25 (1989) 301
Parallel branching 9 (1982) 161
Parallel codeblock processor 21 (1987) 319
Parallel computation 18 (1986) 307
Parallel computer 7 (1981) 20
– 10 (1982) 25, 325
– 25 (1989) 203, 287
Parallel computer architecture 24 (1988) 363
– 25 (1989) 209
Parallel computers classification 25 (1989) 203
Parallel computers evaluation 25 (1989) 203
Parallel computing 14 (1984) 155
– 24 (1988) 535
Parallel computing topology 25 (1989) 206
Parallel control 21 (1987) 603
Parallel database machine 25 (1989) 33
Parallel debugging tool 19 (1987) 401
Parallel event administration 24 (1988) 687
Parallel files 22 (1988) 243
Parallel neural network simulation 24 (1988) 621
Parallel operations 9 (1982) 67
Parallel parsing 24 (1988) 819
Parallel picture processing 11 (1983) 227
Parallel process descriptor 22 (1988) 11
Parallel processes 23 (1988) 233
Parallel processing 1(4) (1975) 19
– 10 (1982) 11
– 14 (1984) 17
– 15 (1985) 179, 191
– 16 (1985) 287
– 17 (1986) 29
– 19 (1987) 85, 129
– 21 (1987) 49, 319, 507, 629
– 22 (1988) 23, 159, 333
– 23 (1988) 239
– 24 (1988) 291, 747
– 25 (1989) 203, 327
Parallel processing constructs 23 (1988) 215
Parallel processing system 24 (1988) 511
Parallel processor 20 (1987) 107
– 21 (1987) 39, 313
– 23 (1988) 283
– 25 (1989) 335
Parallel programming 21 (1987) 171, 559
– 24 (1988) 757
Parallel programs 18 (1986) 299
Parallel RISC machine 21 (1987) 39
Parallel searching 22 (1988) 29
Parallel standard bus 11 (1983) 203
Parallel symbolic processor 23 (1988) 149
Parallel systems 21 (1987) 75
– 25 (1989) 195, 333, 341, 353
Parallel test 20 (1987) 283
Parallelism 24 (1988) 599
Parallelism extraction 23 (1988) 87
Parallelizing compiler 24 (1988) 79
Parametric surface 25 (1989) 266
Parametrization 20 (1987) 179
Paris 21 (1987) 101
Partial differential equations 20 (1987) 113
Partial evaluation 21 (1987) 531
Partially integrated I/O channels 2(3) (1976) 41
Partitioning 20 (1987) 287
Partitioning algorithms 23 (1988) 33
PASCAL 7 (1981) 260
– 15 (1985) 17, 47
– 25 (1989) 381, 387
Pascal data structure tool 25 (1989) 387
Pascal extensions 22 (1988) 233
Pascal on 8080 microcomputer 5 (1979) 363
Path tracing 21 (1987) 471
Patient management 16 (1985) 313
Pattern matching 21 (1987) 65
Pattern recognition 19 (1987) 327
– 22 (1988) 333
PC based control system 21 (1987) 153
PC based evaluation 21 (1987) 153
PC based instrumentation 21 (1987) 153
PC based multimedia messaging system 21 (1987) 119
PC-DOS 23 (1988) 191
PCB-CAD system 23 (1988) 359
PCTE 24 (1988) 161
PDP-8E minicomputer 4 (1978) 92
PDP11 instruction set 3(3) (1977) 37
PDV-bus 6 (1980) 369
PEARL 12 (1983) 79
– 25 (1989) 76
Penalty function 25 (1989) 179
Perfect shuffle machine 25 (1989) 277
Performance 25 (1989) 338

Performance analysis 15 (1985) 277
– 18 (1986) 675
– 19 (1987) 5
Performance aspects 21 (1987) 301
Performance comparison 13 (1984) 73
Performance evaluation 7 (1981) 124
– 9 (1982) 309
– 13 (1984) 73
– 17 (1986) 267
– 18 (1986) 145
– 19 (1987) 179
– 21 (1987) 347, 629
– 22 (1988) 205
– 23 (1988) 185
– 24 (1988) 197
Performance evaluation and prediction 18 (1986) 667
Performance increase 5 (1979) 65
Performance increase by adding firmware 6 (1980) 403
Performance increase by microcoding 8 (1981) 229
Performance index 17 (1986) 267
Performance measurement 16 (1985) 207
Performance of information store 17 (1986) 129
Performance studies 24 (1988) 281
Peripheral 19 (1987) 193
– 23 (1988) 129
Peripheral controller 3(1) (1977) 54
Peripheral interface adapter 2(1) (1976) 46
– 6 (1980) 175
Peripheral unit handling 2(3) (1976) 9
Permanent fault 11 (1983) 31
Persistent object manager 25 (1989) 57
Personal computer 10 (1982) 299
– 18 (1986) 89
– 20 (1987) 73
Personal computers in Italy 6 (1980) 87
Personal computers in the Netherlands 6 (1980) 90
Personal computing 6 (1980) 78
– 7 (1981) 211
– 11 (1983) 1
Personal database 12 (1983) 99
Personal supercomputer 18 (1986) 185
PET 6 (1980) 83
PET computer 9 (1982) 215
Petri net 3(1) (1977) 7
– 15 (1985) 179
– 16 (1985) 107
– 18 (1986) 299, 335
– 19 (1987) 335
– 22 (1988) 175, 217
– 23 (1988) 87, 185
– 24 (1988) 471
– 25 (1989) 341
Petri net data flow 25 (1989) 133
Petri net models 24 (1988) 197
Petri net representation 7 (1981) 241
Petri net semantics 24 (1988) 267
Petri net simulation 2(1) (1976) 38
Physical clock 21 (1987) 489
Physical methods 22 (1988) 147
Physical redundancy 20 (1987) 15
Picture coding 21 (1987) 141
Picture generation 23 (1988) 357
Picture processing 9 (1982) 67
Pipeline architecture 2(4) (1976) 40
– 24 (1988) 709
Pipeline multimicroprocessor system 6 (1980) 243
Pipeline picture processing 13 (1984) 267
Pipeline processor 15 (1985) 277
– 22 (1988) 39
Pipeline topology 25 (1989) 249
Pipelined microcontrol unit 9 (1982) 155
Pipelining 10 (1982) 333
– 25 (1989) 203
PLA 16 (1985) 61, 73
– 22 (1988) 217
– 25 (1989) 160
PLA control structures 20 (1987) 179
PLA implementation 20 (1987) 173
PLA microcontroller 14 (1984) 223
Placement for VLSI design 25 (1989) 177
Plan recognition 9 (1982) 277
Planning applications 13 (1984) 189
Plant automation 7 (1981) 169
PLAS 23 (1988) 27
Plug-in subroutines 1(3) (1975) 10
PMS notation 4 (1978) 103
Polygon angles 25 (1989) 63
Polygon boundaries 25 (1989) 62
Polygon edges 25 (1989) 61
Polymorphic torus architecture 24 (1988) 737
Polynomials 20 (1987) 107
Pool sharing 25 (1989) 298
Port 22 (1988) 187
Portability 9 (1982) 17, 201
– 21 (1987) 181
– 22 (1988) 81
Portable communication transport system 25 (1989) 323
Portable language processor 5 (1979) 89
Portable realtime kernel 21 (1987) 189
Portable software 21 (1987) 189
Position independent software 21 (1987) 189

Power applications 24 (1988) 589
Practical education 17 (1986) 77
Pre-college computer science education 14 (1984) 255
Precompiler 9 (1982) 77
– 15 (1985) 191
Predicate/transition sets 24 (1988) 267
Prediction quantisation 21 (1987) 141
Predictive coding 21 (1987) 141
Preprocessor 7 (1981) 260
– 19 (1987) 129
Primitives 22 (1988) 11
Priority arbitration 9 (1982) 299
Priority interrupt 6 (1980) 175
Probabilistic experiment 25 (1989) 197
Probabilistic modeling 7 (1981) 177
Probability based timing verification 25 (1989) 143
Probability machines 25 (1989) 197
Problem oriented architecture 15 (1985) 129
Problem oriented concurrent systems 23 (1988) 317
Problem oriented design 8 (1981) 219
Problem oriented language 9 (1982) 221
Procedural programming 21 (1987) 419
Procedural programs 25 (1989) 367
Procedure flow processor 18 (1986) 591
Process algebra 21 (1987) 541
Process communication 15 (1985) 191
– 21 (1987) 481
– 22 (1988) 11
Process control 21 (1987) 295
Process control architecture 2(4) (1976) 34
Process control computers 12 (1983) 79
Process control microcomputer system 15 (1985) 75
Process coordination 15 (1985) 191
Process description table 19 (1987) 129
Process level dataflow 19 (1987) 385
Process modeling 10 (1982) 139
Process oriented approach 9 (1982) 123
Process scheduler 7 (1981) 229
Process states 15 (1985) 191
Process structure 24 (1988) 187
Process synchronization 9 (1982) 221
– 15 (1985) 191
– 19 (1987) 319
Process synthesis 20 (1987) 25
Process technology 24 (1988) 29
Processing arrays 25 (1989) 113
Processing nodes 25 (1989) 231
Processing period 11 (1983) 31
Processor array 24 (1988) 655
Processor communications 19 (1987) 367
Processor control 23 (1988) 33
Processor interconnections 24 (1988) 197
Processor-memory communication 22 (1988) 367
Processor performance 18 (1986) 441
Processor selection 1(2) (1975) 43
Processor structure 21 (1987) 23
Producer/consumer process 22 (1988) 11
Product control 21 (1987) 361
Product terms 23 (1988) 27
Production diagnostic 25 (1989) 353
Production system 21 (1987) 251
Program behaviour 13 (1984) 153
Program code 18 (1986) 63
Program counter 21 (1987) 23
Program development 6 (1980) 41
Program development system 1(5) (1975) 40
Program loader 5 (1979) 280
Program maintenance 9 (1982) 77
Program manipulation system 17 (1986) 61
Program parallelisation 16 (1985) 227
Program profile 3(1) (1977) 27
Program storage modules 19 (1987) 407
Programmable component 9 (1982) 259
Programmable controller 2(1) (1976) 16
– 7 (1981) 160
Programmable logic array 10 (1982) 1, 163
– 12 (1983) 15
Programmed FIR 21 (1987) 463
Programming applications 21 (1987) 549
Programming computer music 25 (1989) 65
Programming environment support 13 (1984) 1
Programming environments 21 (1987) 287, 361
Programming in logic 13 (1984) 31
Programming languages 9 (1982) 77
– 17 (1986) 79
Programming methodology 15 (1985) 253
Programming paradigms 24 (1988) 535
Programming primitive 18 (1986) 185
Programming productivity 25 (1989) 247
Programming psychology 15 (1985) 253
PROLOG 21 (1987) 259, 515, 531
– 23 (1988) 143
– 25 (1989) 378
Prolog for database 25 (1989) 9
Prolog for VLSI design 25 (1989) 157
Prolog machine 18 (1986) 71
– 21 (1987) 259, 267, 275
– 24 (1988) 607
Prolog machine architecture 21 (1987) 259

Prolog microprocessor design course 3(2) (1977) 28
PROM 3(3) (1977) 68
– 10 (1982) 129
Proper operation region 12 (1983) 285
Protection from transients 15 (1985) 11
Protocol allocation 21 (1987) 205
Protocol converter 9 (1982) 263
Protocol description 24 (1988) 205
Protocol design 24 (1988) 483
Protocol development 18 (1986) 145
Prototype development 4 (1978) 333
Prototyping langauge 24 (1988) 471
Prototyping systems 2(2) (1976) 7
Public key code 18 (1986) 583
PUMPKIN 4 (1978) 67
Purchasing software 17 (1986) 185
PURGE procedure 11 (1983) 287
Push-pull product flow 21 (1987) 433

Quad tree image decomposition 11 (1983) 227
Quad tree picture decomposition 13 (1984) 267
Quantitative analysis 24 (1988) 197
Quasi-periodic signals 16 (1985) 305
Queries 25 (1989) 37
Query functions 25 (1989) 55
Query language 12 (1983) 99
– 23 (1988) 299
– 25 (1989) 12
Query processing 7 (1981) 104
Queueing applications 17 (1986) 129
– 19 (1987) 143
Queues 22 (1988) 39

Racah coefficients 24 (1988) 535
Radar data handling 4 (1978) 148
Radioactive foils 6 (1980) 311
Radioactivity level control 7 (1981) 249
Random access 10 (1982) 261
Random Access Memory (RAM) 2(1) (1976) 5
– 3(3) (1977) 68
Random errors 20 (1987) 331
Random number generation 15 (1985) 17
– 22 (1988) 147
Random testing of microprocessors 19 (1987) 37
Rapid prototyping 21 (1987) 231
– 25 (1989) 361
Rasterization 21 (1987) 57
RDBMS 25 (1989) 9
Reachability analysis 21 (1987) 393
Reactive power compensator control 6 (1980) 384
Real time 9 (1982) 267
Real time analysis 16 (1985) 305
Real time application 5 (1979) 235
– 16 (1985) 231
– 21 (1987) 189
– 24 (1988) 717
– 25 (1989) 347
Real time computing 11 (1983) 287
Real time control 6 (1980) 296
– 9 (1982) 221
– 12 (1983) 193
– 14 (1984) 187
– 25 (1989) 274
Real time control application 18 (1986) 213
Real time control operating system 21 (1987) 603
Real time control systems 19 (1987) 385
– 21 (1987) 629
Real time data acquisition system 17 (1986) 285
Real time data gathering 18 (1986) 499
Real time data processing 24 (1988) 411
Real time data rasterisation 21 (1987) 57
Real time debugging 15 (1985) 75
Real time education 25 (1989) 71
Real time education of systems engineers 25 (1989) 71
Real time emulation 23 (1988) 203
Real time environment 22 (1988) 347
Real time events 11 (1983) 203
Real time Fortran 12 (1983) 79
Real time high level debugging 18 (1986) 387
Real time image compressor 21 (1987) 49
Real time information processing 3(1) (1977) 67
Real time languages 12 (1983) 79
Real time level description 21 (1987) 569
Real time microcomputer network 18 (1986) 469
Real time monitor 7 (1981) 241
Real time multiprocessor system 25 (1989) 221
Real time multitasking kernel 21 (1987) 181
Real time operating system 9 (1982) 309
– 16 (1985) 249
– 18 (1986) 59, 657
– 21 (1987) 197, 325, 603
Real time operating system features 12 (1983) 79
Real time operation 14 (1984) 79
– 18 (1986) 401

Real time parallel image analysis 16 (1985) 267
Real time performance 19 (1987) 401
Real time PL/1 12 (1983) 79
Real time process control 4 (1978) 109
– 9 (1982) 201
Real time process control and supervision 24 (1988) 829
Real time processing 22 (1988) 1
Real time processor 4 (1978) 148
– 15 (1985) 141
Real time programming 24 (1988) 73
Real time programming language 11 (1983) 217
Real time requirements 25 (1989) 295
Real time signal processing 16 (1985) 169
Real time software 21 (1987) 181, 189, 639
Real time speech synthesis 24 (1988) 325
Real time structured analysis 24 (1988) 449
Real time system 4 (1978) 220
– 15 (1985) 277
– 17 (1986) 267
– 21 (1987) 629
– 23 (1988) 157
– 24 (1988) 257, 687
Real time system design 24 (1988) 379
Real time system transputers 25 (1989) 281
Real time systems education 24 (1988) 835
Real time transmission 23 (1988) 199
Real time transputer 25 (1989) 281
Real time tuning 15 (1985) 195
Recognition 21 (1987) 65
Recognition system 24 (1988) 425
Reconfigurable logic state analyzer 3(3) (1977) 46
Reconfigurable multicomputer system 23 (1988) 243
Reconfigurable multitransputer systems 23 (1988) 247
Reconfigurable parallel RISC 21 (1987) 39
Reconfigurable processing arrays 14 (1984) 109
Reconfigurable RISC architecture 21 (1987) 39
Reconfigurable simulator 12 (1983) 237
Reconfiguration 11 (1983) 31
– 13 (1984) 315
– 20 (1987) 331
Reconfiguration algorithm 20 (1987) 127
– 21 (1987) 83
Reconfiguration capability 25 (1989) 116
Reconfiguration of transputer networks 21 (1987) 623
Reconfiguration phases 20 (1987) 11
Reconstruction of images 21 (1987) 129
Recording data channel 25 (1989) 281
Recovery 20 (1987) 331
Recovery blocks 11 (1983) 287
Recovery capability 25 (1989) 117
Recovery mechanisms 15 (1985) 11
Recovery points 14 (1984) 55
– 21 (1987) 481
Recursive descent method 16 (1985) 221
Recursively querying 25 (1989) 12
Reduced instruction set computer (RISC) 12 (1983) 199
– 14 (1984) 133
– 16 (1985) 113
Reduction machine 24 (1988) 389
Redundancy 20 (1987) 331
Redundant graphs 17 (1986) 255
Reflector and headlights testing machine 6 (1980) 336
Refresh storage 23 (1988) 357
Refreshing 17 (1986) 277
Register allocation 11 (1983) 277
Register array processor 21 (1987) 23
Register transfer 8 (1981) 179
Register transfer level (RTL) 18 (1986) 123
Register transfer level simulation 19 (1987) 417
Register transfer operation 19 (1987) 305
Regular decomposition 16 (1985) 287
Regular interconnection topology 21 (1987) 75
– 25 (1989) 353
Regular WSI node 21 (1987) 75
Regularity factor 10 (1982) 163
Relational database 10 (1982) 255
– 12 (1983) 99
Relational database II and III 19 (1987) 101
Relational database system 24 (1988) 555
Relational model 9 (1982) 17
Relational view 10 (1982) 255
Reliability 21 (1987) 333
– 22 (1988) 325
– 23 (1988) 157
– 25 (1989) 113
Reliability assessment 6 (1980) 243
Reliability evaluation of fault tolerance mechanisms 15 (1985) 11
Reliability hardware 20 (1987) 235
Reliability in microcomputer arrays 7 (1981) 185
Reliable procedural programs 25 (1989) 367
Relocating direct linking loader 3(3) (1977) 22
Remote analysis 21 (1987) 347
Renewal theory 19 (1987) 233

Replacement algorithms 20 (1987) 197
Requirements engineering methodology 8 (1981) 167
Requirements specification 8 (1981) 167
Resident software 20 (1987) 55
Residue number system 20 (1987) 15
Resource allocation 9 (1982) 299
– 10 (1982) 11
– 11 (1983) 277
Resource binding 8 (1981) 153
Resource conflict 17 (1986) 29
Resource management 21 (1987) 189
Resource sharing 19 (1987) 143
– 23 (1988) 379
Retry 20 (1987) 331
Ring-bus 12 (1983) 267
RISC architecture 14 (1984) 1
– 17 (1986) 119
– 20 (1987) 223
– 21 (1987) 21, 39, 259
– 24 (1988) 565
RISC biprocessor architecture 24 (1988) 589
RISC for image generation 24 (1988) 581
RISC instruction set 21 (1987) 39
RISC like structure 25 (1989) 121
RISC machine 21 (1987) 39
RISC machine instruction 21 (1987) 39
RISC microprocessor 18 (1986) 427
RISC processor 24 (1988) 573
RISC Prolog machine architecture 21 (1987) 259
RISC (Reduced Instruction Set Computer) 20 (1987) 99
– 21 (1987) 23, 251, 259
– 22 (1988) 385
– 23 (1988) 179
R.M.S. meter 10 (1982) 37
RMX/80 9 (1982) 7
Robot arm control 24 (1988) 673
Robot arm trajectories 23 (1988) 129
Robot manufacturing constraints 9 (1982) 285
Robot motion 21 (1987) 419
Robot position estimator 18 (1986) 89
Robot vision systems 16 (1985) 267
Robot welders 23 (1988) 123
Robotic toys 13 (1984) 221
Robotics 21 (1987) 295, 417
– 24 (1988) 419
Robotics/artificial intelligence education 13 (1984) 227
Robots programming 14 (1984) 165
– 21 (1987) 419, 427
Robust software design 24 (1988) 793
ROM 3(3) (1977) 68
– 10 (1982) 129
Routing 21 (1987) 65
Roving monitoring 20 (1987) 249
Rule based systems 21 (1987) 507
R65XX microcomputer family 5 (1979) 7

S-100 7 (1981) 220
Safety approval 16 (1985) 207
Safety-critical applications 24 (1988) 371
Scan-execute evaluation 18 (1986) 81
Scan-line algorithm 21 (1987) 57
Scanner 22 (1988) 11
Scenario 13 (1984) 189
Scheduler 22 (1988) 11
Scheduling 24 (1988) 681
Scheduling algorithm 20 (1987) 283
Scheduling and dispatching 11 (1983) 69
Scheduling method 20 (1987) 107
Schematics of entry systems 21 (1987) 585
Screen buffers 9 (1982) 93
SECD machine 18 (1986) 81
Segmentation 21 (1987) 65
Segmentation of combinational circuits 12 (1983) 15
Selfchecking capability 24 (1988) 637
Selfdiagnosis 6 (1980) 343
Selfmodifying algorithms 15 (1985) 195
Selfpaced instruction 11 (1983) 117
Selfreconfiguring VLSI arrays 14 (1984) 99
Selfsufficiency skills 13 (1984) 221
Selfsynchronizing circuits 11 (1983) 15
Selftest 7 (1981) 177
– 23 (1988) 153
Selftest program 6 (1980) 168
Selftesting 14 (1984) 99
Selftesting interface 9 (1982) 315
Selftesting units 21 (1987) 489
Selftuning 15 (1985) 195
– 19 (1987) 227
Semantic binary database model 25 (1989) 35
Semantic gap 17 (1986) 1
Semantic implementation 18 (1986) 263
Semaphores 9 (1982) 309
Semi-automatic guided synthesis 21 (1987) 541
Semicompilation 18 (1986) 81
– 22 (1988) 379
Semiconductor technology 10 (1982) 71
Semicustom integrated circuit 18 (1986) 107
Sensor based control 21 (1987) 427
Sensors 21 (1987) 427

Sequel language 4 (1978) 18, 30
Sequential circuit test system 18 (1986) 355
Sequential circuits 22 (1988) 125
Sequential process management 6 (1980) 20
Sequential programs 23 (1988) 87
Sequential Prolog machine 21 (1987) 275
Sequential selfchecking 15 (1985) 11
Serial bus protocol 19 (1987) 153
Serial data link 21 (1987) 613
Serial interface 17 (1986) 153
Serial link 22 (1988) 187
Serial multipliers 23 (1988) 63
Serial network 21 (1987) 613
Serial process data highway station 6 (1980) 369
Serial RAM port 17 (1986) 277
Serial signal processing 20 (1987) 133
Server oriented systems 24 (1988) 179
Service costs 7 (1981) 185
Shared bus 16 (1985) 295
Shared bus multimicroprocessor system 23 (1988) 185
Shared memory 18 (1986) 195, 213
– 22 (1988) 187
Shared memory analysis 19 (1987) 143
Shift register 16 (1985) 331
Ship hydrodynamics 14 (1984) 191
Shuffle machine 25 (1989) 277
Signal acquisition 17 (1986) 139
Signal processing 6 (1980) 381
– 9 (1982) 7
– 12 (1983) 33, 143
– 14 (1984) 17
– 19 (1987) 327
– 21 (1987) 91, 161
– 22 (1988) 315
Signal processing algorithms 16 (1985) 169
Signal processor 14 (1984) 67
Signalling multiprocessors 10 (1982) 311
Signature analysis 17 (1986) 205
– 20 (1987) 309
– 23 (1988) 167
Signature analyzer 16 (1985) 203
Signetics 2652 multiprotocol communication controller 5 (1979) 16
Silicon based firmware 21 (1987) 189
Silicon based kernel 21 (1987) 189
Silicon compilation 18 (1986) 631
Silicon compiler preprocessor 20 (1987) 167
Silicon technology 17 (1986) 119
SIMD 25 (1989) 41, 205
SIMD/SPMD 21 (1987) 171
SIMPL 4 (1978) 67
Simplex algorithm 6 (1980) 237
– 16 (1985) 319
SIMUEVA 21 (1987) 471
SIMULA 15 (1985) 179
– 25 (1989) 217
Simulated annealing 16 (1985) 345
Simulation 7 (1981) 124
– 18 (1986) 417, 667
– 21 (1987) 531
– 22 (1988) 147, 175, 205
– 24 (1988) 205, 681
Simulation environment 18 (1986) 39
Simulation language 12 (1983) 245
Simulation method 25 (1989) 209
Simulation model 25 (1989) 209
Simulation results evaluation 24 (1988) 461
Simulation techniques 11 (1983) 207
Simulation theory 15 (1985) 203
Simulation tool 1(5) (1975) 21
Simulation tool for WAN 25 (1989) 327
Simulation using Petri nets 25 (1989) 341
Simulator 1(5) (1975) 25
– 18 (1986) 27
Simultaneous microdiagnostic design 9 (1982) 27
Simultaneous vs concurrent execution 9 (1982) 27
Single Board Computer (SBC) 11 (1983) 31
Single chip design 21 (1987) 57
Single chip microcomputer 11 (1983) 177
– 15 (1985) 75
– 19 (1987) 1
– 24 (1988) 783
Single chip microprocessor 20 (1987) 69
Single process multitasking system 24 (1988) 3
Skewing 25 (1989) 283
Slide images 10 (1982) 261
Slide projector 10 (1982) 261
Slow frequency hopping 16 (1985) 139
Small scale DBMS 23 (1988) 311
Small scale systems 23 (1988) 299
Small systems 21 (1987) 347
Smalltalk 21 (1987) 623
SMS 201 parallel processor 5 (1979) 48, 53
Social impact of technology 8 (1981) 67
Social science computing 17 (1986) 195
Socio-economic impacts of microelectronics 7 (1981) 291
Software complexity 18 (1986) 319
Software controlled architecture 21 (1987) 39
Software controlled database 21 (1987) 383

Software controlled environment 21 (1987) 285, 359, 361
Software controlled support 21 (1987) 361
Software crisis 13 (1984) 1
Software design 1(2) (1975) 43
Software development 4 (1978) 257
– 24 (1988) 179
Software development environment 21 (1987) 383
Software engineering 3(2) (1977) 16
– 8 (1981) 203
– 10 (1982) 33
– 21 (1987) 285, 359, 371, 541
– 23 (1988) 305
– 25 (1989) 359
Software engineering environment 24 (1988) 161
Software fault tolerance 22 (1988) 175
– 23 (1988) 261
Software generator 25 (1989) 367
Software kernel 21 (1987) 189, 213
Software life cycle 18 (1986) 319
– 21 (1987) 371
Software maintenance 18 (1986) 319
Software management 21 (1987) 361
Software metrics 18 (1986) 605
– 21 (1987) 371
Software pipeling 22 (1988) 39
Software production 9 (1982) 319
Software quality 21 (1987) 371
Software reconfiguration 8 (1981) 83
Software robustness 18 (1986) 63
Software structure validation 25 (1989) 361
Software tools 8 (1981) 229
– 9 (1982) 77
– 10 (1982) 319
– 21 (1987) 295
Software training environment 24 (1988) 761
Software updating 9 (1982) 319
Software verification 16 (1985) 207
Sonar system 21 (1987) 161
Sorting processor 23 (1988) 273
Sound generation 25 (1989) 273
Source scanner control 6 (1980) 311
Space positioning 23 (1988) 339
Space redundancy 21 (1987) 83
Spacedraft computer 18 (1986) 63
Spanning tree 22 (1988) 263
Sparse matrix techniques 24 (1988) 621
Special applications firmware 4 (1978) 293
– 5 (1979) 338
Special computer architecture 12 (1983) 199
– 14 (1984) 133
Special education 13 (1984) 221
Special microcomputer architectures 11 (1983) 187
Special purpose processing units 23 (1988) 227
Special purpose VLSI chip 10 (1982) 1
Specialized hardware 10 (1982) 255
Specification 18 (1986) 99
– 21 (1987) 393
Specification and verification 24 (1988) 371
Specification of digital systems 25 (1989) 151
Speech processing 24 (1988) 397
Speech recognition 5 (1979) 40
Speech synthesis 5 (1979) 40
– 22 (1988) 1
Speech transmission 12 (1983) 231
Speed factor 10 (1982) 163
Speed problems 7 (1981) 110
SPH graph 20 (1987) 183
Spline based evaluation technique 21 (1987) 153
Stack editor 13 (1984) 255
Stack languages 19 (1987) 367
Stack machine 1(2) (1975) 45
Stand alone digital controllers 10 (1982) 229
Stand alone fast editor (SAFE) 8 (1981) 103
Stand alone meter 7 (1981) 249
Stand alone systems 13 (1984) 291
Standard bus 2(2) (1976) 11
Standard tools 21 (1987) 523
Standardization 21 (1987) 333
Stanford MIPS 21 (1987) 31
State machine controllers 16 (1985) 73
State machine support 18 (1986) 59
State space modelling 24 (1988) 793
Static and dynamic links 24 (1988) 281
Static board tester system 5 (1979) 315
Static RAM 10 (1982) 119
Static redundancy 11 (1983) 31
Station implementation 5 (1979) 285
Statistical tests 15 (1985) 17
– 22 (1988) 147
Still pictures 21 (1987) 141
Stochastic force sensing 24 (1988) 419
Stopped flow apparatus 11 (1983) 301
Storage management 1(1) (1974) 45
Storage structure 6 (1980) 44
Store pictures 21 (1987) 405
Strategic IS planning 15 (1985) 1

Strategical level 24 (1988) 681
Stream oriented computation 14 (1984) 17
String functions 8 (1981) 260
String processor architecture 24 (1988) 747
Stripping columns control 5 (1979) 270
Structural descriptions 23 (1988) 15
Structural RT description 21 (1987) 569
Structure attribute 18 (1986) 233
Structure validation 25 (1989) 361
Structured approach to expert system development 25 (1989) 27
Structured design 8 (1981) 203
– 15 (1985) 119, 141
– 17 (1986) 139
– 19 (1987) 153
Structured design methodology 21 (1987) 463
Structured electronic design 7 (1981) 160
Structured information management 4 (1978) 275
Structured multicomputer system 7 (1981) 185
Structured multimicroprocessor system 2(2) (1976) 56
Structured programming 7 (1981) 260
– 13 (1984) 31
Structured programming environments 21 (1987) 295
Structured systems 8 (1981) 93
Structures for parallel computers 25 (1989) 203
Structuring a distributed program 21 (1987) 287
Structuring distributed architectures 21 (1987) 287
Subprocess levels 8 (1981) 93
SUILVE language 5 (1979) 219
Supercomputer 21 (1987) 559, 665
– 25 (1989) 203
Supercomputer accelerator for PC 25 (1989) 51
Supercomputer architecture 25 (1989) 287
Supercomputing 24 (1988) 17
Supermatrix 16 (1985) 133
Supermicroprocessor 17 (1986) 1
Supervisory and cluster kernels 19 (1987) 129
Support environment 23 (1988) 317
Survey of current work in microprogramming 1(4) (1975) 8
Survey of microcomputers 1(1) (1975) 9
SW development environment 21 (1987) 361
SW development support 21 (1987) 361, 383
Switch graphs 21 (1987) 445
Switch level MOS descriptions 21 (1987) 453
Switch organization 14 (1984) 99
Switching circuit theory 12 (1983) 15
Switching elements 23 (1988) 243
Switching system architecture 15 (1985) 113
SYGARE programming 7 (1981) 13
Symbolic computation 11 (1983) 107
Symbolic processing 9 (1982) 143
– 24 (1988) 599
Synchronization 2(2) (1976) 48
– 15 (1985) 179
– 22 (1988) 11
– 24 (1988) 187
Synchronization mechanism 23 (1988) 233
Synchronous-asynchronous communication 8 (1981) 93
Synchronous bus 19 (1987) 5
Synchronous data links 5 (1979) 16
Synchronous machines 10 (1982) 11
Synchronous multiprocessor interconnection network 18 (1986) 463
Synchronous process 11 (1983) 31
Synchronous process communication 21 (1987) 481
Syntactic analysis 9 (1982) 281
Syntax 7 (1981) 37
Syntax directed retrieval 21 (1987) 383
Syntax directed storage 21 (1987) 383
Synthesis algorithm 18 (1986) 347
Synthesis of processor structure 18 (1986) 251
System analysis 21 (1987) 331
System architecture 5 (1979) 190
– 15 (1985) 277
System behavior 1(3) (1975) 17
– 24 (1988) 227
System control 21 (1987) 405
System controller 23 (1988) 123
System design 1(2) (1975) 43
– 4 (1978) 4
System design methodology 22 (1988) 205
System design VLSI 21 (1987) 665
System development 18 (1986) 223
System diagnosability 21 (1987) 489, 497
System diagnosis 19 (1987) 327
– 20 (1987) 283
System efficiency 13 (1984) 171
System element (SE) 7 (1981) 229
System evaluation 14 (1984) 67
– 25 (1989) 353
System faults diagnosability 24 (1988) 723
System level design 24 (1988) 449

System level diagnosis 20 (1987) 323
System level fault diagnosis 21 (1987) 497
System level language 24 (1988) 57
System logic 24 (1988) 29
System model 23 (1988) 157
System modelling and analysis 19 (1987) 143
– 24 (1988) 51, 809
System performance 21 (1987) 331
– 25 (1989) 338
System procedures 11 (1983) 287
System programming 24 (1988) 57
System reliability 23 (1988) 247
System restoration 14 (1984) 55
System service 23 (1988) 279
System shell 21 (1987) 523
System structure 24 (1988) 227
System structuring 24 (1988) 187
System testing 13 (1984) 315
System theory 15 (1985) 203
System timing 24 (1988) 347
Systems programming teaching 10 (1982) 33
Systems with dead-time 11 (1983) 23
Systemwide RT OS 21 (1987) 325
Systolic architecture 21 (1987) 83
– 22 (1988) 115
Systolic array 20 (1987) 121, 133
– 23 (1988) 63
– 24 (1988) 527
– 25 (1989) 77, 107
Systolic dataflow machine 25 (1989) 91
Systolic processor 21 (1987) 137
– 25 (1989) 77
Systolic processors and arrays 25 (1989) 77
Systolic processors high level languages 25 (1989) 79

t-fault diagnosis 16 (1985) 7
Table oriented database 25 (1989) 9
Table processing 6 (1980) 149
Tactile data 24 (1988) 425
Tagged architecture 16 (1985) 127
– 20 (1987) 99
Tape skew 25 (1989) 283
Target machine independence 17 (1986) 19
Target processor independent test system 11 (1983) 217
Task allocation 19 (1987) 335
Task management 7 (1981) 241
Task scheduling 23 (1988) 283
Task synchronization 12 (1983) 193
Task to task communication 23 (1988) 379
Tasking 21 (1987) 301
Teacher education 14 (1984) 255
Technological innovation 8 (1981) 67
Technological literacy 13 (1984) 227
Technological society 8 (1981) 67
Technology assessment 8 (1981) 67
Telecommunication systems 6 (1980) 11
Telegraph channel 16 (1985) 139
Telematic services 25 (1989) 313
Telephone metering system 23 (1988) 345
Teletext 15 (1985) 263
Telex networks 6 (1980) 37
Telex networks problems 6 (1980) 34
Term minimization 12 (1983) 15
Term notations 13 (1984) 31
Terminal concentrator 11 (1983) 295
Terminal performance 19 (1987) 143
Termination detection 22 (1988) 263
Test design 18 (1986) 99
Test generator 18 (1986) 371
Test graph 20 (1987) 283
Test length 19 (1987) 37
Test pattern generation 24 (1988) 233
Test pattern generator 16 (1985) 203
Test patterns 9 (1982) 27
Testability 21 (1987) 371
– 24 (1988) 275
– 25 (1989) 192
Testability conditions 25 (1989) 85
Testability measures 25 (1989) 309
Testability strategies 21 (1987) 371
Testbed 19 (1987) 49
Testing 20 (1987) 323
– 21 (1987) 371
– 22 (1988) 277
– 25 (1989) 119
Testing and diagnosis 20 (1987) 235
Text and graphics integration 24 (1988) 455
Text editor 8 (1981) 103
Text formatter 11 (1983) 233
Text processing 23 (1988) 103
Text readers 23 (1988) 103
Text to speech conversion 22 (1988) 1
Text understanding 9 (1982) 277
Text visualization 9 (1982) 133
Theoretical education 17 (1986) 77
Thermal energy meter 6 (1980) 340
Thermonuclear fusion research 17 (1986) 285
Thinning algorithms 23 (1988) 115
Threaded code 21 (1987) 593
Three-dimensional computer 2(2) (1976) 38
– 4 (1978) 227
Throughput 18 (1986) 667
Throughput evaluation 11 (1983) 207
Throughput simulation 16 (1985) 227

Tightly coupled computers 19 (1987) 129
Tightly coupled devices 22 (1988) 11
Tightly coupled multiprocessor systems 20 (1987) 11
Time critical dedicated microprocessing 12 (1983) 33, 143
Time critical software 18 (1986) 383
Time shared bus 13 (1984) 171
Time stamping 25 (1989) 284
Timing analysis 21 (1987) 471
Timing characteristics 5 (1979) 206
Timing correction 24 (1988) 43
Timing correction algorithm 24 (1988) 125
Timing simulation 10 (1982) 139
Timing verification 24 (1988) 125
– 25 (1989) 143
TMR 11 (1983) 31
– 20 (1987) 287
TMR-Duplex-Simplex 11 (1983) 31
TMS 5501 3(1) (1977) 54
Toeplitz systems 21 (1987) 137
Tool for WAN 25 (1989) 327
Tool integration CAD 21 (1987) 585
Tool interfacing 18 (1986) 233
Toolbox 21 (1987) 361
Tools 21 (1987) 361
Tools for AI 18 (1986) 71
Tools for VLS design 21 (1987) 637
Tools for VLSI design 21 (1987) 567
TOP 23 (1988) 199
Top-down decomposition 16 (1985) 83
Top-down design 10 (1982) 153
Topology 24 (1988) 275
Totally selfchecking checkers 20 (1987) 271
TP microcomputers 20 (1987) 227
Trace driven simulation 22 (1988) 141
Trace file 3(1) (1977) 27
Tracing local variables 21 (1987) 639
Traffic controller 1(5) (1975) 30
Traffic investigation device 6 (1980) 304
Transaction processing 14 (1984) 9
– 20 (1987) 79
Transaction type language 9 (1982) 221
Transcendental instructions 11 (1983) 221
Transducer 10 (1982) 341
Transformation algorithm 21 (1987) 569
Transformation of processor structures 21 (1987) 549
Transformation of software into hardware 22 (1988) 125
Transformation rules 24 (1988) 397
Transformation techniques 21 (1987) 531
Transformational synthesis 21 (1987) 541
Transient fault 11 (1983) 31
– 20 (1987) 3
Transient fault tolerance 24 (1988) 775
Transistor models for VLSI 10 (1982) 77
Translation lookaside buffer 24 (1988) 709
Transmission of still pictures 21 (1987) 141
Transmission system 21 (1987) 241
Transparence 21 (1987) 205
Transparent DMA 17 (1986) 277
Transparent multiprocessing 11 (1983) 69
Transportation system 18 (1986) 647
Transputer 19 (1987) 367
– 21 (1987) 49, 539, 623, 629
– 23 (1988) 149, 253
– 24 (1988) 275, 397, 403, 535, 607, 673, 757
– 25 (1989) 245, 265, 277
Transputer applications 21 (1987) 49
Transputer array 25 (1989) 259
Transputer based accelerator 24 (1988) 299
Transputer net 18 (1986) 185
Transputer network 21 (1987) 559
– 24 (1988) 73, 169, 389
– 25 (1989) 247
Transputers case study 25 (1989) 247
Tree directory 23 (1988) 191
Tree-like multiprocessors 19 (1987) 129
Tree-structured architecture 12 (1983) 153
Tree topology 25 (1989) 248
Triangular system equations 25 (1989) 259
True colour images 19 (1987) 19
TUMULT 21 (1987) 613
Tuning 8 (1981) 189
– 14 (1984) 145
Tuning architecture 8 (1981) 219
Two-dimensional array 25 (1989) 85
Two-dimensional arrays testability 25 (1989) 85
Two-dimensional diagnostic 25 (1989) 353
Two-dimensional graph layout 21 (1987) 577
Two-dimensional matched filter 22 (1988) 333
Two-rail code 20 (1987) 271
Two-way communication 10 (1982) 299
Two-way instruction prefetching 19 (1987) 407

Ultrasonic ranging system 14 (1984) 177
Unidata 7.720 I/O handling 2(3) (1976) 15
Unidirectional errors 20 (1987) 331
Unification 23 (1988) 143
Unilateral array 25 (1989) 86
Universal 8002 microprocessing

microprocessing development aid (MDA) system 5 (1979) 25
Universal AHPL 22 (1988) 97
Universal commutation 5 (1979) 73
Universal program method 4 (1978) 87
Universal real time kernel 14 (1984) 161
UNIX 21 (1987) 205
– 24 (1988) 761
– 25 (1989) 361
UNIX* 18 (1986) 233
UNIX III 21 (1987) 347
UNIX microcomputer 20 (1987) 47
UNIX multiprocessor system 20 (1987) 79
UNIX network applications 25 (1989) 323
Unlimited vocabulary 24 (1988) 325
Upward/downward compatibility 5 (1979) 7
Usage set 13 (1984) 115
Use rating 17 (1986) 185
Use satisfaction 17 (1986) 185
User-computer interface 17 (1986) 79
User interface 7 (1981) 249
– 8 (1981) 103
– 20 (1987) 73
User interface in CAD 21 (1987) 585
User level microprograms 4 (1978) 82
User microprogramming 8 (1981) 179
User's needs 7 (1981) 211
Uses of computers instruction 14 (1984) 255
UTC common bus multiprocessor 19 (1987) 319
UTC common bus system 19 (1987) 401
Utility programs 9 (1982) 77

Vacuum fluorescent display 21 (1987) 413
Validation 8 (1981) 167
– 21 (1987) 393, 445
Validation and verification 24 (1988) 809
VAMPIX 21 (1987) 325
Variable character generation 16 (1985) 31
Variable character size 9 (1982) 133
VARIAN V73 3(3) (1977) 37
Vector processing 24 (1988) 743
Vectored interrupt 6 (1980) 175
Vectorized superminicomputer 24 (1988) 743
Vehicle operators 22 (1988) 151
Verification 17 (1986) 61
– 21 (1987) 223, 393, 445
– 23 (1988) 3
– 24 (1988) 137
Verification of hardware 21 (1987) 453
Vertical microprogramming 17 (1986) 19
Vertical migration 8 (1981) 189, 203, 219, 237, 247, 257
– 9 (1982) 143
– 12 (1983) 185
– 14 (1984) 1
– 15 (1985) 129
Video bus 25 (1989) 42
Video processors 15 (1985) 141
Videodiscs 14 (1984) 267
Videogames 4 (1978) 44
Videotex 6 (1980) 202
Virtual addressing 22 (1988) 141
Virtual machine 8 (1981) 229
– 19 (1987) 375
– 20 (1987) 209
– 22 (1988) 81
Virtual memory 8 (1981) 247
Virtual memory management 5 (1979) 235
Virtual processor 16 (1985) 221
– 21 (1987) 189
Virtual token passing 18 (1986) 161
Vision sensor 16 (1985) 331
VLSI 12 (1983) 33
– 13 (1984) 153
– 16 (1985) 73
– 18 (1986) 99, 123, 153, 195, 275, 335, 363, 417, 435, 445, 567
– 20 (1987) 85
– 21 (1987) 73, 91, 251, 267, 639
– 22 (1988) 39, 115, 315, 353
– 23 (1988) 37, 75, 81, 273, 351
VLSI 32-bit microprocessor 20 (1987) 223
VLSI 370 microprocessor 24 (1988) 23, 29, 35, 43, 125, 131, 137
VLSI and fault tolerance 21 (1987) 73
VLSI applications 23 (1988) 87
VLSI architecture 11 (1983) 177
– 18 (1986) 591
VLSI CAD tools 24 (1988) 455
VLSI circuits 18 (1986) 427
VLSI communicating circuit 18 (1986) 173
VLSI design 10 (1982) 1, 71, 209
– 14 (1984) 125
– 16 (1985) 67
– 18 (1986) 27, 341, 355
– 20 (1987) 179
– 21 (1987) 567, 585
– 24 (1988) 767
– 25 (1989) 157, 171
VLSI design database 21 (1987) 523, 585
VLSI design environment 21 (1987) 585

VLSI design integration 21 (1987) 523
VLSI design methodology 24 (1988) 371
VLSI design techniques 12 (1983) 15
VLSI devices 16 (1985) 101
VLSI heuristics 20 (1987) 189
VLSI implementation 21 (1987) 129
– 24 (1988) 371
VLSI layout design 25 (1989) 177
VLSI layout placement 25 (1989) 163, 177
VLSI layout processing 21 (1987) 65
VLSI modules 23 (1988) 51
VLSI multiplier 20 (1987) 153
VLSI multiprocessor 21 (1987) 251
VLSI Prolog interpreter 21 (1987) 267
VLSI serial multiplier 24 (1988) 637
VLSI system design 21 (1987) 665
VLSI systolic architecture 24 (1988) 647
VLSI technology 17 (1986) 79
VLSI/WSI array processors 20 (1987) 127
VME based module 23 (1988) 221
VME bus 16 (1985) 273
– 18 (1986) 289
– 20 (1987) 65
– 21 (1987) 101
– 25 (1989) 41
Voice announcement 22 (1988) 151
Voice service 25 (1989) 314
Voice signal processing 8 (1981) 15
VSLI design techniques 16 (1985) 143

W-search trees 22 (1988) 29
Wafer scale integration 21 (1987) 75, 83
– 25 (1989) 353
WAN 25 (1989) 327
Warren's instruction set 18 (1986) 71
Waste water treatment 21 (1987) 603
Wavefront algorithm 25 (1989) 260
Weighing multiplexing 16 (1985) 261
Welding machine 6 (1980) 296
WHT 25 (1989) 341
Wide area network 20 (1987) 47
Window overlapping 18 (1986) 539
Wisdom 24 (1988) 169
Word compaction algorithm 24 (1988) 21
Word control strategy 8 (1981) 93
Word minimization 23 (1988) 159
Word reduction 9 (1982) 161
Work table 7 (1981) 229
Worker productivity 11 (1983) 1
Workload analysis 21 (1987) 347
Workload generator 19 (1987) 675
Workstation 14 (1984) 267
– 18 (1986) 195
– 24 (1988) 3, 11
WP-calculus 25 (1989) 491
Writable control store 8 (1981) 237
WSI implementation 20 (1987) 121
WSI node 21 (1987) 75

X.25 network 9 (1982) 263
X.25 protocol 21 (1987) 55
– 25 (1989) 307
XMDS approach 21 (1987) 287

Yield enhancement 25 (1989) 113

Zero delay elements 17 (1986) 335
Zilog Z8000 6 (1980) 135